

DHU11

DHU-11 FUNC TST PART 1
CZDHUBO

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The image shows a microfiche card with a grid of 14 columns and 24 rows of frames. Each frame contains a small, illegible image or text. The frames are arranged in a regular grid pattern. There are vertical bars on the right side of the card, likely for identification or tracking.

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IDENTIFICATION

PRODUCT CODE: AC-T794B-MC
PRODUCT NAME: CZDHUBO DHU-11 FUNC TST PART1
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***** MODIFICATION HISTORY *****

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TWO NEW TESTS WERE INCLUDED IN THIS PART:

- TEST 15 - CSR BIT 4 TEST.
- TEST 25 - DIAGNOSTIC FIELD (BMP) TEST.

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1.0 GENERAL PROGRAM CONSIDERATIONS

1.1 PROGRAM ABSTRACT

CZDHUBO IS PART ONE OF THE DHU FUNCTIONAL VERIFICATION TEST. THIS PART OF THE TEST VERIFIES THE RESET, SELFTEST, REGISTER ACCESS, BMP CODE, AND INTERRUPT FUNCTIONS OF THE BOARD ARE FUNCTIONING CORRECTLY.

THIS DIAGNOSTIC HAS BEEN WRITTEN FOR USE WITH THE DIAGNOSTIC RUNTIME SERVICES SOFTWARE (SUPERVISOR). THESE SERVICES PROVIDE THE INTERFACE TO THE OPERATOR AND TO THE SOFTWARE ENVIRONMENT. THIS PROGRAM CAN BE USED WITH XXDP+, ACT, APT, SLIDE AND PAPER TAPE. FOR A COMPLETE DESCRIPTION OF THE RUNTIME SERVICES, REFER TO THE XXDP+ USER'S MANUAL. THERE IS A BRIEF DESCRIPTION OF THE RUNTIME SERVICES IN THE OPERATING INSTRUCTIONS-COMMANDS OF THIS DOCUMENT.

1.2 SYSTEM REQUIREMENTS

THE FOLLOWING HARDWARE IS REQUIRED TO RUN THE DHU11 FVT:

- 0 UNIBUS PROCESSOR WITH AT LEAST 32K BYTES OF MEMORY.
- 0 DHU BOARDS INSTALLED ON THE UNIBUS.
- 0 APPROPRIATE PROGRAM LOAD DEVICE SUPPORTING XXDP+ MEDIA OR A DOWN LINE LOADING SYSTEM.

1.3 RELATED DOCUMENTS AND STANDARDS

- 0 XXDP+ USER'S MANUAL - DESCRIBES THE RUNNING OF DIAGNOSTICS UNDER THE XXDP+ MONITOR.

1.4 DIAGNOSTIC HIERARCY PREREQUISITES

THE PROCESSOR, THE UNIBUS, THE SYSTEM MEMORY, THE CONSOLE TERMINAL AND THE LOAD MEDIA ARE ASSUMED TO HAVE BEEN TESTED AND FOUND WORKING BEFORE THIS PROGRAM IS RUN.

2.0 OPERATING INSTRUCTIONS

THIS SECTION CONTAINS A BRIEF DESCRIPTION OF THE RUNTIME SERVICES. FOR DETAILED INFORMATION, REFER TO THE XXDP+ USER'S MANUAL (CHQUS).

2.1 COMMANDS

THERE ARE ELEVEN LEGAL COMMANDS FOR THE DIAGNOSTIC RUNTIME SERVICES (SUPERVISOR). THIS SECTION LISTS THE COMMANDS AND GIVES A VERY BRIEF DESCRIPTION OF THEM. THE XXDP+ USER'S MANUAL HAS MORE DETAILS.

COMMAND	EFFECT
START	START THE DIAGNOSTIC FROM AN INITIAL STATE
RESTART	START THE DIAGNOSTIC WITHOUT INITIALIZING
CONTINUE	CONTINUE AT TEST THAT WAS INTERRUPTED (AFTER +C)
PROCEED	CONTINUE FROM AN ERROR HALT
EXIT	RETURN TO XXDP+ MONITOR (XXDP+ OPERATION ONLY!)
ADD	ACTIVATE A UNIT FOR TESTING (ALL UNITS ARE CONSIDERED TO BE ACTIVE AT START TIME)
DROP	DEACTIVATE A UNIT
PRINT	PRINT STATISTICAL INFORMATION (IF IMPLEMENTED BY THE DIAGNOSTIC - SEE PERFORMANCE AND PROGRESS REPORTS SECTION OF THIS DOCUMENT)
DISPLAY	TYPE A LIST OF ALL DEVICE INFORMATION
FLAGS	TYPE THE STATE OF ALL FLAGS (SEE FLAGS SECTION)
ZFLAGS	CLEAR ALL FLAGS (SEE FLAGS SECTION)

A COMMAND CAN BE RECOGNIZED BY THE FIRST THREE CHARACTERS. SO YOU MAY, FOR EXAMPLE, TYPE "STA" INSTEAD OF "START". MORE INFORMATION CAN BE FOUND WITHIN THE SECTION LABELLED EXTENDED COMMAND SYNTAX

2.2 SWITCHES

THERE ARE SEVERAL SWITCHES WHICH ARE USED TO MODIFY SUPERVISOR OPERATION. THESE SWITCHES ARE APPENDED TO THE LEGAL COMMANDS. ALL OF THE LEGAL SWITCHES ARE TABULATED BELOW WITH A BRIEF DESCRIPTION OF EACH. IN THE DESCRIPTIONS BELOW, A DECIMAL NUMBER IS DESIGNATED BY "DDDD".

SWITCH	EFFECT
/TESTS:LIST	EXECUTE ONLY THOSE TESTS SPECIFIED IN THE LIST. LIST IS A STRING OF TEST NUMBERS, FOR EXAMPLE - /TESTS:1:5:7-10. THIS LIST WILL CAUSE TESTS 1,5,7,8,9,10 TO BE RUN. ALL OTHER TESTS WILL NOT BE RUN.
/PASS:DDDD	EXECUTE DDDDD PASSES (DDDD = 1 TO 64000)
/FLAGS:FLGS	SET SPECIFIED FLAGS. SEE THE FLAGS SECTION OF THIS DOCUMENT.
/EOP:DDDD	REPORT END OF PASS MESSAGE AFTER EVERY DDDDD PASSES ONLY. (DDDD = 1 TO 64000)
/UNITS:LIST	TEST/ADD/DROP ONLY THOSE UNITS SPECIFIED IN THE LIST. LIST EXAMPLE - /UNITS:0:5:10-12 USE UNITS 0,5,10,11,12 (UNIT NUMBERS = 0-63)

EXAMPLE OF SWITCH USAGE:

START/TESTS:1-5/PASS:1000/EOP:100

THE EFFECT OF THIS COMMAND WILL BE: 1) TESTS 1 THROUGH 5 WILL BE EXECUTED, 2) ALL UNITS WILL TESTED 1000 TIMES AND 3) THE END OF PASS MESSAGES WILL BE PRINTED AFTER EACH 100 PASSES ONLY. A SWITCH CAN BE RECOGNIZED BY THE FIRST THREE CHARACTERS. YOU MAY, FOR EXAMPLE, TYPE "/TES:1-5" INSTEAD OF "/TESTS:1-5".

BELOW IS A TABLE THAT SPECIFIES WHICH SWITCHES CAN BE USED BY EACH COMMAND.

	TESTS	PASS	FLAGS	EOP	UNITS
START	X	X	X	X	X
RESTART	X	X	X	X	X
CONTINUE		X	X	X	
PROCEED			X		
DROP					X
ADD					X
PRINT					
DISPLAY					X
FLAGS					
ZFLAGS					
EXIT					

2.3 FLAGS

FLAGS ARE USED TO SET UP CERTAIN OPERATIONAL PARAMETERS SUCH AS LOOPING ON ERROR. ALL FLAGS ARE CLEARED AT STARTUP AND REMAIN CLEARED UNTIL EXPLICITLY SET USING THE FLAGS SWITCH. FLAGS ARE ALSO CLEARED AFTER A START COMMAND UNLESS SET USING THE FLAG SWITCH. THE ZFLAGS COMMAND MAY ALSO BE USED TO CLEAR ALL FLAGS. WITH THE EXCEPTION OF THE START AND ZFLAGS COMMANDS, NO COMMANDS AFFECT THE STATE OF THE FLAGS; THEY REMAIN SET OR CLEARED AS SPECIFIED BY THE LAST FLAG SWITCH.

FLAG	EFFECT
-----	-----
HOE	HALT ON ERROR - CONTROL IS RETURNED TO RUNTIME SERVICES COMMAND MODE
LOE	LOOP ON ERROR
IER*	INHIBIT ALL ERROR REPORTS
IBR*	INHIBIT ALL ERROR REPORTS EXCEPT FIRST LEVEL (FIRST LEVEL CONTAINS ERROR TYPE, NUMBER, PC, TEST AND UNIT)
IXR*	INHIBIT EXTENDED ERROR REPORTS (THOSE CALLED BY PRINTX MACRO'S)
PRI	DIRECT MESSAGES TO LINE PRINTER
PNT	PRINT TEST NUMBER AS TEST EXECUTES
BOE	"BELL" ON ERROR
UAM	UNATTENDED MODE (NO MANUAL INTERVENTION)
ISR	INHIBIT STATISTICAL REPORTS (DOES NOT APPLY TO DIAGNOSTICS WHICH DO NOT SUPPORT STATISTICAL REPORTING)
IDR	INHIBIT PROGRAM DROPPING OF UNITS
ADR	EXECUTE AUTODROP CODE
LOT	LOOP ON TEST
EVL	EXECUTE EVALUATION (ON DIAGNOSTICS WHICH HAVE EVALUATION SUPPORT)

*SEE THE ERROR INFORMATION SECTION OF THIS DOCUMENT.

SEE THE XXDP* USER'S MANUAL FOR MORE DETAILS ON FLAGS. YOU MAY SPECIFY MORE THAN ONE FLAG WITH THE FLAG SWITCH. FOR EXAMPLE, TO CAUSE THE PROGRAM TO LOOP ON ERROR, INHIBIT ERROR REPORTS AND TYPE A "BELL" ON ERROR, YOU MAY USE THE FOLLOWING STRING:

/FLAGS:LOE:IER:BOE

2.4 EXTENDED COMMAND SYNTAX

2.4.1 START COMMAND -

```
*****  
STA(RT)/TESTS:<TEST-LIST>/PASS:<PASS-CNT>/FLAGS:  
<FLAG-LIST>/EOP:<INCR>  
*****
```

2.4.1.1 TESTS SWITCH (/TESTS:<TEST-LIST>) -

<TEST-LIST> IS A SEQUENCE OF DECIMAL NUMBERS (1:2 ETC.) OR RANGES OF DECIMAL NUMBERS (1-5:8-10 ETC.), SEPERATED BY COLONS, THAT SPECIFY THE TESTS TO BE EXECUTED. TESTS WILL BE EXECUTED IN NUMERICAL ORDER REGARDLESS OF THE ORDER OF SPECIFICATION. THE DEFAULT IS TO EXECUTE ALL TESTS. ON THIS AND ALL SWITCHES, THE ANGLE BRACKETS <> ARE PUNCTUATION USED IN THE DEFINITION ONLY, AND ARE NOT TO BE TYPED BY THE OPERATOR. SEE EXAMPLE AT END OF "EFFECT OF START COMMAND" SECTION.

2.4.1.2 PASS SWITCH (/PASS:<PASS-CNT>) -

<PASS-CNT> IS A DECIMAL NUMBER INDICATING THE DESIRED NUMBER OF PASSES. A PASS IS DEFINED AS THE EXECUTION OF THE FULL DIAGNOSTIC (ALL SELECTED TESTS). THE DEFAULT IS NON-ENDING EXECUTION. IN THIS CASE, EXIT FROM THE PROGRAM IS ACCOMPLISHED EITHER BY TYPING A CONTROL/C OR BY OCCURANCE OF AN ERROR WITH THE HALT ON ERROR FLAG BEING SET. THE EXIT IS A RETURN TO COMMAND MODE. SEE EXAMPLE AT END OF "EFFECT OF START COMMAND" SECTION.

2.4.1.3 FLAGS SWITCH (/FLAGS:<FLAG-LIST>) -

<FLAG-LIST> IS A SEQUENCE OF ELEMENTS OF THE FORM <FLAG>, <FLAG=1>, OR <FLAG=0>, SEPERATED BY COLONS, WHERE <FLAG> HAS ONE OF THE FOLLOWING VALUES:

HOE	HALT ON ERROR, CAUSING COMMAND MODE TO BE ENTERED WHEN AN ERROR IS ENCOUNTERED.
LOE	LOOP ON ERROR, CAUSING THE DIAGNOSTIC TO LOOP CONTINUOUSLY WITHIN THE SMALLEST DEFINED BLOCK OF CODING (SEGMENT, SUBTEST, OR TEST) CONTAINING THE ERROR.
IER	INHIBIT ERROR REPORTING.
IBE	INHIBIT BASIC ERROR REPORTS.
IXE	INHIBIT EXTENDED ERROR REPORTS.
PRI	DIRECT ALL MESSAGES TO A LINE PRINTER.
PNT	PRINT NUMBER OF TEST BEING EXECUTED.
BOE	BELL ON ERROR (NOT RELATED TO BELL PROMPTING).
UAM	RUN IN UNATTENDED MODE, BYPASSING MANUAL INTERVENTION (ILLEGAL FOR THIS DIAGNOSTIC).
ISR	INHIBIT STATISTICAL REPORTS.

IDU INHIBIT DROPPING OF UNITS BY DIAGNOSTIC.
(HAS NO EFFECT IN THIS DIAGNOSTIC.)

LOT LOOP ON TEST.

THE FLAGS NAMED OR EQUATED TO 1 ARE SET. THOSE EQUATED TO 0 ARE CLEARED. A FLAG NOT SPECIFIED IS CLEARED. IF THE FLAGS SWITCH IS NOT GIVEN ALL FLAGS ARE CLEARED. SEE EXAMPLE AT END OF "EFFECT OF START COMMAND" SECTION.

2.4.1.4 END OF PASS SWITCH (/EOP:<INCR>) -

<INCR> IS A DECIMAL NUMBER INDICATING HOW OFTEN (IN TERMS OF PASSES) IT IS DESIRED THAT THE END OF PASS MESSAGE BE PRINTED. THE DEFAULT IS AT THE END OF EVERY PASS. SEE EXAMPLE AT END OF "EFFECT OF START COMMAND" SECTION.

2.4.1.5 EFFECT OF START COMMAND -

THE EFFECT OF THE START COMMAND IS TO INITIATE THE HARDWARE PARAMETER DIALOGUE, THE SOFTWARE PARAMETER DIALOGUE, THE INITIALIZATION QUESTIONS, AND THEN THE DIAGNOSTIC COMMENCES TESTING.

THE HARDWARE PARAMETER DIALOGUE COMMENCES WITH THE QUESTION "UNITS (D) ?" TO WHICH THE OPERATOR SHOULD REPLY WITH THE NUMBER OF UNITS TO BE TESTED. FOLLOWING THIS ARE THE QUESTIONS WHEREBY THE P-TABLES THEMSELVES ARE BUILT. EACH P-TABLE IS A CORE-RESIDENT TABLE CONTAINING ALL THE HARDWARE INFORMATION FOR ONE COMPLETE UNIT. EACH QUESTION IS FOLLOWED BY THE RESPONSE RADIX (D FOR DECIMAL, B FOR BINARY, O FOR OCTAL, L FOR YES/NO) IN PARENTHESES AND THE DEFAULT VALUE AFTER THE PARENTHESES. FOR THE ACTUAL HARDWARE P-TABLE QUESTIONS SEE THE "HARDWARE PARAMETERS" SECTION.

FOLLOWING THE HARDWARE QUESTIONS ARE THE SOFTWARE QUESTIONS TO BUILD THE SOFTWARE TABLES, WHICH DEFINE OPERATING PARAMETERS OF THE DIAGNOSTIC PROGRAM. THESE QUESTIONS ARE DESCRIBED IN THE "SOFTWARE PARAMETERS" SECTION.

EXAMPLE:

STA/TESTS:1:3-4:/PASS:3/FLAGS:IER:HOE=1

THIS COMMAND WILL CAUSE THREE PASSES TO BE MADE, WITH EACH PASS CONSISTING OF TESTS 1,3, AND 4. THERE IS NO DIFFERENCE BETWEEN SAYING <FLAG> AND SAYING <FLAG=1>. THE NOTATION <FLAG=0> IS MEANINGFUL ONLY ON A COMMAND OTHER THAN START TO CLEAR A FLAG THAT WAS PREVIOUSLY SET. NOTE THAT ON ALL COMMANDS ONLY THE FIRST THREE LETTERS ARE SCANNED.

2.4.2 RESTART COMMAND -

RES(TART)/TESTS:<TEST-LIST>/PASS:<PASS-CNT>/FLAGS:
<FLAG-LIST>/UNITS:<UNIT-LIST>

2.4.2.1 TESTS, PASS, AND FLAGS SWITCHES -

<TEST-LIST>, <PASS-CNT>, AND <FLAG-LIST> ARE AS IN THE START
COMMAND.

2.4.2.2 UNITS SWITCH (/UNITS:<UNIT-LIST>) - <UNIT-LIST> IS A SEQUENCE
OF DECIMAL NUMBERS (0,1 ETC.) OR RANGES OF DECIMAL NUMBERS (0-5, 8-10
ETC.) THAT SPECIFY THE UNITS TO BE TESTED. THE NUMBERS ARE SEPARATED
BY COLONS. THE NUMBERS MAY RANGE FROM 0 THRU N-1 (N IS THE NUMBER OF
UNITS SPECIFIED IN THE PREVIOUS START COMMAND). THE NUMBER INDICATES
THE POSITION OF THE P-TABLE AS THE DATA WAS ENTERED DURING THE
HARDWARE DIAGLOGUE. THE UNITS WHICH ARE SELECTED MUST NOT HAVE BEEN
DROPPED BY THE DROP COMMAND. SEE THE DISCUSSION OF ADD AND DROP
COMMANDS BELOW. DEFAULT IS TO TEST ALL UNITS WHICH HAVE NOT BEEN
DROPPED BY A DROP COMMAND.

2.4.2.3 EFFECT OF RESTART COMMAND -

THE RESTART COMMAND DIFFERS FROM THE START COMMAND IN THAT THE
P-TABLES FROM THE PREVIOUS START COMMAND (THERE MUST HAVE BEEN ONE)
ARE USED, INSTEAD OF NEW ONES BEING BUILT. THE UNITS SWITCH SHOULD
NOT BE USED WITH THIS PROGRAM. THE SOFTWARE DIALOGUE MAY OPTIONALLY
BE REEXECUTED (OPERATOR WILL BE ASKED). THE COMMAND CAN BE USED AFTER
COMMAND MODE HAS BEEN REENTERED IN ANY OF THE THREE NORMAL WAYS: A)
THE REQUESTED NUMBER OF PASSES HAVE BEEN MADE, B) AN ERROR WAS
ENCOUNTERED WITH THE HALT ON ERROR FLAG SET, OR C) A CONTROL/C WAS
ENTERED BY THE OPERATOR.

2.4.3 CONTINUE COMMAND -

CON(TINUE)/PASS:<PASS-CNT>/FLAGS:<FLAG-LIST>

2.4.3.1 FLAG SWITCH (/FLAGS:<FLAG-LIST>) -

<FLAG-LIST> IS SAME AS IN THE START COMMAND, BUT UNSPECIFIED
FLAGS RETAIN THEIR CURRENT VALUE.

2.4.3.2 EFFECT OF CONTINUE COMMAND -

CONTINUE MUST FOLLOW A START OR RESTART, AND COMMAND MODE MUST HAVE BEEN ENTERED DUE TO A HALT ON ERROR OR A CONTROL/C. THE EFFECT OF THE COMMAND IS TO GO TO THE BEGINNING OF THE TEST THAT WAS BEING EXECUTED WHEN THE HALT OR CONTROL/C TOOK PLACE. SOFTWARE DIALOGUE MAY OPTIONALLY BE REEXECUTED. HARDWARE PARAMETERS MAY NOT BE CHANGED.

2.4.4 PROCEED COMMAND -

PRO(CEED)/FLAGS:<FLAG-LIST>

2.4.4.1 FLAGS SWITCH (/FLAGS:<FLAG-LIST>) -

<FLAG-LIST> IS AS IN THE START COMMAND, BUT UNSPECIFIED FLAGS RETAIN THEIR CURRENT VALUE.

2.4.4.2 EFFECT OF PROCEED COMMAND -

PROCEED MUST FOLLOW A START, RESTART, OR CONTINUE. COMMAND MODE MUST HAVE BEEN ENTERED VIA A HALT ON ERROR. THE EFFECT OF THE COMMAND IS TO BEGIN EXECUTION AT THE LOCATION FOLLOWING THE ERROR CALL. NEITHER HARDWARE NOR SOFTWARE PARAMETERS MAY BE ALTERED.

2.4.5 ADD COMMAND -

ADD/UNITS:<UNIT-LIST>

2.4.6 EFFECT OF ADD COMMAND -

THE UNITS SPECIFIED ARE ADDED TO THE TEST SEQUENCE. EACH UNIT MUST HAVE A P-TABLE IN MEMORY DUE TO AN EARLIER HARDWARE DIALOGUE. THIS COMMAND MUST BE FOLLOWED BY A RESTART OR CONTINUE. THE UNITS SWITCH MUST BE SPECIFIED. THE ADD COMMAND IS MEANINGFUL ONLY FOR UNITS THAT WERE PREVIOUSLY DROPPED.

2.4.7 DROP COMMAND -

DRO(P)/UNITS:<UNIT-LIST>

M1
2.4.8 EFFECT OF DROP COMMAND -
THE UNITS SPECIFIED WILL BE DROPPED FROM TESTING. THE UNITS
WILL BE RESELECTED ONLY BY THE EXECUTION OF AN ADD OR START
COMMAND. THE UNITS SWITCH MUST BE ENTERED. THIS COMMAND
MUST BE FOLLOWED BY A RESTART OR A CONTINUE COMMAND.

2.4.9 PRINT COMMAND -

PRI(NT)

2.4.9.1 EFFECT OF PRINT COMMAND -
THE TOTAL NUMBER OF ERRORS FOR EACH UNIT SINCE THE LAST
START OR RESTART COMMAND ARE PRINTED. THE ISR (INHIBIT
STATISTICAL REPORTING) FLAG IS CLEARED.

2.4.10 DISPLAY COMMAND -

DIS(PLAY)/UNITS:<UNIT-LIST>

2.4.10.1 EFFECT OF DISPLAY COMMAND -
THE HARDWARE P-TABLE FOR THE TEST STATION IS PRINTED IN THE
FORMAT IN WHICH IT WAS ENTERED.

2.4.11 FLAGS COMMAND -

FLA(GS)

2.4.11.1 EFFECT OF FLAGS COMMAND -
THE CURRENT SETTINGS OF ALL FLAGS ARE PRINTED.

2.4.12 ZFLAGS COMMAND -

ZFL(AGS)

2.4.13 ZFLAGS COMMAND -

ALL FLAGS ARE CLEARED.

2.4.14 CONTROL CHARACTERS -

- C A CONTROL/C (C) ENTERED DURING THE EXECUTION OF A DIAGNOSTIC CAUSES A RETURN TO COMMAND MODE.
- Z A CONTROL/Z (Z) ENTERED DURING ONE OF THE TWO OPERATOR DIALOGUES-- HARDWARE P-TABLE DIALOGUE OR SOFTWARE P-TABLE DIALOGUE CAUSES THE DEFAULTS TO BE TAKEN FOR THE REMAINDER OF THAT DIALOGUE.
- O A CONTROL/O (O) ENTERED DURING THE EXECUTION OF A DIAGNOSTIC CAUSES ALL TELETYPE OUTPUT TO BE SURPRESSED FOR THE REMAINDER OF THE DIAGNOSTIC OR UNTIL ANOTHER CONTROL/O IS TYPED, WHICH RESTORES NORMAL TELETYPE OUTPUT.

2.5 HARDWARE QUESTIONS

WHEN A DIAGNOSTIC IS STARTED, THE RUNTIME SERVICES WILL PROMPT THE USER FOR HARDWARE INFORMATION BY TYPING "CHANGE HW (L) ?" YOU MUST ANSWER "Y" AFTER A START COMMAND UNLESS THE HARDWARE INFORMATION HAS BEEN "PRELOADED" USING THE SETUP UTILITY (SEE CHAPTER 6 OF THE XXDP, USER'S MANUAL). WHEN YOU ANSWER THIS QUESTION WITH A "Y", THE RUNTIME SERVICES WILL ASK FOR THE NUMBER OF UNITS (IN DECIMAL). YOU WILL THEN BE ASKED THE FOLLOWING QUESTIONS FOR EACH UNIT.

1. CSR ADDRESS - THIS QUESTION REQUESTS THE CSR ADDRESS OF THE SPECIFIED DMU-11. THE DEFAULT ANSWER FOR THIS QUESTION IS ADDRESS 160460 (OCTAL).
2. INTERRUPT VECTOR ADDRESS - THIS QUESTION REQUESTS THE INTERRUPT VECTOR ADDRESS OF THE SPECIFIED DMU-11. THE DEFAULT ANSWER IS 310 (OCTAL).
3. ACTIVE LINES BIT MAP - THIS QUESTION REQUESTS AN OCTAL BIT MAP OF THE SERIAL COMMUNICATION LINES ON THE DMU11 WHICH ARE BEING SELECTED FOR TESTING. IF THE BIT IN THE BIT MAP IS SET WHICH CORRESPONDS TO A PARTICULAR LINE (I.E. BIT 5 FOR LINE 5) THAT LINE WILL BE TESTED BY THE FVT.
4. BR LEVEL - THIS QUESTION REQUESTS THE INTERRUPT BR LEVEL OF THE SPECIFIED DMU-11. THE DEFAULT ANSWER IS BR 5.

2.6 SOFTWARE QUESTIONS

AFTER YOU HAVE ANSWERED THE HARDWARE QUESTIONS OR AFTER A RESTART OR CONTINUE COMMAND, THE RUNTIME SERVICES WILL ASK FOR SOFTWARE PARAMETERS. THESE PARAMETERS WILL GOVERN SOME DIAGNOSTIC SPECIFIC OPERATION MODES. YOU WILL BE PROMPTED BY "CHANGE SW (L) ?" IF YOU WISH TO CHANGE ANY PARAMETERS, ANSWER BY TYPING "Y". THE SOFTWARE QUESTIONS AND THE DEFAULT VALUES ARE DESCRIBED IN THE NEXT PARAGRAPH(S).

1. REPORT UNIT NUMBER AS EACH UNIT IS TESTED - THIS QUESTION ASKS WHETHER THE PROGRAM SHOULD REPORT THE NUMBER OF THE UNIT WHICH IT IS TESTING AS IT BEGINS TO TEST THAT UNIT.
2. ROM VERSION PRINTOUT ON THE FIRST PASS - THIS QUESTION ASKS WHETHER THE PROGRAM SHOULD PRINTOUT THE VERSIONS OF THE ON BOARD PROCESSOR ROMS DURING THE FIRST PASS OF THE PROGRAM.
3. EXTENDED ERROR REPORTING - THIS QUESTION ASKS WHETHER EXTENDED ERROR INFORMATION IS REQUIRED OTHER THAN THE "TEST FAILED" MESSAGE, ON EACH ERROR REPORTED. THE DEFAULT IS "NO" I.E. ONLY A MESSAGE REPORTING THE FACT THAT THE TEST FAILED WILL BE PRINTED.
4. NUMBER OF INDIVIDUAL DATA ERRORS TO REPORT ON A LINE - THIS QUESTION IS ASKED ONLY IF THE PREVIOUS QUESTION WAS ANSWERED "YES". THE QUESTION ASKS FOR THE NUMBER OF DATA ERRORS WHICH SHOULD BE REPORTED INDIVIDUALLY BY THIS PROGRAM FOR EACH LINE FOR EACH TRANSMISSION TEST. ERRORS WHICH ARE NOT REPORTED INDIVIDUALLY ARE REPORTED IN SUMMARY ERROR REPORTS.

2.7 EXTENDED P-TABLE DIALOGUE

WHEN YOU ANSWER THE HARDWARE QUESTIONS, YOU ARE BUILDING ENTRIES IN A TABLE THAT DESCRIBES THE DEVICES UNDER TEST. THE SIMPLEST WAY TO BUILD THIS TABLE IS TO ANSWER ALL QUESTIONS FOR EACH UNIT TO BE TESTED. IF YOU HAVE A MULTIPLEXED DEVICE SUCH AS A MASS STORAGE CONTROLLER WITH SEVERAL DRIVES OR A COMMUNICATION DEVICE WITH SEVERAL LINES, THIS BECOMES TEDIOUS SINCE MOST OF THE ANSWERS ARE REPETITIOUS.

TO ILLUSTRATE A MORE EFFICIENT METHOD, SUPPOSE YOU ARE TESTING A FICTIONAL DEVICE, THE XY11. SUPPOSE THIS DEVICE CONSISTS OF A CONTROL MODULE WITH EIGHT UNITS (SUB-DEVICES) ATTACHED TO IT. THESE UNITS ARE DESCRIBED BY THE OCTAL NUMBERS 0 THROUGH 7. THERE IS ONE HARDWARE PARAMETER THAT CAN VARY AMONG UNITS CALLED THE Q-FACTOR. THIS Q-FACTOR MAY BE 0 OR 1. BELOW IS A SIMPLE WAY TO BUILD A TABLE FOR ONE XY11 WITH EIGHT UNITS.

UNITS (0) ? 8<CR>

UNIT 1

CSR ADDRESS (0) ? 160000<CR>
 SUB-DEVICE # (0) ? 0<CR>
 Q-FACTOR (0) 0 ? 1<CR>

UNIT 2

CSR ADDRESS (0) ? 160000<CR>
 SUB-DEVICE # (0) ? 1<CR>
 Q-FACTOR (0) 1 ? 0<CR>

UNIT 3

CSR ADDRESS (0) ? 160000<CR>
 SUB-DEVICE # (0) ? 2<CR>
 Q-FACTOR (0) 0 ? <CR>

UNIT 4

CSR ADDRESS (0) ? 160000<CR>
 SUB-DEVICE # (0) ? 3<CR>
 Q-FACTOR (0) 0 ? <CR>

UNIT 5

CSR ADDRESS (0) ? 160000<CR>
 SUB-DEVICE # (0) ? 4<CR>
 Q-FACTOR (0) 0 ? <CR>

UNIT 6

CSR ADDRESS (0) ? 160000<CR>
 SUB-DEVICE # (0) ? 5<CR>
 Q-FACTOR (0) 0 ? <CR>

UNIT 7

CSR ADDRESS (0) ? 160000<CR>
 SUB-DEVICE # (0) ? 6<CR>
 Q-FACTOR (0) 0 ? 1<CR>

```
UNIT 8  
CSR ADDRESS (0) 160000<CR>  
SUB-DEVICE # (0) ? 7<CR>  
Q-FACTOR (0) 1 ? <CR>
```

NOTICE THAT THE DEFAULT VALUE FOR THE Q-FACTOR CHANGES WHEN A NON-DEFAULT RESPONSE IS GIVEN. BE CAREFUL WHEN SPECIFYING MULTIPLE UNITS!

AS YOU CAN SEE FROM THE ABOVE EXAMPLE, THE HARDWARE PARAMETERS DO NOT VARY SIGNIFICANTLY FROM UNIT TO UNIT. THE PROCEDURE SHOWN IS NOT VERY EFFICIENT.

THE RUNTIME SERVICES CAN TAKE MULTIPLE UNIT SPECIFICATIONS HOWEVER. LET'S BUILD THE SAME TABLE USING THE MULTIPLE SPECIFICATION FEATURE.

```
# UNITS (0) ? 8<CR>
```

```
UNIT 1  
CSR ADDRESS (0) ? 160000<CR>  
SUB-DEVICE # (0) ? 0,1<CR>  
Q-FACTOR (0) 0 ? 1,0<CR>
```

```
UNIT 3  
CSR ADDRESS (0) ? 160000<CR>  
SUB-DEVICE # (0) ? 2-5<CR>  
Q-FACTOR (0) 0 ? 0<CR>
```

```
UNIT 7  
CSR ADDRESS (0) ? 160000<CR>  
SUB-DEVICE # (0) ? 6,7<CR>  
Q-FACTOR (0) 0 ? 1<CR>
```

AS YOU CAN SEE IN THE ABOVE DIALOGUE, THE RUNTIME SERVICES WILL BUILD AS MANY ENTRIES AS IT CAN WITH THE INFORMATION GIVEN IN ANY ONE PASS THROUGH THE QUESTIONS. IN THE FIRST PASS, TWO ENTRIES ARE BUILT SINCE TWO SUB-DEVICES AND Q-FACTORS WERE SPECIFIED. THE SERVICES ASSUME THAT THE CSR ADDRESS IS 160000 FOR BOTH SINCE IT WAS SPECIFIED ONLY ONCE. IN THE SECOND PASS, FOUR ENTRIES WERE BUILT. THIS IS BECAUSE FOUR SUB-DEVICES WERE SPECIFIED. THE "-" CONSTRUCT TELLS THE RUNTIME SERVICES TO INCREMENT THE DATA FROM THE FIRST NUMBER TO THE SECOND. IN THIS CASE, SUB-DEVICES 2, 3, 4 AND 5 WERE SPECIFIED. (IF THE SUB-DEVICE WERE SPECIFIED BY ADDRESSES, THE INCREMENT WOULD BE BY 2 SINCE ADDRESSES MUST BE ON AN EVEN BOUNDARY.) THE CSR ADDRESSES AND Q-FACTORS FOR THE FOUR ENTRIES ARE ASSUMED TO BE 160000 AND 0 RESPECTIVELY SINCE THEY WERE ONLY SPECIFIED ONCE. THE LAST TWO UNITS ARE SPECIFIED IN THE THIRD PASS.

THE WHOLE PROCESS COULD HAVE BEEN ACCOMPLISHED IN ONE PASS AS SHOWN BELOW.

```
# UNITS (0) ? 8<CR>
```


UNIT 1
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 0-7<CR>
Q-FACTOR (0) 0 ? 0.1,0....,1.1<CR>

AS YOU CAN SEE FROM THIS EXAMPLE, NULL REPLIES (COMMAS ENCLOSING A NULL FIELD) TELL THE RUNTIME SERVICES TO REPEAT THE LAST REPLY.

2.8 QUICK START-UP PROCEDURE (XXDP+)

TO START-UP THIS PROGRAM:

1. BOOT XXDP+
2. GIVE THE DATE AND ANSWER THE LSI/UNIBUS AND 50HZ (IF THERE IS A CLOCK) QUESTIONS. NOTE, NOT ALL VERSIONS OF XXDP+ ASK FOR THE CLOCK FREQUENCY
3. TYPE "R NAME", WHERE NAME IS THE NAME OF THE BIN OR BIC FILE FOR THIS PROGRAM
4. TYPE "START"
5. ANSWER THE "CHANGE HW" QUESTION WITH "Y"
6. ANSWER ALL THE HARDWARE QUESTIONS
7. ANSWER THE "CHANGE SW" QUESTION WITH "N"

WHEN YOU FOLLOW THIS PROCEDURE YOU WILL BE USING ONLY THE DEFAULTS FOR FLAGS AND SOFTWARE PARAMETERS. FOR DEFAULT INFORMATION SEE THE SECTIONS WITHIN THIS DOCUMENT ON FLAGS, AND HARDWARE QUESTIONS.

3.0 ERROR INFORMATION

3.1 TYPES OF ERROR MESSAGES

THERE ARE THREE LEVELS OF ERROR MESSAGES THAT MAY BE ISSUED BY A DIAGNOSTIC: GENERAL, BASIC AND EXTENDED. GENERAL ERROR MESSAGES ARE ALWAYS PRINTED UNLESS THE "IER" FLAG IS SET (SEE THE FLAGS SECTION OF THIS DOCUMENT).

THE GENERAL ERROR MESSAGE IS OF THE FORM:

NAME TYPE NUMBER ON UNIT NUMBER TST NUMBER PC:XXXXXX
ERROR MESSAGE

.WHERE; NAME = DIAGNOSTIC NAME
TYPE = ERROR TYPE (SYS FATAL, DEV FATAL, HARD OR SOFT)
NUMBER = ERROR NUMBER
UNIT NUMBER = 0 - N (N IS LAST UNIT IN PTABLE)
TST NUMBER = TEST AND SUBTEST WHERE ERROR OCCURRED
PC:XXXXXX = ADDRESS OF ERROR MESSAGE CALL

BASIC ERROR MESSAGES ARE MESSAGES THAT CONTAIN SOME ADDITIONAL INFORMATION ABOUT THE ERROR. THESE ARE ALWAYS PRINTED UNLESS THE "IER" OR "IBR" FLAGS ARE SET (SEE THE FLAGS SECTION OF THIS DOCUMENT).
THESE MESSAGES ARE PRINTED AFTER THE ASSOCIATED GENERAL MESSAGE.

EXTENDED ERROR MESSAGES CONTAIN SUPPLEMENTARY ERROR INFORMATION SUCH AS REGISTER CONTENTS OR GOOD/BAD DATA. THESE ARE ALWAYS PRINTED UNLESS THE "IER", "IBR" OR "IXR" FLAGS ARE SET (SEE THE FLAGS SECTION OF THIS DOCUMENT).
THESE MESSAGES ARE PRINTED AFTER THE ASSOCIATED GENERAL ERROR MESSAGE AND ANY ASSOCIATED BASIC ERROR MESSAGES.

3.2 SPECIFIC ERROR MESSAGES

THIS PROGRAM IS INTENDED TO PROVIDE A GO/NOGO INDICATION OF THE FUNCTIONALITY OF THE DHU-11 BOARDS. TO EXECUTE THE PROGRAM IN THIS MODE THE OPERATOR NEED ONLY ANSWER THE "EXTENDED ERROR REPORTING" SOFTWARE QUESTION WITH "NO". THE PROGRAM WILL THEN ONLY PRINT THE NAME OF THE FAILING TEST THE TEST AND ERROR NUMBERS. FOR A LIST OF THE TEST NAMES IN THIS PROGRAM SEE THE TEST SUMMARIES SECTION OF THIS DOCUMENT. AN EXAMPLE OF SUCH AN ERROR MESSAGE IS THE FOLLOWING:

CZDHU DVC FTL ERR 01603 ON UNIT 02 TST 16 SUB 000 PC: XXXXXX
DEVICE REGISTER WORD READ/WRITE TEST FAILED.

THIS ERROR INDICATES THAT A FATAL ERROR WAS ENCOUNTERED WITHIN THE TEST WHICH TESTS THE READ/WRITE CAPABILITY OF THE DHU-11 REGISTERS.

IF THE OPERATOR HAD REQUESTED EXTENDED ERROR REPORTING THE SAME ERROR WOULD BE REPORTED AS FOLLOWS:

CZDHU DVC FTL ERR 01603 ON UNIT 02 TST 16 SUB 000 PC: XXXXXX
DEVICE REGISTER WORD READ/WRITE TEST FAILED.
BAD BIT(S) IN DEVICE TBUFAD1 REGISTER FOR LINE 7 (D).
EXPECTED DATA: 000000 (0).
ACTUAL DATA: 000023 (0).

4.0 PERFORMANCE AND PROGRESS REPORTS

AT THE END OF EACH PASS, THE PASS COUNT IS GIVEN ALONG WITH THE TOTAL NUMBER OF ERRORS REPORTED SINCE THE DIAGNOSTIC WAS STARTED. THE "EOP" SWITCH CAN BE USED TO CONTROL HOW OFTEN THE END OF PASS MESSAGE IS PRINTED. FOR FUTHER INFORMATION SEE THE SWITCHES SECTION OF THIS DOCUMENT.

5.0 TEST SUMMARIES

THE FOLLOWING ARE INCLUDED WITHIN CZDHUB:

1. DEVICE REGISTER ACCESS TEST - VERIFIES THAT THE UUT REGISTERS WILL RESPOND WITH THE CORRECT UNIBUS HANDSHAKING SIGNALS. VERIFIES THAT THE UUT IS AT THE CORRECT ADDRESS.
2. MASTER.RESET (SELFTEST) TEST - VERIFIES THAT THE MASTER.RESET BIT CLEARS WITHIN A SPECIFIED TIME OF IT BEING SET.
3. MASTER.RESET (SKIP SELFTEST) TEST - VERIFIES THAT THE MASTER RESET CLEARS WITHIN A SHORT TIME AFTER IT IS SET WHEN THE SKIP SELFTEST SEQUENCE IS USED.
4. RX.CHARACTER FIELD TEST - VERIFIES THAT THE DATA BITS OF THE CODES IN THE RXFIFO AFTER A MASTER RESET AND SKIP SELFTEST ARE CONSISTANT WITH THE SKIP SELFTEST CODES.
5. RX.FLAG FIELD TEST - VERIFIES THAT THE 3 DATA STATUS BITS (OVERRUN, FRAMING AND PARITY ERROR BITS) ARE ALL SET ON EACH OF THE SKIP SELFTEST CODES IN THE FIFO AFTER A MASTER RESET AND SKIP SELFTEST SEQUENCE.
6. RX.DATA.AVAIL TEST - VERIFIES THAT THE RX.DATA.AVAIL BIT IS SET WHEN THE SKIP SELFTEST CODES ARE IN THE FIFO AND THAT IT CLEARS AFTER THEY HAVE BEEN READ.
7. RX.DATA.VALID TEST - VERIFIES THAT THE RX.DATA.VALID BIT IS SET FOR ALL THE CODES IN THE FIFO AND CLEAR AFTER ALL THE CODES HAVE BEEN READ.
8. RX.LINE FIELD TEST - VERIFIES THAT THE RX.LINE LINE FIELDS ARE CORRECT FOR THE SKIP SELFTEST CODES.
9. BMP CHECK TEST - VERIFIES THAT THE DUT DOES NOT IMMEDIATELY FAIL THE BACKGROUND MONITOR PROGRAM, AS THIS MAY INVALIDATE FURTHER TESTS.
10. SKIP SELFTEST TEST - VERIFIES THAT THE DUT SKIPS THE SELFTEST IN THE TIME ALLOWED, AND THAT THE FIFO CONTAINS THE CORRECT CODES AFTER ITS COMPLEATION.
11. DIAGNOSTIC.FAIL (SKIP SELFTEST) TEST - VERIFIES USING THE SKIP SELFTEST SEQUENCE THAT THE DIAG.FAIL BIT GOES TO BOTH THE ACTIVE AND INACTIVE STATES WITHIN THE ALLOWED TIMES.
12. SELFTEST TEST - VERIFIES THAT THE DUT'S SELFTEST EXECUTES WITHIN THE CORRECT TIME AND THAT THE CORRECT CODES ARE RETURNED IN THE FIFO AFTER ITS COMPLEATION.
13. SELFTEST FAIL TEST - VERIFIES THAT THE DUT WILL REPORT ERRORS CORRECTLY WHEN IT IS FORCED TO FAIL.

14. ROM VERSION NUMBER - VERIFIES THAT THE ROM VERSION NUMBERS ARE REPORTED CORRECTLY AND IF REQUESTED PRINTS THEM OUT.
15. CSR BIT 4 TEST - VERIFIES THAT WHEN SET THIS BIT CAUSES THE SELFTEST TO LOOP, AND WHEN CLEARED THE SKIP SELFTEST CODES ARE RETURNED IN THE RXFIFO.
16. WORD ACCESS READ/WRITE TEST - VERIFIES THAT THE REGISTERS RESPOND CORRECTLY TO READ AND WRITE ACCESSES.
17. WORD ACCESS READ/MODIFY/WRITE TEST - VERIFIES THAT THE REGISTERS WILL RESPOND CORRECTLY TO READ/MODIFY/WRITE ACCESSES.
18. BYTE ACCESS READ/WRITE TEST - VERIFIES THAT THE REGISTERS WILL RESPOND CORRECTLY TO BYTE READ/WRITE ACCESSES.
19. BYTE ACCESS READ/MODIFY/WRITE - VERIFIES THAT THE REGISTERS WILL RESPOND CORRECTLY TO BYTE READ/MODIFY/WRITE ACCESSES.
20. ID.BIT TEST - VERIFIES THAT THE ID BIT READS AS SET.
21. TX.ENABLE (INACTIVE) TEST - VERIFIES THAT WHEN A LINE'S TX.ENBL BIT IS CLEAR, TRANSMISSION WILL NOT TAKE PLACE ON THAT LINE.
22. TX.ENABLE (ACTIVE) TEST - VERIFIES THAT WHEN A LINE'S TX.ENBL BIT IS SET, TRANSMISSION WILL TAKE PLACE ON THAT LINE.
23. INTERRUPT TEST - VERIFIES THAT THE DUT WILL GENERATE RECEPTION AND TRANSMISSION INTERRUPTS CORRECTLY.
24. BR LEVEL TEST - VERIFIES THAT THE DUT INTERRUPTS AT THE CORRECT BUS REQUEST LEVEL.
25. DIAGNOSTIC FIELD (BMP) TEST - VERIFIES THAT A REQUEST TO THE DUT TO REPORT BMP STATUS CODES IS COMPLIED WITH WITHIN THE SPECIFIED TIME. ALL ACTIVE LINES ARE TESTED.
26. REPORT BMP CODES TEST - THIS PSEUDO TEST REPORTS THE FIRST 32 BMP CHARACTERS WHICH WERE DISCOVERED IN THE FIFO DURING THE EXECUTION OF THE OTHER TESTS. THIS AVOIDS INTERRUPTION OF THE OTHER TESTS BY THESE CODES IF THEY ARE NOT CRITICAL TO THE PERFORMANCE OF THE TESTS.

6.0 EXAMPLE ERROR FREE PASS

THE FOLLOWING IS AN EXAMPLE OF AN ERROR FREE PASS DIALOGUE:

.R CZDHUBO
CZDHUBO.BIN

DRS
CZDHU-B-0
DHU-11 FUNC TST PART1
UNIT IS DHU-11
RESTR: ADDR: 147670
DR>STA/PAS:1

CHANGE HW (L) ? Y

* UNITS (D) ? 2

UNIT 0
CSR ADDRESS: (0) 160460 ? +Z

UNIT 1
CSR ADDRESS: (0) 160460 ? 160500
INTERRUPT VECTOR ADDRESS: (0) 310 ? 320
ACTIVE LINE BIT MAP: (0) 177777 ? <CR>
INTERRUPT BR LEVEL: (0) 5 ? <CR>

CHANGE SW (L) ? Y

REPORT UNIT NUMBER AS EACH UNIT IS TESTED: (L) Y ? <CR>
ROM VERSION PRINTOUT ON THE FIRST PASS: (L) Y ? <CR>
EXTENDED ERROR REPORTING: (L) N ? Y
NUMBER OF INDIVIDUAL DATA ERRORS TO REPORT ON A LINE: (D) 0 ? 1

TESTING UNIT : 0(D)

ROM VERSION NUMBERS: PROC_1 = 2(D) PROC_2 = 2(D)

TESTING UNIT : 1(D)

ROM VERSION NUMBERS: PROC_1 = 2(D) PROC_2 = 2(D)

CZDHU EOP 1
0 TOTAL ERRS

DR>

E

1073
1074
1082
1083
1084
1085
1086
1087
1088
1089
1090
1091
1092
1093
1094
1095
1096
1097
1098
1099
1100
1101
1102
1103
1104
1105
1106
1107
1108
1109
1110
1111
1112
1113
1114
1115
1116
1117
1118
1135
1136

000000

000001
000001
000001
000001
000001

000000

002000

002000

002000
002000
002001
002002
002003
002004
002005
002006
002007
002010

103
132
104
110
125
000
000
000

```
.LIST SEQ,LOC,BIN,MEB
.NLIST CND

.SBTTL PROGRAM HEADER

.MCALL SVC
SVC ; INITIALIZE SUPERVISOR MACROS

;*****
; IF STRUCTURED MACROS ARE TO BE USED, ADD ".MCALL STRUCT" AND "STRUCT"
; TO INITIALIZE THE STRUCTURED MACROS.
;*****

SVCINS= 1 ; LIST INSTRUCTIONS, SHIFTED RIGHT
SVCTST= 1 ; LIST TEST TAGS, SHIFTED RIGHT
SVCSUB= 1 ; LIST SUBTEST TAGS, SHIFTED RIGHT
SVCGBL= 1 ; LIST GLOBAL TAGS, SHIFTED RIGHT
SVCTAG= 1 ; LIST OTHER TAGS, SHIFTED RIGHT

; CHANGE THE VALUES OF THE SVC... SYMBOLS TO BE ZERO IF YOU WISH
; TO ALIGN THE MACRO CALLS AND THEIR EXPANSIONS. CHANGE THE
; SYMBOLS TO BE MINUS-ONE TO NOT LIST THE EXPANSIONS. YOU MAY
; CHANGE THE SYMBOLS AT ANY POINT IN YOUR PROGRAM.
;*****

.ENABL ABS
;.ENABL AMA
. = 2000

BGNMOD

; **
; THE PROGRAM HEADER IS THE INTERFACE BETWEEN
; THE DIAGNOSTIC PROGRAM AND THE SUPERVISOR.
; **

POINTER BGNRPT,BGNSW,BGNSFT,BGNDU,ERRTBL

HEADER CZDHU,B,0,16,0,PRI07
```

```
L$NAME::
.ASCII /C/
.ASCII /Z/
.ASCII /D/
.ASCII /H/
.ASCII /U/
.BYTE 0
.BYTE 0
.BYTE 0

L$REV::
```

002010 102
 002011
 002011 060
 002012
 002012 000000
 002014
 002014 000016
 002016
 002016 036700
 002020
 002020 037072
 002022
 002022 002212
 002024
 002024 002224
 002026
 002026 037454
 002030
 002030 000000
 002032
 002032 000000
 002034
 002034 000000
 002036
 002036 000000
 002040
 002040 002124
 002042
 002042 000340
 002044
 002044 000000
 002046
 002046 0C0000
 002050
 002050 003
 002051 003
 002052
 002052 000000
 002054 000000
 002056
 002056 000000
 002060
 002060 004036
 002062
 002062 024222
 002064
 002064 000000
 002066
 002066 000000
 002070
 002070 000000
 002072
 002072 025076
 002074
 002074 000000
 002076
 002076 004046

L\$DEPO: .ASCII /B/
 .ASCII /O/
 L\$UNIT: .WORD 0
 L\$TIML: .WORD 16
 L\$HPCP: .WORD L\$HARD
 L\$SPCP: .WORD L\$SOFT
 L\$HPTP: .WORD L\$HW
 L\$SPTP: .WORD L\$SW
 L\$LADP: .WORD L\$LAST
 L\$STA: .WORD 0
 L\$CO: .WORD 0
 L\$DTYP: .WORD 0
 L\$APT: .WORD 0
 L\$DTP: .WORD 0
 L\$PRI0: .WORD L\$DISPATCH
 L\$ENVI: .WORD PRI07
 L\$EXP1: .WORD 0
 L\$MREV: .WORD 0
 .BYTE C\$REVISION
 L\$EF: .BYTE C\$EDIT
 .WORD 0
 .WORD 0
 L\$SPC: .WORD 0
 L\$DEVP: .WORD L\$DVTYP
 L\$REPP: .WORD L\$RPT
 L\$EXP4: .WORD 0
 L\$EXP5: .WORD 0
 L\$AUT: .WORD 0
 L\$DUT: .WORD L\$DU
 L\$LUN: .WORD 0
 L\$DESP: .WORD L\$DESC

002100	
002100	104035
002102	
002102	003766
002104	
002104	024236
002106	
002106	025060
002110	
002110	025056
002112	
002112	024230
002114	
002114	000000
002116	
002116	000000
002120	
002120	000000

1137

L\$LOAD::	EMT	E\$LOAD
L\$ETP::		
L\$ICP::	.WORD	L\$ERRTBL
L\$CCP::	.WORD	L\$INIT
L\$ACP::	.WORD	L\$CLEAN
L\$PRT::	.WORD	L\$AUTO
L\$TEST::	.WORD	L\$PROT
L\$DLY::	.WORD	0
L\$HIME::	.WORD	0
	.WORD	0

1149
1150
1151
1152
1153
1154
1155
1156

.SBTTL DISPATCH TABLE

; THE DISPATCH TABLE CONTAINS THE STARTING ADDRESS OF EACH TEST.
; IT IS USED BY THE SUPERVISOR TO DISPATCH TO EACH TEST.

DISPATCH 26

002122
002122 000032
002124
002124 025214
002126 025476
002130 025726
002132 026172
002134 026370
002136 026562
002140 027000
002142 027206
002144 027414
002146 027610
002150 030024
002152 030252
002154 030546
002156 031034
002160 031434
002162 031776
002164 032244
002166 032434
002170 032752
002172 033214
002174 033330
002176 033646
002200 034222
002202 035360
002204 036322
002206 036616
1157

.WORD 26
L\$DISPATCH::
.WORD T1
.WORD T2
.WORD T3
.WORD T4
.WORD T5
.WORD T6
.WORD T7
.WORD T8
.WORD T9
.WORD T10
.WORD T11
.WORD T12
.WORD T13
.WORD T14
.WORD T15
.WORD T16
.WORD T17
.WORD T18
.WORD T19
.WORD T20
.WORD T21
.WORD T22
.WORD T23
.WORD T24
.WORD T25
.WORD T26

1172
 1173
 1174
 1175
 1176
 1177
 1178
 1179
 1180
 1181
 1182
 1183
 1184
 1185
 1186
 1187
 1188
 1189
 1190
 1191

002210
 002210 000004
 002212
 002212
 160460
 000310
 177777
 005
 002222
 002222

.SBTTL DEFAULT HARDWARE P-TABLE

```

; **
; THE DEFAULT HARDWARE P-TABLE CONTAINS DEFAULT VALUES OF
; THE TEST-DEVICE PARAMETERS. THE STRUCTURE OF THIS TABLE
; IS IDENTICAL TO THE STRUCTURE OF THE HARDWARE P-TABLES,
; AND IS USED AS A "TEMPLATE" FOR BUILDING THE P-TABLES.
; --
    
```

BGNHW DFPTBL

```

        .WORD    L10000-L$HW/2
L$HW::
DFPTBL::
    
```

```

.WORD    160460 ;DEFAULT CSR ADDRESS
.WORD    310    ;DEFAULT VECTOR ADDRESS
.WORD    177777 ;DEFAULT ACTIVE LINES BIT MAP
.BYTE    5     ;DEFAULT BR LEVEL
.EVEN
    
```

ENDHW

L10000:

1200
1201
1202
1203
1204
1205
1206
1207
1208
1209
1210
1211

002222
002222 000002
002224
002224

1212
1213
1214
1215
1216

002224 000021
002226 000000
002230
002230

.SBTTL SOFTWARE P-TABLE

```

; **
; THE SOFTWARE TABLE CONTAINS VARIOUS DATA USED BY THE
; PROGRAM AS OPERATIONAL PARAMETERS. THESE PARAMETERS ARE
; SET UP AT ASSEMBLY TIME AND MAY BE VARIED BY THE OPERATOR
; AT RUN TIME.
; --
    
```

BGNSW SFPTBL

```

        .WORD L10001-L$SW/2
L$SW::
SFPTBL::
    
```

```

OPTION:: .WORD 21
NDERPT:: .WORD 0
    
```

```

;BIT MAP OF PROGRAM CONTROL FLAGS
;DEFAULT NUMBER OF INDIVIDUAL DATA ERRORS TO RPT.
    
```

ENDSW

L10001:

1225
 1226
 1227
 1237
 1238
 1239
 1240
 1241
 1242
 1243
 1244
 1245
 1246
 1247
 1248
 1249
 1250
 1251
 1252
 1267 002230

.SBTTL GLOBAL EQUATES SECTION

 ; THE GLOBAL EQUATES SECTION CONTAINS PROGRAM EQUATES THAT
 ; ARE USED IN MORE THAN ONE TEST.
 ;--

000020
 177777

NUMLNS==20 ;NUMBER OF LINES ON DHU11 IS 16.
 MAPLNS==177777 ;BIT MAP OF LINES ON DHU11.

000004
 000006
 000016

***** DEVICE REGISTER OFFSETS FROM THE CSR'S ADDRESS *****
 LPRO==4 ;LINE PARAMETER REGISTER OFFSET FROM THE CSR ADDRESS
 FLSO==6 ;FIFOSIZE/STATUS REGISTER OFFSET FROM THE CSR ADDRESS
 TXBFCO==16 ;TRANSMIT COUNT REGISTER OFFSET FROM THE CSR ADDRESS

EQUALS

; BIT DEFINITIONS

100000
 040000
 020000
 010000
 004000
 002000
 001000
 000400
 000200
 000100
 000040
 000020
 000010
 000004
 000002
 000001

 001000
 000400
 000200
 000100
 000040
 000020
 000010
 000004
 000002
 000001

BIT15== 100000
 BIT14== 40000
 BIT13== 20000
 BIT12== 10000
 BIT11== 4000
 BIT10== 2000
 BIT09== 1000
 BIT08== 400
 BIT07== 200
 BIT06== 100
 BIT05== 40
 BIT04== 20
 BIT03== 10
 BIT02== 4
 BIT01== 2
 BIT00== 1

 BIT9== BIT09
 BIT8== BIT08
 BIT7== BIT07
 BIT6== BIT06
 BIT5== BIT05
 BIT4== BIT04
 BIT3== BIT03
 BIT2== BIT02
 BIT1== BIT01
 BIT0== BIT00

; EVENT FLAG DEFINITIONS
 ; EF32:EF17 RESERVED FOR SUPERVISOR TO PROGRAM COMMUNICATION

000040
 000037
 000036

EF.START== 32. ; START COMMAND WAS ISSUED
 EF.RESTART== 31. ; RESTART COMMAND WAS ISSUED
 EF.CONTINUE== 30. ; CONTINUE COMMAND WAS ISSUED

000035
000034

EF.NEW== 29.
EF.PWR== 28.

; A NEW PASS HAS BEEN STARTED
; A POWER-FAIL/POWER-UP OCCURRED

;
;
; PRIORITY LEVEL DEFINITIONS

000340
000300
000240
000200
000140
000100
000040
000000

PRI07== 340
PRI06== 300
PRI05== 240
PRI04== 200
PRI03== 140
PRI02== 100
PRI01== 40
PRI00== 0

;
; OPERATOR FLAG BITS

000004
000010
000020
000040
000100
000200
000400
001000
002000
004000
010000
020000
040000
100000

EVL== 4
LOT== 10
ADR== 20
IDU== 40
ISR== 100
JAM== 200
BOE== 400
PNT== 1000
PRI== 2000
IXE== 4000
IBE== 10000
IER== 20000
LOE== 40000
HOE== 100000

1268

1270
 1278
 1279
 1280
 1281
 1282
 1283
 1284
 1285
 1286
 1287
 1288
 1289
 1290
 1291
 1292 002230 177777
 1293 002232 000300
 1294 002234 000304
 1295 002236 000000
 1296 002240 004
 1297
 1298
 1299
 1300
 1301
 1302 002242
 1303 002242 160020
 1304 002244 160022
 1305 002246 160024
 1306 002250 160026
 1307
 1308 002252 160030
 1309 002254 160032
 1310 002256 160034
 1311 002260 160036
 1312
 1313
 1314
 1315
 1316 002262 005464
 1317 002264 005470
 1318 002266 005475
 1319 002270 005501
 1320 002272 005517
 1321 002274 005526
 1322 002276 005537
 1323 002300 005550
 1324
 1325
 1326
 1327
 1328 002302 000000
 1329 002304 000000
 1330 002306 000000
 1331 002310 000000
 1332 002312 000000
 1333 002314 000000

.SBTTL GLOBAL DATA SECTION

 ; THE GLOBAL DATA SECTION CONTAINS DATA THAT ARE USED
 ; IN MORE THAN ONE TEST.
 ;--

 ; UNIT VARIABLE AREA

ACTLNS:: .WORD 177777 ;ACTIVE LINE BIT MAP.
 RXVECA:: .WORD 300 ;RX VECTOR ADDRESS.
 TXVECA:: .WORD 304 ;TX VECTOR ADDRESS.
 UNITN:: .WORD 0 ;UNIT NUMBER.
 BRLEVEL:: .BYTE 4 ;INTERRUPT BUS REQUEST LEVEL
 .EVEN

 ; DEVICE REGISTER ADDRESS TABLE

DRADRT::
 CSRA:: .WORD 160020 ;DHU-11 CSR ADDRESS.
 RXTMA:: RBUFA:: .WORD 160022 ;DHU-11 RECIEVE BUFFER/TIMER ADDRESS.
 LPRA:: .WORD 160024 ;DHU-11 LINE PARAMETER REGISTER ADDRESS.
 FDATA:: FLSA:: .WORD 160026 ;DHU-11 FIFO SIZE/LINE STATUS REGISTER ADDRESS,
 ;AND FIFO DATA REGISTER ADDRESS.
 LNCTRA:: .WORD 160030 ;DHU-11 LINE CONTROL REGISTER ADDRESS.
 TXAD1A:: .WORD 160032 ;DHU-11 TRANSMIT BUFFER 1 REGISTER ADDRESS
 TXAD2A:: .WORD 160034 ;DHU-11 TRANSMIT BUFFER 2 REGISTER ADDRESS
 TXBFCA:: .WORD 160036 ;DHU-11 TRANSMIT BUFFER COUNT REGISTER ADDRESS

 ; REGISTER MESSAGE ADDRESS TABLE

RMATBB:: .WORD DR00MG ;ADDRESS OF "CSR" MESSAGE.
 .WORD DR02MG ;ADDRESS OF "RBUF" MESSAGE.
 .WORD DR04MG ;ADDRESS OF "LPR" MESSAGE.
 .WORD DR06MG ;ADDRESS OF "STAT" MESSAGE.
 .WORD DR10MG ;ADDRESS OF "LNCTRL" MESSAGE.
 .WORD DR12MG ;ADDRESS OF "TBUFFAD1" MESSAGE.
 .WORD DR14MG ;ADDRESS OF "TBUFFAD2" MESSAGE.
 .WORD DR16MG ;ADDRESS OF "TBUFFCT" MESSAGE.

 ; ASSORTED GLOBAL VARIABLES:

BUFPTR:: .WORD 0 ;STORAGE FOR RECEIVE CHARACTER BUFFER POINTER.
 EXOERR:: .WORD 0 ;"EXIT ON ERROR" FLAG.
 CTRLCF:: .WORD 0 ;STORAGE FOR THE CONTROL-C FLAG.
 IESTAT:: .WORD 0 ;STORAGE FOR THE INTERRUPT ENABLE BIT STATES.
 PASCNT:: .WORD 0 ;STO'G FOR PASS COUNT USED IN ROM VERSION TEST.
 RXINTC:: .WORD 0 ;STORAGE FOR RECEIVER INTERRUPT FLAGS.

```

1334 002316 000000
1335 002320 000000
1336 002322 000000
1337 002324 000001
1338 002326 000000
1339 002330 000000
1340 002332 000000
1341
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1343
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1345 002334 177546
1346 002336 000300
1347 002340 000100
1348 002342 000074
1349 002344 000000
1350 002346 000000
1351 002350 000170
1352 002352 000170
1353 002354 000021
1354 002356 000062
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1359 002360 177572
1360 002362 000000
1361 002364 000000
1362 002366 172340
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1367 002370 137660
1368 002372 177777
1369 002374 000007
1370 002376 177777
1371 002400 166051
1372 002402 000000
1373 002404 177774
1374 002406 000000
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1379 002410 000001
1380 002412 000002
1381 002414 000004
1382 002416 000010
1383 002420 000020
1384 002422 000040
1385 002424 000100
1386 002426 000200
1387 002430 000400
1388 002432 001000
1389 002434 002000
1390 002436 004000
    
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RXINTF:: .WORD 0 ;STORAGE FOR RECEIVER INTERRUPT FLAGS.
TP4FLG:: .WORD 0 ;FLAGS SET WHEN AN EXPECTED 004 TRAP OCCURS.
TP4VEC:: .WORD 0 ;STORAGE FOR THE NORMAL 004 TRAP VECTOR.
TSTNUM:: .WORD 1 ;STORAGE FOR THE TEST NUMBER.
TXINTC:: .WORD 0 ;STORAGE FOR TRANSMIT INTERRUPT COUNT.
TXINTF:: .WORD 0 ;STORAGE FOR TRANSMIT INTERRUPT FLAGS.
WORD1:: .WORD 0 ;LOCATION FOR PASSING INDIRECT PARAMETERS.

;*****
; LINE TIME CLOCK VARIABLES AND STORAGE.
;*****
CLKCSR:: .WORD 177546 ;CSR ADDRESS OF THE LTC.
CLKBRL:: .WORD PRI06 ;INTERRUPT PRIORITY LEVEL OF THE LTC.
CLKVEC:: .WORD 100 ;INTERRUPT VECTOR ADDRESS OF THE LTC.
CLKHRZ:: .WORD 60. ;INTERRUPT FREQUENCY OF THE LTC.
TIMER1:: .WORD 0 ;HARDWARE CLOCK COUNTER #1.
TIMER2:: .WORD 0 ;HARDWARE CLOCK COUNTER #2.
TIMER3:: .WORD 120. ;HARDWARE BREAK COUNTER LOCATION.
BCOUNT:: .WORD 120. ;BREAK COUNT VALUE IN CLOCK TICKS.
MSTICK:: .WORD 17. ;NUMBER OF MILLI-SECONDS PER LTC TICK.
MSLCNT:: .WORD 62 ;LOOP COUNT (USED BY MSLOOP) TO DELAY 1 MS.

;*****
; MEMORY MANAGEMENT VARIABLES AND FLAGS.
;*****
MMSRO:: .WORD 177572 ;ADDRESS OF MEM MGT STATUS REGISTER #0.
MMPRES:: .WORD 0 ;MEM MGT PRESENT FLAG (0 IF MM NOT PRESENT).
MMENAB:: .WORD 0 ;MEM MGT ENABLED FLAG (0 IF MM NOT ENABLED).
PAROA:: .WORD 172340 ;ADDRESS OF MEM MGT PAR #0.

;*****
; BIT MASK TABLE OF UN-USED DHU DEVICE REGISTER BITS.
;*****
UNBTTB:: .WORD 137660 ;UNUSED BIT MASK FOR THE CSR
        .WORD 177777 ;UNUSED BIT MASK FOR THE RBUF/RXTIMER REG
        .WORD 7 ;UNUSED BIT MASK FOR THE LPR
        .WORD 177777 ;UNUSED BIT MASK FOR THE STAT/FIFOSIZE/DATA REG
        .WORD 166051 ;UNUSED BIT MASK FOR THE LNCTRL
        .WORD 0 ;UNUSED BIT MASK FOR THE TBUFFAD1
        .WORD 177774 ;UNUSED BIT MASK FOR THE TBUFFAD2
        .WORD 0 ;UNUSED BIT MASK FOR THE TBUFFCT

;*****
; TABLE OF WORDS WITH CORRESPONDING BIT SET FOR GENERATION OF BIT MAPS.
;*****
BITTBL:: .WORD 1 ;BIT 0 SET.
        .WORD 2 ;BIT 1 SET.
        .WORD 4 ;BIT 2 SET.
        .WORD 10 ;BIT 3 SET.
        .WORD 20 ;BIT 4 SET.
        .WORD 40 ;BIT 5 SET.
        .WORD 100 ;BIT 6 SET.
        .WORD 200 ;BIT 7 SET.
        .WORD 400 ;BIT 8 SET.
        .WORD 1000 ;BIT 9 SET.
        .WORD 2000 ;BIT 10 SET.
        .WORD 4000 ;BIT 11 SET.
    
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1391 002440 010000          .WORD 10000          ;BIT 12 SET.
1392 002442 020000          .WORD 20000          ;BIT 13 SET.
1393 002444 040000          .WORD 40000          ;BIT 14 SET.
1394 002446 100000          .WORD 100000         ;BIT 15 SET.
1395
1396
1397          ;*****
1398          ;*      GPR SAVE AREA ZERO.
1399          ;*****
1400 002450 000000 GPRS0B::          ;BASE OF GPR SAVE AREA NUMBER ZERO.
1401 002452 000000          .WORD 0              ;WORD 1, STORAGE FOR R1.
1402 002454 000000          .WORD 0              ;WORD 2, STORAGE FOR R2.
1403 002456 000000          .WORD 0              ;WORD 3, STORAGE FOR R3.
1404 002460 000000          .WORD 0              ;WORD 4, STORAGE FOR R4.
1405          .WORD 0              ;WORD 5, STORAGE FOR R5.
1406
1407          ;*****
1408          ;*      TRANSMISSION AND RECEPTION VARIABLES, POINTERS, AND FLAGS.
1409          ;*****
1409 002462 000000          ERSMRF:: .WORD 0      ;ERROR SUMMARY REPORT FLAGS.
1410 002464          ERCNTB:: .BLKW 16.      ;TABLE OF ERROR COUNTERS.
1411
1412          ;*****
1413          ;      STORAGE AREA FOR THE BMP CODE QUEUE.
1414          ;*****
1415 002524 000000          BMPCQP:: .WORD 0      ;POINTER USED TO ACCESS THE NEXT CELL IN QUE.
1416 002526          BMPCQB:: .BLKW 64.      ;STORAGE FOR 32 CELLS, TEST# PLUS BMP CODE.
1417 002726          BMPCQE::          ;LAST ADDRESS PLUS 2 OF THE BMP CODE QUEUE.
1418
1419          ;*****
1420          ;      GENERAL TABLE AND BUFFER AREA--513 WORDS.
1421          ;*****
1422 002726          BUFBAS::          ;BASE OF MEMORY BUFFER.
1423 002726          ERLTBL::          .BLKW 128.      ;FIRST HALF OF GENERAL TABLE OR BUFFER.
1424 003326          BUFMID::          .BLKW 64.      ;SECOND HALF OF GENERAL TABLE OR BUFFER.
1425 003526          BUF3QT::          .BLKW 64.      ;LAST QUARTER OF THE BUFFER AREA.
1426 003726          BUFEND::          ;END OF GENERAL PURPOSE MEMORY BUFFER.
1427 003726          ENDETBL::          .BLKW 16.      ;BUFFER OVERFLOW SPACE.
1428
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1443          ERRTBL
1443 003766          ERRTP::          .WORD 0
1443 003766 000000          ERRNBR::          .WORD 0
1443 003770 000000          ERRMSG::          .WORD 0
1443 003772 000000          ERRBLK::          .WORD 0
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1445          .EVEN

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L\$ERRTBL::

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.SBTTL GPR HANDLING ROUTINES FOR SUBROUTINE CALLS.
;*****
;*
;* THERE ARE 4 ROUTINES AND MACRO DEFINITIONS USED FOR THE HANDLING OF
;* GPR VALUES DURING SUBROUTINE CALLS WITHIN THIS PROGRAM. THE FOUR
;* ROUTINES/MACRO CALLS HAVE THE FOLLOWING NAMES:
;*
;* SAVE - MACRO DEFINITION USED AT THE BEGINNING OF A SUBROUTINE TO
;* SAVE THE GPR CONTENTS FOR LATER RESTORATION.
;* PASS - MACRO DEFINITION USED AT THE END OF A SUBROUTINE TO RESTORE
;* THE PREVIOUSLY SAVED GPR CONTENTS AND TO LEAVE THE CONTENTS
;* OF THE SPECIFIED GPR(S) INTACT (NOT RESTORED).
;* PREG05 - SUBROUTINE WHICH IS CALLED FROM THE SAVE AND PASS MACRO
;* EXPANSIONS WHICH ACTUALLY PERFORMS THE ACTIONS ON THE GPRS.
;*
;* DURING A SUBROUTINE WHICH USES THESE GPR SAVE ROUTINES THE VALUES
;* OF THE GPRS ARE STORED ON THE STACK IN THE FOLLOWING STACK FRAME:
;*
;*      SP      -> RET PC INTO PREG05 ROUTINE.
;*      SP+2    -> GPR R0 CONTENTS.
;*      SP+4    -> GPR R1 CONTENTS.
;*      SP+6    -> GPR R2 CONTENTS.
;*      SP+8    -> GPR R3 CONTENTS.
;*      SP+10   -> GPR R4 CONTENTS.
;*      SP+12   -> GPR R5 CONTENTS.
;*      SP+14   -> RET PC INTO CALLER OF SUB'TNE WHICH CALLED PREG05.
;*
;* EACH LEVEL OF SUB'TNE CALLING USES 8 WORDS OF STACK OVERHEAD.
;* THE SAVE AND PASS MACROS CAN ALSO BE USED IN "STRAIGHT LINE CODE"
;* TO SAVE AND RESTORE THE GPR VALUES. IN ANY CASE, AFTER THE
;* ISSUING OF A PASS CALL THE GPRS WILL BE RESTORED TO THE VALUES
;* THEY HAD PRIOR TO THE LAST SAVE CALL (EXCEPT FOR THE EXCEPTED,
;* OR PASSED INTACT, GPRS SPECIFIED AS PARAMETERS TO THE PASS CALL)
;* AND THE SP WILL ALSO BE RESTORED TO ITS CONDITION BEFORE THE LAST
;* SAVE CALL. THE PROGRAMMER MUST BE SURE THAT THE SP HAS THE SAME
;* VALUE WHEN THE PASS MACRO IS CALLED AS IT HAD IMMEDIATELY AFTER
;* THE SAVE MACRO WAS CALLED.
;*****
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.SBTTL GPR FRAME ACCESS EQUATES

;***
;EQUATES THAT ALLOW ACCESS TO THE STACK FRAME. THESE ARE THE
;OFFSETS INTO THE STACK FOR REGISTERS SAVED DURING THE PREG05
;ROUTINE.
;---

LPCSLT== 36 ;OFFSET FOR LAST RETURN PC.
PCSLT== 16 ;OFFSET FOR RETURN PC.
R5SLOT== 14 ;OFFSET FOR R5.
R4SLOT== 12 ;OFFSET FOR R4.
R3SLOT== 10 ;OFFSET FOR R3.
R2SLOT== 6 ;OFFSET FOR R2.
R1SLOT== 4 ;OFFSET FOR R1.
R0SLOT== 2 ;OFFSET FOR R0.

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.SBTTL GLOBAL MACRO DEFINITION - SAVE -  
:*****  
:* THIS MACRO IS USED AT THE BEGINNING OF A SUBROUTINE TO SAVE THE  
:* CONTENTS OF THE GPRS R0 THRU R5.  
:*  
:* INPUTS: SP - UNCHANGED SINCE SUBROUTINE WAS ENTERED  
:* R5SLOT - OFFSET TO STACK SLOT FOR R5 (EQUATED TO 14 OCTAL)  
:*  
:* OUTPUTS: GPR SAVE AREA ON THE STACK IS LOADED WITH THE CONTENTS OF GPRS  
:* TOP OF STACK - LOADED WITH THE RETURN ADDRESS INTO PREG05  
:*  
:* CALLING SEQUENCE: SAVE  
:*  
:* COMMENTS: NO ARGUMENTS ARE ALLOWED.  
:* THE PASS MACRO SHOULD BE CALLED TO RESTORE THE GPR VALUES.  
:*  
:* SUBORDINATE ROUTINES CALLED: PREG05.  
:*****  
  
.MACRO SAVE  
.LIST JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.  
.NLIST  
.ENDM SAVE
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.SBTTL GLOBAL MACRO DEFINITION - PASS -
:*****
:* THIS MACRO IS USED IN CONJUNCTION WITH THE SAVE MACRO. IT IS
:* CALLED AT END OF A SUBROUTINE TO PASS PARAMETERS IN GPRS BACK TO THE
:* CALLING ROUTINE BY ALTERING THE GPR SAVE AREA ON THE STACK AND THEN
:* RETURNING TO PREG05 TO RESTORE THE GPRS TO THEIR SAVED VALUES.
:*
:* INPUTS: ONLY ALLOWED ARGUMENTS ARE "R0" THRU "R5".
:* ROSLOT THRU R5SLOT MUST BE EQUATED TO THEIR RESPECTIVE GPR SAVE
:* SLOT OFFSETS BEFORE CALLING THIS MACRO.
:*
:* OUTPUTS: THE GPR VALUES ARE PUT IN THEIR RESPECTIVE SLOTS ON THE STACK.
:*
:* CALLING SEQUENCE: PASS R0,R1,...
:*
:* COMMENTS: ANY COMBINATION OF GPR ARGUMENTS MAY BE LISTED IN ANY ORDER.
:* FOR EXAMPLE, THE FOLLOWING ARE LEGAL:
:* PASS R1
:* PASS R4,R0,R2
:* THE GPRS LISTED AS ARGUMENTS WILL BE PASSED INTACT TO THE
:* CALLING ROUTINE, ALL OTHER GPRS WILL BE RESTORED.
:* THE SP MUST BE AT ITS ORIGINAL VALUE WHEN PASS IS CALLED.
:*
:* THE MACRO CALL
:* PASS R0,R3
:* EXPANDS INTO THE FOLLOWING ASSEMBLY CODE:
:* MOV R0,ROSLOT(SP) ;PUT R0 IN STACK SLOT.
:* MOV R3,R3SLOT(SP) ;PUT R3 IN STACK SLOT.
:* JSR PC,0(SP)+ ;RETURN TO PREG05 SUBRT.
:* IN THIS EXAMPLE GPRS R1, R2, R4, AND R5 WILL BE RESTORED TO
:* THEIR VALUES CONTAINED IN THE STACK FRAME AND R0 AND R3
:* WILL BE LEFT AT THEIR VALUES PRIOR TO THIS PASS CALL.
:*
:* SUBORDINATE ROUTINES CALLED: (PREGRT - LABEL WITHIN PREG05, VALUE ON STACK.)
:*****
.*MACRO PASS A,B,C,D,E,F
.*IRP X,<A,B,C,D,E,F>
.*IF NB,X
.*LIST
MOV X,X'SLOT(SP) ;PUT X IN STACK SLOT.
.*NLIST
.*ENDC
.*ENDM
.*LIST
JSR PC,0(SP)+ ;RETURN TO PREG05 SUBRT.
.*NLIST
.*ENDM PASS
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1600 003776
1601 003776 010446
1602 004000 010346
1603 004002 010246
1604 004004 010146
1605 004006 010046
1606 004010 010546
1607 004012 016605 000014
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1609 004016 004736
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1619 004020 012605
1620 004022 012600
1621 004024 012601
1622 004026 012602
1623 004030 012603
1624 004032 012604
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1626 004034 000205
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```
.SBTTL GLOBAL SUBROUTINE - PREG05 -
;*****
;* PRESERVE REGISTERS R0 THROUGH R5 FOR SUBROUTINE CALLS.
;*
;* INPUTS: THE RETURN ADDRESS BACK INTO THE CALLING ROUTINE MUST BE IN
;* GPR R5. (I.E.- MACROS USE "JSR R5,PREG05".)
;*
;* OUTPUTS: REGISTERS R0 THROUGH R5 ARE SAVED ON THE STACK.
;*
;* CALLING SEQUENCE: SAVE ;MACRO EXPANSION CALLS PREG05.
;* [SUBROUTINE CODE]...
;* PASS ;MACRO EXPANSION RECALLS PREG05.
;*
;* COMMENTS: THIS ROUTINE IS RE-ENTRANT.
;*
;* PARAMETERS MAY BE PASSED OUT OF A SUBROUTINE BY MODIFYING THE
;* REGISTER SAVE AREA ON THE STACK. USE THE PASS GPRN MACRO
;* TO RETURN GPR VALUES INTACT.
;* USE THE RNSLOT OFFSETS FROM THE SP TO PASS OTHER PARAMETERS.
;* [EXAMPLE: MOV VALUE,R0SLOT(SP) ]
;* MAKE SURE THE SP IS AT ITS ORIGINAL VALUE WHEN YOU DO THIS.
;*
;* SUBORDINATE ROUTINES CALLED: NONE.
;*****

PREG05: ;R5 HAS BEEN LOADED ON THE STACK BY THE SUBROUTINE CALL
MOV R4,-(SP) ;SAVE R4
MOV R3,-(SP) ;SAVE R3
MOV R2,-(SP) ;SAVE R2
MOV R1,-(SP) ;SAVE R1
MOV R0,-(SP) ;SAVE R0
MOV R5,-(SP) ;PUSH RETURN PC ON TOP OF STACK
MOV R5SLOT(SP),R5 ;RESTORE R5 TO VALUE IT HAD BEFORE CALLS

JSR PC,@(SP)+ ;CALL THE SUBROUTINE AT THE RETURN ADDRESS
;FROM THE PREG05 CALL, PUTTING THE PRESENT
;PC ON THE STACK AS A RETURN ADDRESS INTO
;THIS (PREG05) ROUTINE.

;+++
;THE FOLLOWING CODE IS EXECUTED WHEN THE CALLING ROUTINE DOES A
;"RETURN" [JSR PC,@(SP)+] USING THE PC DEPOSITED ON THE STACK ABOVE.
;---

PREGRT:: MOV (SP)+,R5 ;PUT RETURN PC IN R5.
MOV (SP)+,R0 ;RESTORE R0.
MOV (SP)+,R1 ;RESTORE R1.
MOV (SP)+,R2 ;RESTORE R2.
MOV (SP)+,R3 ;RESTORE R3.
MOV (SP)+,R4 ;RESTORE R4.

RTS R5 ;RETURN TO THE SUBROUTINE WHICH CALLED PREG05.
;RESTORING R5 IN THE PROCESS.
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004036
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004041
004044

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055
000

110
061

125
061

.SBTTL GLOBAL TEXT SECTION

; THE GLOBAL TEXT SECTION CONTAINS FORMAT STATEMENTS,
; MESSAGES, AND ASCII INFORMATION THAT ARE USED IN
; MORE THAN ONE TEST.

; NAMES OF DEVICES SUPPORTED BY PROGRAM

DEVTYP <DMU-11>

L\$DVTYP::
.ASCIZ /DMU-11/

.EVEN

1649
1655
1656
1657
1658

004046
004046
004046

104

110

125

; TEST DESCRIPTION

DESCRIPT <DMU-11 FUNC TST PART1>

L\$DESC::
.ASCIZ /DMU-11 FUNC TST PAR

T1/

004051
004054
004057
004062
004065
004070
004073

055
040
116
124
040
122
000

061
106
103
123
120
124

061
125
040
124
101
061

.EVEN

.EVEN

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1676
1677      .NLIST BIN
1678
1679      ; ***** FORMAT STATEMENTS USED IN PRINT CALLS *****
1680
1681 004074 EF0503:: .ASCIZ /#T#N/
1682 004101 EF0505:: .ASCIZ /#A      #D5#A ILLEGAL INTERRUPTS RECEIVED.#N/
1683 004154 EF1401:: .ASCIZ /#N#A ROM VERSION NUMBERS: PROC_1 = #D2#A(D)  PROC_2 = #D2#A(D)#N/
1684 004256 EF1402:: .ASCIZ /#T#A ROM VERSION NUMBER #T#N/
1685 004313 EF1601:: .ASCIZ /#A      #T#A, TEST ABORTED #N/
1686 004345 EF1602:: .ASCIZ /#A      EXPECTED DATA: #06#A (0).#N/
1687 004407 EF1603:: .ASCIZ /#A      ACTUAL DATA: #06#A (0).#N/
1688 004451 EF1604:: .ASCIZ /#A      BAD BIT(S) IN DEVICE #T#A REGISTER FOR LINE #D2#A (D).#N/
1689 004546 EF3001:: .ASCIZ /#A      EXPECTED OR CORRECT VALUE: #03#N/
1690 004615 EF3002:: .ASCIZ /#A      ACTUAL OR MEASURED VALUE: #03#N/
1691 004664 EF9006:: .ASCIZ /#A      #T#A #D2#A(D)#N/
1692 004710 EF9010:: .ASCIZ /#A      NUMBER OF ERRORS DETECTED ON LINE #D2#A IS #D5#N/
1693 004777 EF9016:: .ASCIZ /#A      UNEXPECTED #T#A FOR LINE #D2#A(D) IN FIFO AFTER RESET:#N/
1694 005074 EF9017:: .ASCIZ /#A      #T#A (WITH ERROR FLAGS) IS #06#A(0)#N/
1695 005150 EF9018:: .ASCII /#A      #T#A IN SELFTEST CODE FIFO SLOT FOR LINE #D2/
1696 005230      .ASCIZ /#A(D) AFTER RESET.#N/
1697 005255 EF9301:: .ASCIZ /#A      #T#D2#A(D), BMP CODE REPORTED :#03#A(0)#N/
1698 005333 EF9302:: .ASCIZ /#A      OVERFLOW OCCURRED (MORE THAN 31 BMP CODES FOUND IN QUEUE)#N/
1699 005433 MFUNIT:: .ASCIZ /#N#A TESTING UNIT :#D4#N/
1700      .EVEN
1701      .LIST BIN

```



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1710
1711      .NLIST BIN
1712
1713
1714      ;***** GLOBAL ERROR MESSAGES *****
1715
1716 005464 DR00MG:: .ASCIZ /CSR/
1717 005470 DR02MG:: .ASCIZ /RBUF/
1718 005475 DR04MG:: .ASCIZ /LPR/
1719 005501 DR06MG:: .ASCIZ /FIFOSIZE,STAT/
1720 005517 DR10MG:: .ASCIZ /LNCTRL/
1721 005526 DR12MG:: .ASCIZ /TBUFFAD1/
1722 005537 DR14MG:: .ASCIZ /TBUFFAD2/
1723 005550 DR16MG:: .ASCIZ /TBUFFCT/
1724 005560 EM0103:: .ASCIZ /DEVICE REGISTER ACCESS ERRORS/
1725 005616 EM0201:: .ASCIZ / MASTER RESET TEST FAILED./
1726 005651 EM0202:: .ASCIZ / MASTER RESET BIT DID NOT CLEAR AFTER BOARD RESET./
1727 005735      .ASCIZ / WAITED 5 SECONDS. BIT DEFECTIVE OR FIRMWARE HUNG./
1728 006024 EM0203:: .ASCIZ / MASTER RESET BIT CLEAR IMMEDIATELY AFTER BOARD RESET./
1729 006114      .ASCIZ / BIT DEFECTIVE OR BOARD FIRMWARE ERROR./
1730 006167 EM0204:: .ASCIZ \ MR BIT WENT CLEAR WITHIN 1/2 SECOND OF BOARD RESET.\
1731 006255      .ASCIZ / BIT DEFECTIVE OR SELFTEST WAS (INCORRECTLY) SKIPPED./
1732 006346 EM0301:: .ASCIZ /MASTER RESET (SKIP SELFTEST) TEST FAILED./
1733 006420 EM0302:: .ASCIZ / MR BIT CLR WITHIN 10 MILLISECOND AFTER BOARD RESET./
1734 006505      .ASCIZ / BIT DEFECTIVE OR BOARD FIRMWARE ERROR./
1735 006560 EM0303:: .ASCIZ \ MR BIT WENT CLEAR 1/5 TO 5 SECONDS AFTER RESET.\
1736 006642      .ASCIZ / SELFTEST DID NOT GET SKIPPED (SHOULD HAVE BEEN SKIPPED)./
1737 006737 EM0401:: .ASCIZ /RBUF REGISTER RX CHARACTER FIELD TEST FAILED./
1738 007015 EM0402:: .ASCIZ / IMPROPER CODE FOUND IN RX FIFO AFTER DUT RESET./
1739 007077      .ASCIZ / EXPECTED: SELFTEST CODE. ACTUAL: IMPROPER CODE./
1740 007165 EM0501:: .ASCIZ /RBUF REGISTER ERROR FLAGS FIELD TEST FAILED/
1741 007241 EM0502:: .ASCIZ / RX ERROR FLAG(S) FOUND CLEAR ON SELFTEST CODE./
1742 007322      .ASCIZ / EXPECTED: ALL ERROR FLAGS SET, ACTUAL: FLAG(S) CLEAR./
1743 007415 EM0525:: .ASCIZ / RX INTERRUPT(S) RECEIVED WITH RX INTERRUPTS DISABLED./
1744 007505 EM0526:: .ASCIZ / TX INTERRUPT(S) RECEIVED WITH TX INTERRUPTS DISABLED./
1745 007575 EM0601:: .ASCIZ /CSR RX.DATA.AVAIL BIT TEST FAILED/
1746 007637 EM0602:: .ASCIZ / RX.DATA.AVAIL BIT FOUND CLEAR AFTER RESET COMPLETION./
1747 007727      .ASCIZ / EXPECTED BIT TO BE SET FROM SELFTEST CODES IN FIFO./
1748 010017 EM0603:: .ASCIZ / RX.DATA.AVAIL BIT COULD NOT BE CLEARED BY PURGING FIFO./
1749 010111      .ASCIZ / 600 CHARS READ FROM FIFO WITHOUT R.D.A BIT CLEARING./
1750 010202 EM0701:: .ASCIZ /RBUF RX.DATA.VALID BIT TEST FAILED/
1751 010245 EM0702:: .ASCIZ / RX.DATA.VALID BIT FOUND CLEAR AFTER RESET COMPLETION./
1752 010335      .ASCIZ / EXPECTED BIT TO BE SET FROM SELFTEST CODES IN FIFO./
1753 010425 EM0703:: .ASCIZ / RX.DATA.VALID BIT COULD NOT BE CLEARED BY PURGING FIFO./
1754 010517      .ASCIZ / 600 CHARS READ FROM FIFO WITHOUT R.D.V BIT CLEARING./
1755 010610 EM0801:: .ASCIZ /RBUF RX.LINE.NUMBER FIELD TEST FAILED/
1756 010656 EM0802:: .ASCIZ / LINE NUMBER WRONG ON A SELFTEST CODE./
1757 010726 EM0901:: .ASCIZ /CHECK FOR BMP CODES TEST FAILED/
1758 010766 EM0902:: .ASCIZ /UNEXPECTED BMP CODES FOUND./
1759 011022 EM1001:: .ASCIZ /SKIP SELF-TEST TEST FAILED/
1760 011055 EM1002:: .ASCIZ / SKIP SELF-TEST TOOK TOO LONG TO COMPLETE, > 50 MS./
1761 011142 EM1003:: .ASCIZ / SKIP SELF-TEST COMPLETED TOO SOON, < 10 MS./
1762 011220 EM1101:: .ASCIZ /DIAGNOSTIC FAIL (SKP SELFTEST) TEST FAILED/
1763 011273 EM1201:: .ASCIZ /SELF-TEST TEST FAILED/
1764 011321 EM1202:: .ASCIZ / SELF-TEST TOOK TOO LONG TO COMPLETE, > 5 SECONDS./
1765 011405 EM1203:: .ASCIZ \ SELF-TEST COMPLETED TOO SOON, < 1/2 SECOND.\
1766 011463 EM1204:: .ASCIZ / SELF-TEST DID NOT EXECUTE/

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1767 011517 EM1205:: .ASCIZ / DIAG FAIL BIT BAD/
1768 011543 EM1301:: .ASCIZ /FAIL SELF-TEST TEST FAILED/
1769 011576 EM1302:: .ASCIZ / SELF-TEST ERROR REPORTING BAD/
1770 011635 EM1401:: .ASCIZ /ROM VERSION_NUMBER TEST FAILED/
1771 011674 EM1402:: .ASCIZ / FIFO EMPTY, ONE OR MORE ROM VERSION_NUMBERS MISSING/
1772 011762 EM1403:: .ASCIZ / ROM VERSION_NUMBER FOUND OUT OF SEQUENCE/
1773 012035 EM1404:: .ASCIZ / ONE OR MORE ROM VERSION_NUMBERS MISSING/
1774 012107 EM1405:: .ASCIZ / PROC_1/
1775 012122 EM1406:: .ASCIZ / PROC_2/
1776 012135 EM1407:: .ASCIZ /NOT FOUND/
1777 012147 EM1408:: .ASCIZ /FOUND/
1778 012155 EM1501:: .ASCIZ /CSR BIT 4 TEST FAILED/
1779 012203 EM1502:: .ASCIZ /CSR BIT 4 BAD/
1780 012221 EM1601:: .ASCIZ /TIMEOUT OCCURRED WAITING FOR MASTER RESET TO CLEAR/
1781 012304 EM1604:: .ASCIZ \DEVICE REGISTER WORD READ/WRITE TEST FAILED\
1782 012360 EM1701:: .ASCIZ \DEVICE REGISTER WORD READ/MODIFY/WRITE TEST FAILED\
1783 012443 EM1801:: .ASCIZ \DEVICE REGISTER BYTE READ/WRITE TEST FAILED\
1784 012517 EM1901:: .ASCIZ \DEVICE REGISTER BYTE READ/MODIFY/WRITE TEST FAILED\
1785 012602 EM2001:: .ASCIZ /DEVICE STAT REGISTER ID BIT TEST FAILED/
1786 012652 EM2002:: .ASCIZ /ID BIT BAD. EXPECTED: SET, ACTUAL: CLEAR./
1787 012725 EM2301:: .ASCIZ /TX_ENABLE (INACTIVE) BIT TEST FAILED/
1788 012772 EM2302:: .ASCIZ / TX_ENABLE BIT BAD ON LINE: /
1789 013030 EM2401:: .ASCIZ /TX_ENABLE (ACTIVE) BIT TEST FAILED/
1790 013073 EM2501:: .ASCIZ /RECEIVE INTERRUPT TEST FAILED/
1791 013131 EM2602:: .ASCIZ / NO RX INT GENERATED (DATA_VALID SET, RX INTS ENABLED)./
1792 013222 EM2603:: .ASCIZ / NO RX INT GENERATED (NO CODES IN FIFO AFTER RESET)./
1793 013310 EM2604:: .ASCIZ / NO RX INT GENERATED (RX_DATA_AVAIL CLR, RX INTS ENABLED)./
1794 013404 EM2605:: .ASCIZ / RX INTERRUPT GENERATED WITH RX_DATA_AVAIL CLEAR./
1795 013467 EM2606:: .ASCIZ /TRANSMIT INTERRUPT TEST ERROR:/
1796 013526 EM2607:: .ASCIZ / TX_ACTION SET REPEATEDLY AFTER BOARD RESET, NO DATA SENT./
1797 013622 EM2608:: .ASCIZ / TX_ACTION STUCK SET AFTER BOARD RESET./
1798 013673 EM2609:: .ASCIZ / TX INTERRUPT GENERATED WITH TX_ACTION CLEAR./
1799 013752 EM2610:: .ASCIZ / NO TX INTERRUPT WITH TX_ACTION SET AND TX INTS ENABLED./
1800 014044 EM2611:: .ASCIZ / TX_ACTION NOT SET AFTER CHARS SENT ON ALL LINES./
1801 014127 EM2612:: .ASCIZ / NO RX INT GENERATED (RX_DATA_AVAIL SET, RX INTS ENABLED)./
1802 014223 EM3001:: .ASCIZ /INTERRUPT BR LEVEL TEST FAILED/
1803 014262 EM3002:: .ASCIZ / NO RX_DATA_AVAIL FROM SELFTEST CODES IN FIFO AFTER RESET./
1804 014356 EM3003:: .ASCIZ / TX INTERRUPT GENERATED AT WRONG BR LEVEL:/
1805 014432 EM3004:: .ASCIZ / RX INTERRUPT GENERATED AT WRONG BR LEVEL:/
1806 014506 EM3005:: .ASCIZ / TX INTERRUPT GIVEN PRECEDENCE OVER SIMULTANEOUS RX INT./
1807 014600 EM3101:: .ASCIZ /DIAGNOSTIC FIELD (BMP) TEST FAILED/
1808 014643 EM3102:: .ASCIZ / DIAGNOSTIC FIELD (BMP REQUEST) BAD ON LINE: /
1809 014722 EM9014:: .ASCIZ /SUMMARY REPORTS FOR LINES WITH EXCESSIVE NUMBERS OF ERRORS:/
1810 015016 EM9017:: .ASCII / FIFO WILL NOT PURGE (DATA_VALID STUCK SET)./
1811 015073 .ASCIZ / REMAINDER OF TEST SKIPPED./
1812 015127 EM9018:: .ASCIZ /NO CODE/
1813 015137 EM9019:: .ASCIZ /NON-SELFTEST/
1814 015154 EM9020:: .ASCIZ /SELFTEST ERROR CODE/
1815 015200 EM9022:: .ASCIZ /DATA CHARACTER/
1816 015217 EM9023:: .ASCIZ /MODEM STATUS CODE/
1817 015241 EM9024:: .ASCIZ /SELFTEST CODE/
1818 015257 EM9301:: .ASCIZ /BMP CODES WERE REPORTED DURING THIS DIAGNOSTIC/
1819 015336 EM9302:: .ASCIZ /BMP CODE FOUND IN TEST /
1820 015366 EM9303:: .ASCIZ /THE LAST BMP CODE WAS FOUND IN TEST /
1821 015433 EM9304:: .ASCIZ /UNEXPECTED BMP CODES FOUND DURING THIS PASS/
1822 .EVEN
1823 .LIST BIN

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.SBTTL GLOBAL ERROR REPORT SECTION

;++
; THE GLOBAL ERROR REPORT SECTION CONTAINS MESSAGE PRINTING AREAS
; USED BY MORE THAN ONE TEST TO OUTPUT ADDITIONAL ERROR INFORMATION. PRINTB
; (BASIC) AND PRINTX (EXTENDED) CALLS ARE USED TO CALL PRINT SERVICES.
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1865 015510
015510
1866 015510
015510 004567 166262
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1868 015514 012700 000100
1869 015520 046700 164500
1870 015524 001036
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1875 015526 032705 000001
1876 015532 001410
1877 015534
015534 012746 015626
015540 012746 000001
015544 010600
015546 104414
015550 062706 000004
1878 015554 032705 000002
1879 015560 001410
1880 015562
015562 012746 015704
015566 012746 000001
015572 010600
015574 104414
015576 062706 000004
1881 015602
015602 012746 015763
015606 012746 000001
015612 010600
015614 104415
015616 062706 000004

```
.SBTTL GLOBAL ERROR REPORTING ROUTINE - ERO101 -
;*****
;* THIS IS AN ERROR REPORTING SUBROUTINE WHICH PRINTS ADDITIONAL ERROR
;* INFORMATION IF AN ERROR IS DETECTED IN TEST 1 (REGISTER ADDRESS
;* ACCESS TEST). IF THE "EXTENDED ERROR INFO" OPTION HAS BEEN SELECTED
;* THEN THIS SUBROUTINE WILL REPORT THE TYPE OF ACCESS (READ OR WRITE OR
;* BOTH) WHICH CAUSED A BUS TIME-OUT TRAP (004 TRAP). A MESSAGE INDICATING
;* THAT THE DHU MAY BE AT THE WRONG UNIBUS ADDRESS IS ALSO PRINTED.
;*
;* INPUTS:      R5 - ERROR FLAG WORD.
;*              IF BIT 0 IS SET, A READ ERROR OCCURED.
;*              IF BIT 1 IS SET, A WRITE ERROR OCCURED.
;*
;* OUTPUTS:     MESSAGES ARE PRINTED AT THE OPERATOR CONSOLE.
;*
;* CALLING SEQUENCE:  INCLUDE THE LABEL "ERO101" AS THE MESSAGE POINTER
;*                    PARAMETER IN THE DRS ERROR REPORT MACRO CALL.
;*
;* COMMENTS:
;*
;* SUBORDINATE ROUTINES USED: NONE.
;*****
BGNMSG ERO101
SAVE                                ERO101::
                                JSR      R5,PREG05 ;SAVE THE GPR CONTENTS.
                                ;CALL REGISTER SAVE SUBRT.
MOV      #BIT06,R0                ;SET-UP THE BIT MAP FOR 'REPORT EXT'D ERROR INFO'
BIC      OPTION,R0                ;TRY AND CLEAR THE FLAG.
BNE      6$                        ;EXIT IF OPTION NOT SELECTED.
;+
; REPORT EXTENDED ERROR INFOMATION
;-
                                BIT      #BIT0,R5 ;TEST FOR READ ERROR.
                                BEQ      2$ ;SKIP READ ERROR MSG IF NO READ ERROR.
                                PRINTB   #MSG1 ;PRINT READ ERROR MESSAGE.
                                MOV      #MSG1,-(SP)
                                MOV      #1,-(SP)
                                MOV      SP,R0
                                TRAP     C$PNTB
                                ADD      #4,SP
2$: BIT      #BIT1,R5                ;TEST FOR WRITE ERROR.
                                BEQ      4$ ;SKIP WRITE ERROR MSG IF NO WRITE ERROR.
                                PRINTB   #MSG2 ;PRINT WRITE ERROR MESSAGE.
                                MOV      #MSG2,-(SP)
                                MOV      #1,-(SP)
                                MOV      SP,R0
                                TRAP     C$PNTB
                                ADD      #4,SP
4$: PRINTX  #MSG3                    ;SUGGEST THAT DHU MAY BE AT WRONG ADDRESS.
                                MOV      #MSG3,-(SP)
                                MOV      #1,-(SP)
                                MOV      SP,R0
                                TRAP     C$PNTX
                                ADD      #4,SP
```



```

1882 015622          6$: PASS          ;RESTORE THE GPR CONTENTS.
      015622 004736          JSR          PC,@(SP)+          ;RETURN TO PREG05 SUBRT.
1883 015624          ENDMSG
      015624          L10002: TRAP C$MSG
      015624 104423
1884
1885 015626          045          101          102 MSG1:: .ASCIZ /%ABUS TIME-OUT TRAP CAUSED BY READ ATTEMPT.%N/
      015631          125          123          040
      015634          124          111          115
      015637          105          055          117
      015642          125          124          040
      015645          124          122          101
      015650          120          040          103
      015653          101          125          123
      015656          105          104          040
      015661          102          131          040
      015664          122          105          101
      015667          104          040          101
      015672          124          124          105
      015675          115          120          124
      015700          056          045          116
      015703          000
1886 015704          045          101          102 MSG2:: .ASCIZ /%ABUS TIME-OUT TRAP CAUSED BY WRITE ATTEMPT.%N/
      015707          125          123          040
      015712          124          111          115
      015715          105          055          117
      015720          125          124          040
      015723          124          122          101
      015726          120          040          103
      015731          101          125          123
      015734          105          104          040
      015737          102          131          040
      015742          127          122          111
      015745          124          105          040
      015750          101          124          124
      015753          105          115          120
      015756          124          056          045
      015761          116          000
1887 015763          045          101          104 MSG3:: .ASCIZ /%ADHU MAY BE AT THE WRONG UNIBUS ADDRESS.%N%N/
      015766          110          125          040
      015771          115          101          131
      015774          040          102          105
      015777          040          101          124
      016002          040          124          110
      016005          105          040          127
      016010          122          117          116
      016013          107          040          125
      016016          116          111          102
      016021          125          123          040
      016024          101          104          104
      016027          122          105          123
      016032          123          056          045
      016035          116          045          116
      016040          000

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.EVEN

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016042 004567 165730
016046 012700 000100
016052 046700 164146
016056 001025
016060 010102
016062 105722
016064 001376
016066
016066 010146
016070 012746 004074
016074 012746 000002
016100 010600
016102 104414
016104 062706 000006
016110
016110 010246
016112 012746 004074
016116 012746 000002
016122 010600
016124 104414
016126 062706 000006
016132
016132 004736
016134
016134
016134 104423

```
.SBTTL GLOBAL ERROR REPORTING ROUTINE - ERO201 -
;*****
;* THIS IS AN ERROR REPORTING SUBROUTINE WHICH PRINTS 2 CONTIGUOUS
;* ASCII ERROR MESSAGES. THE ADDRESS OF THE FIRST MESSAGE IS PASSED
;* AS AN INPUT PARAMETER AND THE ADDRESS OF THE SECOND IS FOUND BY
;* SEARCHING FOR THE END OF THE FIRST MESSAGE. THE MESSAGES ARE ONLY
;* PRINTED IF EXT'D ERROR REPORTING HAS BEEN REQUESTED.
;*
;* INPUTS: R1 - ADDRESS OF THE FIRST MESSAGE TO PRINT.
;*
;* OUTPUTS: A MESSAGES IS PRINTED AT THE OPERATOR CONSOLE.
;*
;* CALLING SEQUENCE: LOAD THE ADDRESS OF THE FIRST MESSAGE IN R1.
;* INCLUDE THE LABEL "ERO201" AS THE MESSAGE POINTER
;* PARAMETER IN THE DIAG SUPER ERROR REPORT MACRO CALL.
;*
;* COMMENTS: THE MESSAGE IS PRINTED AS BASIC ERROR INFORMATION.
;* THE SECOND MESSAGE SHOULD FOLLOW THE FIRST ONE IN THE PROGRAM
;* MEMORY. EACH MESSAGE SHOULD BE DEFINED USING .ASCIIZ
;*
;* SUBORDINATE ROUTINES USED: NONE.
;*****
```

```
BGNMSG ERO201
SAVE JSR ERO201:: ;SAVE THE GPR CONTENTS.
R5,PREG05 ;CALL REGISTER SAVE SUBRT.

MOV #BIT06,R0 ;TRY TO CLEAR THE
BIC OPTION,R0 ;EXT'D ERROR REPORTING FLAG
BNE 4$ ;EXIT IF FLAG NOT SET.

2$: MOV R1,R2
TSTB (R2), ;CHECK FOR A ZERO BYTE (END OF MESSAGE).
BNE 2$ ;LOOP UNTIL NEXT MESSAGE IS FOUND.

PRINTB #EF0503,R1 ;PRINT THE FIRST MESSAGE.

MOV R1,-(SP)
MOV #EF0503,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C$PNTB
ADD #6,SP

PRINTB #EF0503,R2 ;PRINT THE SECOND MESSAGE.

MOV R2,-(SP)
MOV #EF0503,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C$PNTB
ADD #6,SP

4$: PASS JSR ;RESTORE THE GPR CONTENTS.
PC,@(SP), ;RETURN TO PREG05 SUBRT.

ENDMSG

L10003: TRAP C$MSG
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1951 016136
016136
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1953 016136 012700 000100
1954 016142 046700 164056
1955 016146 001011
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1957
1958 016150
016150 010146
016152 012746 004074
016156 012746 000002
016162 010600
016164 104414
016166 062706 000006
1959
1960 016172
016172
016172 104423

```
.SBTTL GLOBAL ERROR REPORTING ROUTINE - ER0503 -
;*****
;* THIS IS AN ERROR REPORTING SUBROUTINE WHICH PRINTS AN ADDITIONAL ERROR
;* MESSAGE WHOSE ADDRESS IS PASSED AS AN INPUT PARAMETER, PROVIDED
;* EXTENDED ERROR REPORTING HAS BEEN REQUESTED.
;*
;* INPUTS: R1 - ADDRESS OF THE MESSAGE TO PRINT.
;*
;* OUTPUTS: A MESSAGES IS PRINTED AT THE OPERATOR CONSOLE.
;*
;* CALLING SEQUENCE: LOAD THE ADDRESS OF THE MESSAGE IN R1.
;* INCLUDE THE LABEL "ER0503" AS THE MESSAGE POINTER
;* PARAMETER IN THE DIAG SUPER ERROR REPORT MACRO CALL.
;*
;* COMMENTS: THE MESSAGE IS PRINTED AS BASIC ERROR INFORMATION.
;*
;* SUBORDINATE ROUTINES USED: NONE.
;*****
```

```
BGNMSG ER0503
ER0503::
MOV #BIT06,R0 ;TRY TO CLEAR THE
BIC OPTION,R0 ;EXT'D ERROR REPORTING FLAG
BNE 2$ ;EXIT IF FLAG NOT SET.

PRINTB #EF0503,R1 ;PRINT THE MESSAGE.
MOV R1,-(SP)
MOV #EF0503,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C$PNTB
ADD #6,SP

2$: ENDMSG
L10004: TRAP C$MSG
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.SBTTL GLOBAL ERROR REPORTING ROUTINE - ER0504 -
;*****
;* THIS IS AN ERROR REPORTING SUBROUTINE WHICH PRINTS ADDITIONAL ERROR
;* MESSAGES WHEN ILLEGAL INTERRUPTS ARE RECEIVED.
;*
;* INPUTS:      R1 - ADDRESS OF THE MESSAGE TO PRINT.
;*              R2 - NUMBER OF ILLEGAL INTERRUPTS RECEIVED.
;*
;* OUTPUTS:     MESSAGESS ARE PRINTED AT THE OPERATOR CONSOLE.
;*
;* CALLING SEQUENCE:  LOAD THE ADDRESS OF THE MESSAGE IN R1.
;*                   LOAD THE NUMBER OF ILLEGAL INTS IN R2.
;*                   INCLUDE THE LABEL "ER0504" AS THE MESSAGE POINTER
;*                   PARAMETER IN THE DIAG SUPER ERROR REPORT MACRO CALL.
;*
;* COMMENTS:
;*
;* SUBORDINATE ROUTINES USED: NONE.
;*****
```

1982 016174
016174

BGNMSG ER0504

ER0504::

1983

1984 016174 012700 000100
1985 016200 046700 164020
1986 016204 001022

```
MOV #BIT06,R0 ;TRY TO CLEAR THE
BIC OPTION,R0 ;EXT'D ERROR REPORTING FLAG
BNE 2$ ;EXIT IF FLAG NOT SET.
```

1987

1988 016206
016206 010146
016210 012746 004074
016214 012746 000002
016220 010600
016222 104414
016224 062706 000006

```
PRINTB #EF0503,R1 ;PRINT THE FIRST LINE OF THE MESSAGE.
```

```
MOV R1,-(SP)
MOV #EF0503,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C$PNTB
ADD #6,SP
```

1989

016230
016230 010246
016232 012746 004101
016236 012746 000002
016242 010600
016244 104415
016246 062706 000006

```
PRINTX #EF0505,R2 ;PRINT THE NUMBER OF INTS RECEIVED.
```

```
MOV R2,-(SP)
MOV #EF0505,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #6,SP
```

1990

1991 016252
016252
016252 104423

```
2$: ENDMSG
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```
L10005: TRAP C$MSG
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.SBTTL GLOBAL ERROR REPORTING ROUTINE - ER1401 -
;*****
;* THIS IS AN ERROR REPORTING SUBROUTINE WHICH PRINTS ADDITIONAL ERROR
;* INFORMATION (IF REQUESTED DURING THE SOFTWARE QUESTIONS) IF AN ERROR
;* IS DETECTED IN THE ROM VERSION TEST. THIS SUBROUTINE ANALYSES THE INPUT
;* PARAMETERS WHICH CONTAIN THE ROM VERSION NUMBERS FOR PROC_1 AND PROC_2
;* AND REPORTS THE APPROPRIATE ERROR MESSAGE TO THE OPERATOR.
;*
;* INPUTS: R1 - CONTAINS THE ADDRESS OF THE FIRST MESSAGE TO BE REPORTED.
;* R3 - CONTAINS THE ROM VERSION NUMBER OF PROC_1.
;* R4 - CONTAINS THE ROM VERSION NUMBER OF PROC_2.
;*
;* OUTPUTS: BASIC AND EXTENDED ERROR MESSAGES ARE REPORTED AT THE
;* OPERATORS CONSOLE.
;*
;* CALLING SEQUENCE: INCLUDE THE LABEL "ER1401" AS THE MESSAGE POINTER
;* PARAMETER IN THE DRS ERROR REPORT MACRO CALL.
;*
;* COMMENTS:
;*
;* SUBORDINATE ROUTINES USED: NONE.
;*****
```

2016 016254
016254

BGNMSG ER1401

ER1401::

2017
2018 016254 012700 000100
2019 016260 046700 163740
2020 016264 001053

```
MOV #BIT06,R0 ;TRY TO CLEAR THE
BIC OPTION,R0 ;EXT'D ERROR REPORTING FLAG
BNE 60$ ;EXIT IF FLAG NOT SET.
```

2021
2022 016266
016266 010146
016270 012746 004074
016274 012746 000002
016300 010600
016302 104414
016304 062706 000006

```
PRINTB #EF0503,R1 ;REPORT THE ERROR MESSAGE PASSED IN.
MOV R1,-(SP)
MOV #EF0503,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C$PNTB
ADD #6,SP
```

2023
2024
2025
2026
2027

```
;+
; DETERMINE WHICH ROM VERSION NUMBER(S) ARE MISSING.
;-
```

2028 016310 012705 000143
2029 016314 012701 012107
2030 016320 012702 012135
2031 016324 120305
2032 016326 001402
2033 016330 012702 012147
2034 016334 004767 000026
2035
2036 016340 012701 012122
2037 016344 012702 012135
2038 016350 120405
2039 016352 001402
2040 016354 012702 012147
2041 016360 004767 000002
2042 016364 000413

```
MOV #99,R5 ;GET INVALID ROM NUMBER.
MOV #EM1405,R1 ;SELECT PROC_1 MESSAGE.
MOV #EM1407,R2 ;SELECT THE "NOT FOUND" MESSAGE.
CMPB R3,R5 ;CHECK PROC_1 ROM VERSION NUMBER.
BEQ 2$ ;GO REPORT PROC_1 CODE NOT FOUND.
MOV #EM1408,R2 ;SELECT "FOUND" MESSAGE.
JSR PC,50$ ;GO REPORT MESSAGE.

2$: MOV #EM1406,R1 ;SELECT PROC_2 MESSAGE.
MOV #EM1407,R2 ;SELECT THE "NOT FOUND" MESSAGE.
CMPB R4,R5 ;CHECK PROC_2 ROM VERSION NUMBER.
BEQ 4$ ;GO REPORT PROC_2 CODE NOT FOUND.
MOV #EM1408,R2 ;SELECT "FOUND" MESSAGE.
JSR PC,50$ ;GO REPORT THE MESSAGE.
BR 60$ ;EXIT.
```

2043
2044 016366
016366 010246
016370 010146
016372 012746 004256
016376 012746 000003
016402 010600
016404 104415
016406 062706 000010
2045 016412 000207
2046 016414
016414
016414 104423

50\$: PRINTX #EF1402,R1,R2 ;REPORT THE MESSAGE.

MOV R2,-(SP)
MOV R1,-(SP)
MOV #EF1402,-(SP)
MOV #3,-(SP)
MOV SP,R0
TRAP C\$PNTX
ADD #10,SP

60\$: RTS PC ;RETURN.
ENDMSG

L10006: TRAP C\$MSG


```

2048 .SBTTL GLOBAL ERROR REPORTING ROUTINE - ER1601 -
2049 ;*****
2050 ;* THIS AN ERROR REPORTING SUBROUTINE WHICH PRINTS ADDITIONAL ERROR
2051 ;* INFORMATION IF AN ERROR IS DETECTED IN ONE OF THE DEVICE REGISTER
2052 ;* ACCESS TESTS, PROVIDED EXTENDED ERROR REPORTING HAS BEEN REQUESTED.
2053 ;* THIS SUBROUTINE REPORTS THE ACTUAL AND EXPECTED FROM THE DEVICE
2054 ;* REGISTER(S) WHICH IS(ARE) IN FAULTY.
2055 ;*
2056 ;* INPUTS: R1 - ACTUAL DATA (UNUSED BITS SET TO 0).
2057 ;* R2 - EXPECTED DATA (UNUSED BITS SET TO 0).
2058 ;* R3 - OFFSET (IN BYTES) TO THE REGISTER BEING TESTED.
2059 ;* R5 - LINE NUMBER OF REGISTER BEING TESTED.
2060 ;* RMATBB - LABEL AT BASE OF REGISTER MESSAGE ADDRESS TABLE.
2061 ;*
2062 ;* OUTPUTS: MESSAGES ARE PRINTED AT THE OPERATORS CONSOLE.
2063 ;*
2064 ;* CALLING SEQUENCE: INCLUDE THE LABEL "ER1601" AS THE MESSAGE POINTER
2065 ;* PARAMETER IN THE DRS ERROR REPORT MACRO CALL.
2066 ;*
2067 ;* COMMENTS:
2068 ;*
2069 ;* SUBORDINATE ROUTINES CALLED: NONE
2070 ;*****
2071 016416 BGNMSG ER1601
2072 016416 ER1601::
2073 016416 012700 000100 MOV #BIT06,R0 ;TRY TO CLEAR THE
2074 016422 046700 163576 BIC OPTION,R0 ;EXT'D ERROR REPORTING FLAG
2075 016426 001036 BNE 2$ ;EXIT IF FLAG NOT SET.
2076
2077
2078 016430 016304 002262 MOV RMATBB(R3),R4 ;FETCH ADDRESS OF REGISTER NAME MESSAGE.
2079
2080 016434 PRINTB #EF1604,R4,R5 ;REPORT BASIC MESSAGE (REG NAME AND LINE #).
016434 010546 MOV R5,-(SP)
016436 010446 MOV R4,-(SP)
016440 012746 004451 MOV #EF1604,-(SP)
016444 012746 000003 MOV #3,-(SP)
016450 010600 MOV SP,R0
016452 104414 TRAP C$PNTB
016454 062706 000010 ADD #10,SP
2081 016460 PRINTX #EF1602,R2 ;PRINT THE EXPECTED DATA.
016460 010246 MOV R2,-(SP)
016462 012746 004345 MOV #EF1602,-(SP)
016466 012746 000002 MOV #2,-(SP)
016472 010600 MOV SP,R0
016474 104415 TRAP C$PNTX
016476 062706 000006 ADD #6,SP
2082 016502 PRINTX #EF1603,R1 ;PRINT THE ACTUAL DATA.
016502 010146 MOV R1,-(SP)
016504 012746 004407 MOV #EF1603,-(SP)
016510 012746 000002 MOV #2,-(SP)
016514 010600 MOV SP,R0
016516 104415 TRAP C$PNTX
016520 062706 000006 ADD #6,SP
2083 016524 2$: ENDMSG
016524

```

L10007:

016524 104423

TRAP CMSG

2085
2086
2087
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2100
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2106

```
.SBTTL GLOBAL ERROR REPORTING ROUTINE - ER1603 -
;*****
;* THIS ERROR REPORTING ROUTINE IS USED TO PRINT OUT A BASIC ERROR
;* MESSAGE, ALONG WITH A MESSAGE INFORMING THE OPERATOR WHICH TEST IS
;* ABOUT TO BE ABORTED, PROVIDED EXTENDED ERROR INFORMATION HAS BEEN
;* REQUESTED, OTHERWISE ONLY A "TEST FAILURE" MESSAGE WILL BE PRINTED.
;*
;* INPUTS: R1 - CONTAINS THE ADDRESS OF THE MESSAGE TO BE PRINTED.
;* ERRMSG - CONTAINS THE ADDRESS OF THE MESSAGE THAT INDICATES
;* THE TEST THAT IS BEING PERFORMED, EG DMA, BREAK ETC.
;*
;* OUTPUTS: MESSAGES ARE PRINTED AT THE OPERATORS CONSOLE.
;* "TESTNAME TEST ABORTED"
;*
;* CALLING SEQUENCE: INCLUDE THE LABEL "ER1603" AS THE MESSAGE POINTER
;* PARAMETER IN THE DRS ERROR REPORT MACRO CALL.
;*
;* COMMENTS:
;*
;* SUBORDINATE ROUTINES CALLED: NONE.
;*****
```

2107
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2110
2111
2112
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2114
2115
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2118
2119
2120
2121

016526
016526
016526 004567 165244
016532 012700 000100
016536 046700 163462
016542 001024
016544
016544 010146
016546 012746 004074
016552 012746 000002
016556 010600
016560 104414
016562 062706 000006
016566 016702 165200
016572
016572 010246
016574 012746 004313
016600 012746 000002
016604 010600
016606 104414
016610 062706 000006
016614
016614 004736
016616
016616 104423

```
BGNMSG ER1603
ER1603::
SAVE JSR R5,PREG05 ;SAVE THE CONTENTS OF THE GPRS.
;CALL REGISTER SAVE SUBRT.
MOV #BIT06,R0 ;TRY TO CLEAR THE
BIC OPTION,R0 ;EXT'D ERROR REPORTING FLAG
BNE 2$ ;EXIT IF FLAG NOT SET.
PRINTB #EF0503,R1 ;PRINT BASIC MESSAGE ON OPERATORS CONSOLE.
MOV R1,-(SP)
MOV #EF0503,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C$PNTB
ADD #6,SP
MOV ERRMSG,R2 ;GET THE "TEST MESSAGE".
PRINTB #EF1601,R2 ;PRINT "TEST ABORTED" MESSAGE.
MOV R2,-(SP)
MOV #EF1601,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C$PNTB
ADD #6,SP
2$: PASS ;RESTORE THE CONTENTS OF THE GPRS.
JSR PC,@(SP) ;RETURN TO PREG05 SUBRT.
ENDMSG
L10010: TRAP C$MSG
```

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2144

```
.SBTTL GLOBAL ERROR REPORTING ROUTINE - ER3001 -
:*****
: THIS IS AN ERROR REPORTING SUBROUTINE WHICH IS INTENDED FOR USE IN THE
: INTERRUPT BR LEVEL TEST. IT REPORTS ADDITIONAL INFORMATION WHEN AN
: INTERRUPT HAS OCCURRED AT THE WRONG BR LEVEL. UNLESS EXTENDED ERROR
: REPORTING HAS BEEN REQUESTED, ONLY THE TEST FAIL MESSAGE
: BE PRINTED.
:
: INPUTS: R1 - ADDRESS OF MESSAGE TO PRINT FIRST.
: R4 - BR LEVEL AT WHICH THE INT REQUEST OCCURRED.
: R5 - EXPECTED OR CORRECT BR LEVEL FOR THE DUT.
:
: OUTPUTS: A MESSAGES IS PRINTED AT THE OPERATOR CONSOLE.
:
: CALLING SEQUENCE: INCLUDE THE LABEL "ER3001" AS THE MESSAGE POINTER
: PARAMETER IN THE DIAG SUPER ERROR REPORT MACRO CALL.
:
: COMMENTS: THE MESSAGE IS PRINTED AS BASIC AND EXTENDED ERROR INFORMATION.
:
: SUBORDINATE ROUTINES USED: NONE.
:*****
```

2145 016620
016620
2146
2147 016620 012700 000100
2148 016624 046700 163374
2149 016630 001033
2150
2151 016632
016632 010146
016634 012746 004074
016640 012746 000002
016644 010600
016646 104414
016650 062706 000006
2152 016654
016654 010546
016656 012746 004546
016662 012746 000002
016666 010600
016670 104415
016672 062706 000006
2153 016676
016676 010446
016700 012746 004615
016704 012746 000002
016710 010600
016712 104415
016714 062706 000006
2154
2155 016720
016720
016720 104423

```
BGNMSG ER3001
ER3001::
MOV #BIT06,R0 ;TRY TO CLEAR THE
BIC OPTION,R0 ;EXT'D ERROR REPORTING FLAG
BNE 2$ ;EXIT IF FLAG NOT SET.
PRINTB #EF0503,R1 ;PRINT THE FIRST LINE OF THE MESSAGE.
MOV R1,-(SP)
MOV #EF0503,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C$PNTB
ADD #6,SP
PRINTX #EF3001,R5 ;REPORT EXPECTED BR LEVEL.
MOV R5,-(SP)
MOV #EF3001,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #6,SP
PRINTX #EF3002,R4 ;REPORT ACTUAL BR LEVEL.
MOV R4,-(SP)
MOV #EF3002,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #6,SP
2$: ENDMSG
L10011: TRAP C$MSG
```


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2168
2169
2170
2171
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2173
2174
2175
2176
2177
2178
2179 016722
016722
2180 016722 012700 000100
2181 016726 046700 163272
2182 016732 001040
2183 016734
016734 012746 014722
016740 012746 004074
016744 012746 000002
016750 010600
016752 104414
016754 062706 000006
2184 016760 005002
2185 016762 016703 163474
2186 016766 005004
2187 016770 000241
2188 016772 006003
2189 016774 103013
2190 016776
016776 016446 002464
017002 010246
017004 012746 004710
017010 012746 000003
017014 010600
017016 104415
017020 062706 000010
2191 017024 012405
2192 017026 005202
2193 017030 005703
2194 017032 001356
2195 017034
017034
017034 104423

```
.SBTTL GLOBAL ERROR REPORTING ROUTINE - ER9004 -
;*****
;* THIS IS AN ERROR REPORTING SUBROUTINE WHICH REPORTS ERROR SUMMARIES
;* FOR LINES WHICH HAVE EXCEEDED THE SPECIFIED MAXIMUM NUMBER OF
;* INDIVIDUAL RECEPTION ERRORS, PROVIDED EXTENDED ERROR REPORTING HAS
;* BEEN REQUESTED BY THE OPERATOR.
;*
;* INPUTS: R1 - ADDRESS OF MESSAGE TO PRINT FIRST.
;* ERCNTB - LABEL AT BASE OF LINE ERROR COUNTERS TABLE.
;* ERSMRF - "REPORT ERROR SUMMARY FOR LINE" FLAGS.
;*
;* OUTPUTS: A MESSAGE IS PRINTED AT THE OPERATOR CONSOLE.
;*
;* CALLING SEQUENCE: INCLUDE THE LABEL "ER9004" AS THE MESSAGE POINTER
;* PARAMETER IN THE DIAG SUPER ERROR REPORT MACRO CALL.
;*
;* COMMENTS: THE MESSAGE IS PRINTED AS BASIC AND EXTENDED ERROR INFORMATION.
;* THE CONTENTS OF GPR'S R2, R3, R4, AND R5 ARE DESTROYED.
;*
;* SUBORDINATE ROUTINES USED: NONE.
;*****
```

```
BGNMSG ER9004
ER9004::
MOV #BIT06,R0 ;TRY TO CLEAR THE
BIC OPTION,R0 ;EXT'D ERROR REPORTING FLAG
BNE 6$ ;EXIT IF FLAG NOT SET.
PRINTB #EF0503,#EM9014 ;REPORT THE SECONDARY ERROR MESSAGE.
MOV #EM9014,-(SP)
MOV #EF0503,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C$PNTB
ADD #6,SP
CLR R2 ;CLEAR THE LINE COUNTER.
MOV ERSMRF,R3 ;GET THE ERROR SUMMARY FLAGS.
CLR R4 ;CLEAR "LINE COUNTER TIMES 2" OFFSET.
2$: CLC ;CLEAR THE CARRY FOR THE FOLLOWING ROTATE.
ROR R3 ;SHIFT ANOTHER ERROR SUMMARY FLAG INTO CARRY.
BCC 4$ ;SKIP PRINTING MESSAGE IF FLAG FOR LINE CLEAR.
PRINTX #EF9010,R2,ERCNTB(R4)
MOV ERCNTB(R4),-(SP)
MOV R2,-(SP)
MOV #EF9010,-(SP)
MOV #3,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #10,SP
4$: MOV (R4),R5 ;INCREMENT THE LINE OFFSET BY 2.
INC R2 ;INCREMT THE LINE COUNTER.
TST R3 ;CHECK THE ERROR SUMMARY FLAGS.
BNE 2$ ;IF MORE FLAGS SET, LOOP TO DO OTHER LINES.
6$: ENDMMSG
L10012: TRAP C$MSG
```

2197
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2199
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2201
2202
2203
2204
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2206
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2208
2209
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2215
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2217
2218

```
.SBTTL GLOBAL ERROR REPORTING ROUTINE - ER9007 -
;*****
;* THIS IS AN ERROR REPORTING SUBROUTINE WHICH IS USED TO REPORT THAT
;* SOMETHING OTHER THAN A SELFTEST CODE WAS FOUND IN A SELFTEST CODE
;* FIFO SLOT DURING THE REMOVAL OF THE SELFTEST CODES FROM THE FIFO.
;* THIS ROUTINE IS USED BY THE RSTRPT ROUTINE. EXTENDED ERROR INFOMATION
;* IS GIVEN ONLY WHEN REQUESTED IN THE SOFTWARE QUESTIONS.
;*
;* INPUTS:      R1 - ADDRESS OF ERROR MESSAGE QUALIFIER STRING.
;*              R2 - INCORRECT CODE AS READ FROM THE SELFTEST CODE FIFO SLOT.
;*              R3 - LINE NUMBER ASSOCIATED WITH THE SELFTEST FIFO SLOT.
;*
;* OUTPUTS:     A MESSAGE IS PRINTED AT THE OPERATOR CONSOLE.
;*
;* CALLING SEQUENCE:  INCLUDE THE LABEL "ER9007" AS THE MESSAGE POINTER
;*                   PARAMETER IN THE DIAG SUPER ERROR REPORT MACRO CALL.
;*
;* COMMENTS:     THE MESSAGE IS PRINTED AS BASIC AND EXTENDED ERROR INFORMATION.
;*
;* SUBORDINATE ROUTINES USED: NONE.
;*****
```

2219 017036
017036

BGNMSG ER9007

ER9007::

2220

2221 017036 012700 000100
2222 017042 046700 163156
2223 017046 001026
2224

```
MOV #BIT06,R0 ;TRY TO CLEAR THE
BIC OPTION,R0 ;EXT'D ERROR REPORTING FLAG
BNE 2$ ;EXIT IF FLAG NOT SET.
```

2225 017050 042703 177760
2226 017054

```
BIC #177760,R3 ;REMOVE ALL BUT LINE # BITS FROM LINE # WORD.
PRINTB #EF9018,R1,R3 ;REPORT SECONDARY ERROR MESSAGE.
```

017054 010346
017056 010146
017060 012746 005150
017064 012746 000003
017070 010600
017072 104414
017074 062706 000010

```
MOV R3,-(SP)
MOV R1,-(SP)
MOV #EF9018,-(SP)
MOV #3,-(SP)
MOV SP,R0
TRAP C$PNTB
ADD #10,SP
```

2227 017100
017100 010246
017102 010146
017104 012746 005074
017110 012746 000003
017114 010600
017116 104415
017120 062706 000010

```
PRINTX #EF9017,R1,R2 ;REPORT THE ACTUAL INCORRECT CODE.
```

```
MOV R2,-(SP)
MOV R1,-(SP)
MOV #EF9017,-(SP)
MOV #3,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #10,SP
```

2228

2229 017124
017124
017124 104423

2\$: ENDMSG

L10013: TRAP C\$MSG

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2237
2238
2239
2240
2241
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2246
2247
2248
2249
2250

```
.SBTTL GLOBAL ERROR REPORTING ROUTINE - ER9008 -
;*****
;* THIS IS AN ERROR REPORTING SUBROUTINE WHICH IS USED TO REPORT THAT
;* AN UNEXPECTED CODE OR CHARACTER HAS BEEN FOUND IN THE DUT RECEIVE
;* CHARACTER FIFO. THE ADDITIONAL ERROR IS REPORTED ONLY IF REQUESTED
;* DURING THE SOFTWARE QUESTIONS.
;*
;* INPUTS: R1 - ADDRESS OF PARTIAL ERROR MESSAGE STRING.
;* R2 - INCORRECT CODE AS READ FROM THE SELFTTEST CODE FIFO SLOT.
;*
;* OUTPUTS: A MESSAGE IS PRINTED AT THE OPERATOR CONSOLE.
;*
;* CALLING SEQUENCE: INCLUDE THE LABEL "ER9008" AS THE MESSAGE POINTER
;* PARAMETER IN THE DIAG SUPER ERROR REPORT MACRO CALL.
;*
;* COMMENTS: THE MESSAGE IS PRINTED AS BASIC AND EXTENDED ERROR INFORMATION.
;*
;* SUBORDINATE ROUTINES USED: NONE.
;*****
```

2251 017126
017126

BGNMSG ER9008

ER9008::

2252
2253 017126 012700 000100
2254 017132 046700 163066
2255 017136 001030

```
MOV #BIT06,R0 ;TRY TO CLEAR THE
BIC OPTION,R0 ;EXT'D ERROR REPORTING FLAG
BNE 2$ ;EXIT IF FLAG NOT SET.
```

2256
2257
2258
2259
2260

```
;*
;* EXTRACT THE LINE NUMBER FROM THE INCORRECT CODE OR CHARACTER WHICH WAS READ
;* FROM THE SELFTTEST CODE FIFO SLOT.
;*
```

2261 017140 010203
2262 017142 000303
2263 017144 042703 177760
2264 017150

```
MOV R2,R3
SWAB R3
BIC #177760,R3 ;CALCULATE LINE NUMBER OF CODE.
PRINTB #EF9016,R1,R3 ;REPORT TYPE OF INCORRECT CODE FOUND.
```

017150 010346
017152 010146
017154 012746 004777
017160 012746 000003
017164 010600
017166 104414
017170 062706 000010

```
MOV R3,-(SP)
MOV R1,-(SP)
MOV #EF9016,-(SP)
MOV #3,-(SP)
MOV SP,R0
TRAP C$PNTB
ADD #10,SP
```

2265 017174
017174 010246
017176 010146
017200 012746 005074
017204 012746 000003
017210 010600
017212 104415
017214 062706 000010

```
PRINTX #EF9017,R1,R2 ;REPORT THE ACTUAL INCORRECT CODE.
```

```
MOV R2,-(SP)
MOV R1,-(SP)
MOV #EF9017,-(SP)
MOV #3,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #10,SP
```

2266
2267

2\$: ENDMSG

017220
017220
017220 104423

L10014:
TRAP C\$MSG

2269
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2271
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2280
2281
2282
2283
2284
2285
2286
2287
2288 017222
017222
2289
2290 017222 012700 000100
2291 017226 046700 162772
2292 017232 001012
2293
2294
2295 017234
017234 010146
017236 010246
017240 012746 004664
017244 012746 000003
017250 010600
017252 104414
017254 062706 000010
2296
2297 017260
017260
017260 104423

```
.SBTTL GLOBAL ERROR REPORTING ROUTINE - ER9101 -
;*****
;* THIS IS A GENERAL ERROR REPORTING SUBROUTINE WHICH REPORTS A MESSAGE
;* WHICH TAKES A SINGLE, 2 DIGIT DECIMAL ARGUMENT AFTER THE END OF AN
;* ASCII MESSAGE.
;*
;* INPUTS: R1 - VALUE TO BE PRINTED AFTER MSG AS 2 DECIMAL DIGITS.
;* R2 - ADDRESS OF MESSAGE TO PRINT FIRST.
;*
;* OUTPUTS: A MESSAGES IS PRINTED AT THE OPERATOR CONSOLE.
;*
;* CALLING SEQUENCE: INCLUDE THE LABEL "ER9101" AS THE MESSAGE POINTER
;* PARAMETER IN THE DIAG SUPER ERROR REPORT MACRO CALL.
;*
;* COMMENTS: THE MESSAGE IS PRINTED AS BASIC ERROR INFORMATION.
;*
;* SUBORDINATE ROUTINES USED: NONE.
;*****
```

BGNMSG ER9101

ER9101::

```
MOV #BIT06,R0 ;TRY TO CLEAR THE
BIC OPTION,R0 ;EXT'D ERROR REPORTING FLAG
BNE 2$ ;EXIT IF FLAG NOT SET.
```

```
PRINTB #EF9006,R2,R1 ;REPORT THE STRING FOLLOWED BY THE NUMBER.
```

```
MOV R1,-(SP)
MOV R2,-(SP)
MOV #EF9006,-(SP)
MOV #3,-(SP)
MOV SP,R0
TRAP C$PNTB
ADD #10,SP
```

2\$: ENDMSG

L10015: TRAP C\$MSG

2299
2300
2301
2302
2303
2304
2305
2306
2307
2308
2309
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2311
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2315
2316
2317
2318
2319

```
.SBTTL GLOBAL ERROR REPORTING ROUTINE - ER9301 -
;*****
; THIS IS AN ERROR REPORTING SUBROUTINE WHICH PRINTS ANY BMP CODES
; THAT ARE FOUND IN THE BMP CODE QUEUE, TOGETHER WITH THE THE NUMBER OF
; THE TEST THAT WAS EXECUTING AT THE TIME THE BMP CODE WAS LOGGED.
; PROVIDED EXTENDED ERROR REPORTING HAS BEEN ENABLED.
;
; INPUTS: R1 - THE ADDRESS OF THE FIRST MESSAGE TO BE REPORTED.
; R2 - THE ADDRESS OF THE NEXT EMPTY CELL IN THE QUEUE.
;
; OUTPUTS: THE TEST NUMBER FOLLOWED BY THE BMP CODE ARE PRINTED AT THE
; OPERATOR CONSOLE.
;
; CALLING SEQUENCE: INCLUDE THE LABEL "ER9301" AS THE MESSAGE POINTER
; PARAMETER IN THE DIAG SUPER ERROR REPORT MACRO CALL.
;
; COMMENTS: THE MESSAGE IS PRINTED AS BASIC ERROR INFORMATION.
;
; SUBORDINATE ROUTINES USED: NONE.
;*****
```

2320 017262
017262
2321 017262
017262 004567 164510
2322
2323 017266 012700 000100
2324 017272 046700 162726
2325 017276 001064
2326
2327 017300
017300 010146
017302 012746 004074
017306 012746 000002
017312 010600
017314 104414
017316 062706 000006
2328 017322 012703 002526
2329 017326 012705 015336
2330 017332 012301
2331 017334 012304
2332 017336 004767 C00056
2333 017342 020302
2334 017344 103772
2335
2336
2337
2338
2339
2340
2341 017346 020227 002722
2342 017352 001036
2343 017354 005762 000002
2344 017360 001433
2345 017362 012301
2346 017364 011304
2347 017366 012705 015366

```
BGNMSG ER9301
SAVE JSR ER9301::
;SAVE THE GPRS ON THE STACK.
R5,PREGOS ;CALL REGISTER SAVE SUBRT.

MOV #BIT06,R0 ;TRY TO CLEAR THE
BIC OPTION,R0 ;EXT'D ERROR REPORTING FLAG
BNE 60$ ;EXIT IF FLAG NOT SET.

PRINTB #EF0503,R1 ;REPORT UNEXPECTED BMP CODES FOUND.
MOV R1,-(SP)
MOV #EF0503,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C$PNTB
ADD #6,SP

MOV #BMPCQB,R3 ;GET THE START ADDRESS OF THE BMP CODE QUEUE.
MOV #EM9302,R5 ;GET THE MESSAGE TO BE REPORTED.
2$: MOV (R3)+,R1 ;GET THE NUMBER OF THE TEST THAT WAS EXECUTING.
MOV (R3)+,R4 ;GET BMP CODE THAT WAS REPORTED OFF THE QUEUE.
JSR PC,50$ ;GO REPORT THE BMP CODE.
CMP R3,R2 ;CHECK IF ALL CODES HAVE BEEN REPORTED.
BLO 2$ ;IF IT IS NOT THE LAST BMP CODE THEN LOOP.

;
; CHECK IF OVERFLOW HAS OCCURRED.
; THE CONDITIONS FOR OVERFLOW ARE: THE POINTER CONTAINS THE ADDRESS OF THE
; LAST CELL IN THE QUEUE, AND A BMP CODE HAS ALREADY BEEN WRITTEN INTO THAT
; CELL.
;
CMP R2,#BMPCQE-4 ;CHECK IF THE POINTER IS AT THE LAST LOCATION.
BNE 60$ ;EXIT IF NOT AT THE LAST LOCATION.
TST 2(R2) ;CHECK FOR A BMP CODE IN THE LAST CELL
BEQ 60$ ;EXIT IF NO OVERFLOW HAS OCCURED, CELL EMPTY.
MOV (R3)+,R1 ;GET THE TEST NUMBER OFF THE QUEUE.
MOV (R3),R4 ;GET THE BMP CODE OFF THE QUEUE.
MOV #EM9303,R5 ;SELECT THE MESSAGE TO BE REPORTED.
```

```

2348 017372          PRINTX  #EF9302          ;REPORT OVERFLOW CONDITION.
      017372 012746 005333
      017376 012746 000001
      017402 010600
      017404 104415
      017406 062706 000004
2349 017412 004767 000002
2350 017416 000414
2351
2352 017420          50$: PRINTX  #EF9301,R5,R1,R4 ;PRINT THE MESSAGE.
      017420 010446
      017422 010146
      017424 010546
      017426 012746 005255
      017432 012746 000004
      017436 010600
      017440 104415
      017442 062706 000012
2353 017446 000207
2354 017450          60$: RTS      PC          ;RETURN.
      017450 004736          PASS          ;RESTORE THE GPR CONTENTS.
2355
2356 017452          ENDMSG          JSR      PC,@(SP)+ ;RETURN TO PREG05 SUBRT.
      017452
      017452 104423          L10016: TRAP  C$MSG

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.SBTTL GLOBAL SUBROUTINES SECTION

; THE GLOBAL SUBROUTINES SECTION CONTAINS THE SUBROUTINES
; THAT ARE USED IN MORE THAN ONE TEST.

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017454
017454 004567 164316

017460 010400
017462 005100
017464 040002
017466 016705 162616

017472 000241
017474 006003
017476 103006
017500 010577 162536
017504 011100
017506 040400
017510 050200
017512 010011
017514 005205
017516 005703
017520 001365

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.SBTTL GLOBAL SUBROUTINE - ALTFLD -
;*****
;* - ALTER DEVICE REGISTER FIELDS ROUTINE -
;* THIS SUBROUTINE ALTERS THE SPECIFIED FIELD OF THE SPECIFIED DEVICE
;* REGISTER FOR THE SPECIFIED LINES. THIS ROUTINE CAN BE USED TO SET
;* OR CLEAR BITS WITHIN SELECTED FIELDS OF SELECTED REGISTERS.
;* USE EXAMPLES: SET RX.BAUD.RATE FIELDS ON LINES 3 AND 6.
;* CLEAR TX.DMA BITS ON ALL LINES.
;*
;* INPUTS: R1 - ADDRESS OF THE REGISTERS TO ALTER.
;* R2 - BIT FIELDS SET TO DESIRED STATES.
;* R3 - BIT MAP OF LINES FOR WHICH TO ALTER REGISTER.
;* R4 - MASK OF BITS TO ALTER (1 INDICATES CHANGE BIT).
;* CSRA - CONTAINS THE ADDRESS OF THE DEVICE CSR.
;* IESTAT - SAVED STATES OF THE INTERRUPT ENABLE BITS.
;*
;* OUTPUTS: DEVICE REGISTERS - SPECIFIED REGISTER FIELDS ALTERED.
;* CSR IND.ADR.REG FIELD - DESTROYED.
;*
;* CALLING SEQUENCE: JSR PC,ALTFLD
;*
;* COMMENTS: THIS ROUTINE READS THE SPECIFIED REGISTERS FOR ALL LINES
;* WITH NUMBERS LOWER THAN THE HIGHEST SPECIFIED LINE.
;* THIS ROUTINE DOES NOT READ THE CSR.
;*
;* SUBROUTINES CALLED: NONE.
;-- *****
ALTFLD:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
; R5,PREG05 ;CALL REGISTER SAVE SUBRT.
; JSR
;+
; SET UP TO LOOP FOR EACH LINE:
; PREPARE THE WORD TO BE ORED INTO THE REGISTER CONTENTS.
; SET UP THE WORD TO WRITE INTO THE IND.ADR.REG FIELD OF THE CSR.
;-
MOV R4,R0 ;CALCULATE THE NEW CONTENTS OF THE
COM R0 ; REGISTER FIELDS WHICH ARE TO BE
BIC R0,R2 ; ALTERED BY THIS ROUTINE.
MOV IESTAT,R5 ;SET UP TO WRITE IND.ADR.REG FIELD TO 0.
;+
; LOOP ONCE FOR EACH LINE, ALTERING THE SPECIFIED FIELD IN THE SPECIFIED
; REGISTER IF THE LINE HAS BEEN SELECTED FOR ALTERING.
; EXIT THE LOOP IF NO MORE LINES TO ALTER, OR IF WE HAVE ALTERED THE MAX
; ALLOWABLE NUMBER OF LINES (AS SPECIFIED BY NUMLNS).
;-
CLC ;PREPARE FOR ROTATE. "TST R5" DOES THIS BELOW.
2$: ROR R3 ;GET THE LINE SELECT BIT FOR THIS LINE.
BCC 4$ ;SKIP SETUP IF LINE IS NOT SELECTED.
MOV R5,@CSRA ;SET OUT CSR IND.ADR.REG FIELD TO THIS LINE.
MOV (R1),R0 ;GET THE PRESENT CONTENTS OF THE REG TO ALTER.
BIC R4,R0 ;CLEAR THE BIT FIELDS WE ARE TO ALTER.
BIS R2,R0 ;OR IN THE NEW STATES OF THE FIELDS.
MOV R0,(R1) ;WRITE THE NEW REGISTER CONTENTS TO THE REG.
4$: INC R5 ;SET LINE NUMBER TO THE NEXT LINE.
TST R3 ;CHECK FOR UNHANDLED LINES, CLEAR CARRY FLAG.
BNE 2$ ;LOOP IF SELECTED LINE(S) IS NOT HANDLED.
```


2429
2430 017522
 017522 004736
2431 017524 000207

60\$: PASS
RTS PC

JSR ;RESTORE GPRS.
PC,@(SP)+ ;RETURN TO PREG05 SUBRT.
;RETURN TO CALLING ROUTNE.

```

2433 .SBTTL GLOBAL SUBROUTINE - CALMSL -
2434 : ** *****
2435 : * - CALIBRATE MILLI SECOND LOOP COUNT SUBROUTINE -
2436 : * THIS SUBROUTINE CALIBRATES THE TIMING LOOP WHICH IS USED IN THE MSLOOP
2437 : * ROUTINE. THIS SUBROUTINE CALCULATES A VALUE FOR THE MSLCNT VARIABLE
2438 : * WHICH IS THE NUMBER OF SOFTWARE LOOPS WHICH TAKES 1 MS TO EXECUTE IN
2439 : * THE MSLOOP ROUTINE. THIS ROUTINE CALIBRATES THE COUNT BY USING THE
2440 : * LINE TIME CLOCK (LTC), SO IF NO LTC IS AVAILABLE THE DEFAULT VALUE FOR
2441 : * THE DELAY COUNT MUST BE USED.
2442 : *
2443 : *
2444 : * INPUTS: MSLCNT - DEFAULT 1 MS DELAY LOOP COUNT VALUE, OR
2445 : * VALUE FROM PREVIOUS CALIBRATION.
2446 : * MSTICK - NUMBER OF MS PER LTC CLOCK TICK.
2447 : * TIMER1 - TIMER COUNTER CHANGED BY LTC INTERRUPT SERVICE RTN.
2448 : * CLKHRZ - NUMBER OF LTC CLICKS PER SECOND (50 OR 60).
2449 : *
2450 : * OUTPUTS: CARRY - SET IF LTC IS AVAILABLE, AND NEW CALIBRATION PERFORMED.
2451 : * MSLCNT - NEW 1 MS DELAY LOOP COUNT VALUE IF LTC AVAILABLE, OR
2452 : * UNCHANGED IF NO LTC IS AVAILABLE.
2453 : *
2454 : * CALLING SEQUENCE: JSR PC,CALMSL
2455 : *
2456 : * COMMENTS:
2457 : *
2458 : * SUBORDINATE ROUTINES CALLED: UNSDIV,OOPS.
2459 : - - *****
2460
2461 017526 CALMSL:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
2462 017526 004567 164244 ;R5,PREG05 ;CALL REGISTER SAVE SUBRT.
2463 017532 005067 000210 CLR 62$ ;CLEAR THE 2ND TIME FLAG.
2464
2465 ; SYNCHRONIZE WITH THE LTC.
2466 017536 012705 000001 2$: MOV #1,R5 ;SET OUTER LOOP COUNTER TO 1 LOOP.
2467 ;INCREASE THE VALUE LOADED INTO THIS COUNTER IF THE ***
2468 ;FOLLOWING LOOP FAILS ON FUTURE, FASTER PROCESSORS. ***
2469 017542 005000 CLR R0 ;CLEAR THE WAIT FOR CLOCK INT COUNTER.
2470 017544 012767 000001 162572 MOV #1,TIMER1 ;SET UP COUNT OF 1 TO SYNCH WITH LTC.
2471 017552 005767 162566 4$: TST TIMER1 ;CHECK FOR COUNTER HAVING GONE TO ZERO.
2472 017556 001410 BEQ 6$ ;JUMP OUT OF LOOP IF LTC HAS INTERRUPTED.
2473 017560 005200 INC R0 ;COUNT THIS ITERATION OF THE INNER LOOP.
2474 017562 001373 BNE 4$ ;LOOP IF COUNTER HAS NOT TURNED OVER.
2475 017564 005305 DEC R5 ;DECREMENT THE INNER LOOP COUNTER.
2476 017566 003371 BGT 4$ ;LOOP IF OUTER LOOP COUNT NOT UP.
2477
2478 ; IF WE GOT NO LTC INTERRUPT, INDICATE THAT THERE IS NO LTC AVAILABLE.
2479 ; LTC MUST BE FLAKEY, OR NOT REALLY AN LTC AT ALL.
2480
2481 017570 005067 162546 CLR CLKHRZ ;CLEAR LTC FREQUENCY WORD TO INDICATE NO LTC.
2482 017574 000241 CLC ;INDICATE FAILURE FOR RETURN.
2483 017576 000461 BR 60$ ;BYPASS THE FOLLOWING CALIBRATION PROCEDURES.
2484
2485 ;*
2486 ; WE ARE NOW SYNCHRONIZED WITH THE LTC.
2487 ; SET UP FOR THE CALIBRATION LOOP.
2488 017600 012704 002344 6$: MOV #TIMER1,R4 ;WILL TEST TIMER1 IN THE LOOP BELOW.

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2489 017604 005001          CLR    R1          ;CLEAR THE OUTER LOOP COUNTER.
2490 017606 005002          CLR    R2          ;INDICATE TO CHECK ALL BITS OF TIMER1.
2491 017610 005003          CLR    R3          ;INDICATE TO CHECK FOR TIMER1 CLEAR.
2492 017612 012714 000001  MOV    #1,(R4)     ;LOAD TIMER1 WITH COUNT OF 1.
2493
2494 017616 016705 162534  8%:   MOV    MSLCNT,R5   ;LOAD MS LOOP COUNT.
2495 017622 011400 10%:   MOV    (R4),R0     ;GET THE TIMER1 VALUE.
2496 017624 010067 000120  MOV    R0,64%     ;SAVE WORD (LIKE IN THE REAL LOOP).
2497 017630 040200          BIC    R2,R0       ;LEAVE ALL THE BITS.
2498 017632 020003          CMP    R0,R3       ;COMPARE AGAINST ZERO.
2499 017634 000261          SEC                    ;SET CARRY IN CASE OF SUCCESS.
2500 017636 001406          BEQ                    ;EXIT LOOP IF TIMER1 HAS CLEARED.
2501 017640 005305          DEC    R5          ;COUNT DOWN THE INSIDE MS LOOP COUNT.
2502 017642 001367          BNE    10%        ;LOOP IF MS NOT UP.
2503 017644 005301          DEC    R1          ;DECREMENT THE MS TIME COUNT.
2504 017646 001363          BNE    8%         ;KEEP LOOPING.
2505 017650 004767 000440  JSR    PC,OOPS     ;WE OVERFLOWED, SOMETHING IS WRONG, ABORT.
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2512 017654 005401          ;*
2513 017656 016702 162474  ; WE HAVE NOW HAVE LOOP COUNT INFORMATION FOR ONE CLOCK TICK.
2514 017662 010203          ; WE HAVE NEGATIVE OF NUMBER OF OUTER LOOPS IN R1, EACH IS MSLCNT INNER LOOPS.
2515 017664 160502          ; WE HAVE THE PORTION OF THE LAST OUTER LOOP NOT EXECUTED, IN R5.
2516 017666 010204          ; NOW WE CALCULATE THE TOTAL NUMBER OF INNER LOOPS EXECUTED.
2517 017670 005005          ;-
2518 017672 005301 12%:   NEG    R1          ;GET NUMBER OF OUTER LOOPS.
2519 017674 100403          MOV    MSLCNT,R2   ;GET THE NUMBER OF INNER LOOPS PER OUTER LOOP.
2520 017676 060304          MOV    R2,R3       ;COPY NUMBER OF LOOPS FOR MULTIPLY.
2521 017700 005505          SUB    R5,R2       ;CALC # OF INNER LOOPS DONE IN LAST OUTER LOOP
2522 017702 000773          MOV    R2,R4       ; AND ADD TO ACCUMULATOR LSWORD.
2523
2524
2525
2526 017704 016701 162444  14%:  CLR    R5          ;CLEAR ACCUMULATOR MSWORD.
2527 017710 010403          DEC    R1          ;CHECK R1 FOR 0 CONDITION
2528 017712 010502          BMI    16%        ; SKIP MULTIPLICATION IF ZERO
2529 017714 004767 003140  ADD    R3,R4       ;MULTIPLY NUMBER OF INNER
2530 017720 103402          ADC    R5          ; LOOPS PER OUTER LOOP BY
2531 017722 004767 000366  BR     14%        ;NUMBER OF OUTER LOOPS PERFORMED.
2532 017726 010167 162424  ;*
2533 017732 005167 000010  ; DIVIDE THE TOTAL NUMBER OF INNER LOOPS BY THE NUMBER OF MS PER LTC TICK.
2534 017736 001277          ;-
2535 017740 000261          16%:  MOV    MSTICK,R1   ;# OF MS PER LTC TICK IS DIVISOR.
2536
2537 017742          MOV    R4,R3       ;LSWORD OF LOOP COUNT IS LSWORD OF DIVIDEND.
2538 017744 000207          MOV    R5,R2       ;MSWORD OF LOOP COUNT IS MSWORD OF DIVIDEND.
2539
2540 017746 000000          JSR    PC,UNSDIV   ;DIVIDE NUMBER OF LOOPS BY MS PER LTC TICK.
2541 017750 000000          BCS    18%        ;BYPASS OOPS IF WE'RE OK.
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017752		
017752	004567	164020
017756	005067	162336
017762	011011	
017764	005767	162330
017770	000261	
017772	001401	
017774	000241	
017776	004736	
020000	000207	

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.SBTTL GLOBAL SUBROUTINE - CKTRAP -
;*****
;* CHECK TRAP ROUTINE -
;* THIS SUBROUTINE IS USED TO CHECK FOR A BUS TIME-OUT TRAP (004 TRAP)
;* WHICH IS CAUSED BY AN ACCESS TO A NON-EXISTENT MEMORY OR I/O LOCATION.
;* IF THE TRAP DOES NOT OCCUR, THIS ROUTINE RETURNS A SUCCESS INDICATION.
;*
;* INPUTS: R0 - SOURCE ADDRESS FOR MOVE.
;* R1 - DESTINATION ADDRESS FOR MOVE.
;* (R0) - SOURCE FOR THE MOVE.
;*
;* OUTPUTS: (R1) - WRITTEN TO THE CONTENTS OF (R0).
;* CARRY FLAG - SET ON RETURN IF NO 004 TRAP DETECTED.
;* TP4FLG - NONZERO IF TRAP OCCURRED, CLEARED OTHERWISE.
;*
;* CALLING SEQUENCE: JSR PC,CKTRAP
;*
;* COMMENTS: IF THIS SUBROUTINE CAUSES A TRAP, EITHER THE ADDRESS WHICH
;* IS LABELED ADRPTR WILL BE THE TRAP PC ADDRESS ON THE STACK.
;*
;* SUBORDINATE ROUTINES CALLED: NONE.
;*****
CKTRAP:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
                JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
                CLR TP4FLG ;CLEAR THE 004 TRAP FLAGS.
                MOV (R0),(R1) ;PERFORM THE MOVE IN QUESTION.
ADRPTR:: TST TP4FLG ;CHECK FOR OCCURENCE OF TRAP.
                SEC ;INDICATE SUCCESS.
                BEQ 60$ ;EXIT WITH SUCCESS IF TRAP DID NOT OCCUR.
                CLC ;INDICATE FAILURE.
60$: PASS ;RESTORE GPRS.
                JSR PC,8(SP) ;RETURN TO PREG05 SUBRT.
                RTS PC
    
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020002 004567 163770

020006 004767 001344
020012 103002

020014 004767 000522

020020
020020 004736

020022 000207

```
.SBTTL GLOBAL SUBROUTINE - CLNRST -
;*****
;* - CLEAN RESET OF THE DEVICE UNDER TEST -
;* THIS SUBROUTINE IS USED TO RESET THE DUT TO A KNOWN STATE.
;* THE DUT'S SELF-TEST IS SKIPPED, AND THE FIFO IS PURGED OF ANY ERROR
;* CODES, ETC.
;* IF THE RESET DOES NOT SUCCESSFULLY COMPLETE, THEN THE CARRY BIT IS
;* PASSED BACK TO THE CALLING ROUTINE (CLEAR).
;*
;* INPUTS: CSRA - CONTAINS THE ADDRESS OF THE CSR
;* TXBFCA - CONTAINS ADDRESS OF DUT DMA BUFFER COUNT REGISTER.
;* ERRNBR - ERROR NUMBER FOR POSSIBLE ERROR REPORT.
;* ERRTBL - ERRTP, ERNBR, AND ERRMSG SET UP CORRECTLY.
;*
;* OUTPUTS: THE DUT PERFORMS ITS RESET FUNCTION INTO A KNOWN STATE.
;* CARRY - CLEAR INDICATES THE TEST IS TO BE ABORTED.
;* ERRBLK - VALUE MAY BE DESTROYED.
;* IESTAT - TX AND RX INTERRUPT FLAGS ARE CLEARED.
;* TX AND RX INTERRUPT ENABLE BITS IN THE DUT'S CSR ARE CLEARED.
;*
;* CALLING SEQUENCE: JSR PC, CLNRST
;*
;* COMMENTS: THIS SUBROUTINE CAN REPORT ERRORS WITH NUMBERS ERRNBR.
;* THIS ROUTINE DOES NOT DESTROY THE VALUE OF ERRNBR.
;*
;* SUBORDINATE ROUTINES CALLED: DELAY, MSLGET, PUFIFO, RESETT.
;*****
CLNRST:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
; JSR R5, PREG05 ;CALL REGISTER SAVE SUBRT.
;*
;* RESET THE DUT.
;* THIS ROUTINE REPORTS ERRORS WITH NUMBERS FROM ERRNBR THRU ERRNBR+2.
;*
; JSR PC, RESETT ;RESET THE DUT TO A KNOWN STATE.
; BCC 60$ ;EXIT ROUTINE WITH ABORT TEST INDICATOR.
;*
;* PURGE THE FIFO OF ERROR CODES, SAVE ANY BMP CODES FOUND.
;*
; JSR PC, PUFIFO ;PURGE THE FIFO.
60$:
;EXIT THE TEST USING RESETT OR PUFIFO STATUS.
;RESTORE GPRS, PASS THE FOLLOWING INTACT:
; PC, @ (SP); ;RETURN TO PREG05 SUBRT.
;CARRY BIT: IF CLEAR, THEN ABORT THE TEST.
;
; RTS PC
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020024 004567 163746
2638 020030 012701 000020
2639 020034 005020
2640 020036 005301
2641 020040 001375
2642 020042
020042 004736
2643 020044 000207

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.SBTTL GLOBAL SUBROUTINE - CLR16W -
; * *****
; * - CLEAR SIXTEEN WORDS ROUTINE -
; * THIS SUBROUTINE CLEARS 16 WORDS STARTING WITH THE SPECIFIED WORD.
; *
; * INPUTS: R0 - ADDRESS OF THE FIRST WORD TO CLEAR.
; *
; * OUTPUTS: (R0) TO (R0+15) - 16 WORDS OF MEMORY ARE CLEARED TO 0.
; *
; * CALLING SEQUENCE: JSR PC,CLR16W
; *
; * COMMENTS:
; *
; * SUBORDINATE ROUTINES CALLED: NONE.
; - *****

CLR16W:: SAVE
                JSR    R5,PREG05 ;SAVE CONTENTS OF GPRS R0 THRU R5.
                ;CALL REGISTER SAVE SUBRT.
                MOV    #16.,R1 ;SET THE LOOP COUNTER TO 16.
2$:              CLR    (R0)+. ;CLEAR A WORD OF MEMORY.
                DEC    R1 ;COUNT THIS LOOP.
                BNE    2$ ;LOOP IF NOT 16 WORD CLEARED.
60$:            PASS   ;RESTORE GPRS.
                JSR    PC,@(SP)+ ;RETURN TO PREG05 SUBRT.
                RTS    PC
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020110 056567 002410 162344
020116 000261
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020122 000207

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.SBTTL GLOBAL SUBROUTINE - CNTERR -
; * *****
; * - COUNT ERROR ROUTINE -
; * THIS SUBROUTINE IS USED TO COUNT A "DATA" ERROR ON THE SPECIFIED
; * LINE. IT CHECKS WHETHER ERROR SUMMARY REPORTING IS ACTIVE, OR SHOULD
; * BE MADE ACTIVE ON THIS LINE, AND ACTIVATES IT IF NECESSARY.
; *
; * INPUTS: R5 - LINE NUMBER OF LINE UNDER CONSIDERATION.
; * ERCNTB - LABEL AT BASE OF ERROR COUNTERS TABLE.
; * ERSMRF - ERROR SUMMARY FLAGS (BIT SET IF LINE IN SUMMARY MODE).
; * NDERPT - NUMBER OF INDIVIDUAL DATA ERRORS TO REPORT ON A LINE.
; *
; * OUTPUTS: CARRY - SET IF LINE IS IN ERROR SUMMARY MODE.
; * ERCNT - ERROR COUNTER INCREMENTED FOR SPECIFIED LINE.
; * ERSMRF - BIT SET IF LINE SHOULD BE IN SUMMARY MODE.
; *
; * CALLING SEQUENCE: JSR PC,CNTERR
; *
; * COMMENTS:
; *
; * SUBORDINATE ROUTINES CALLED: NONE.
; - *****
CNTERR:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
; JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
; *
; * COUNT THE ERROR ON THE COUNTER FOR THE SPECIFIED LINE.
; *
; *
; * ASL R5 ;FORM WORD OFFSET FROM LINE NUMBER.
; * MOV ERCNTB(R5),R1 ;GET THE PRESENT ERROR COUNT FOR THIS LINE.
; * INC R1 ;COUNT ERROR.
; * BCS 2$ ;OVERFLOW? YES, DON'T UPDATE COUNTER IN TABLE.
; * MOV R1,ERCNTB(R5) ;UPDATE ERROR COUNTER TABLE ENTRY.
; * TST NDERPT
; * BEQ 60$ ;SUMMARYS DISABLED? YES, EXIT WITH CARRY 0.
; * CMP R1,NDERPT ;NO, CHECK FOR ENOUGH ERRORS FOR SUMMARY USE.
; * BHI 4$ ;ENOUGH ERRORS TO USE SUMMARY? YES, GO HANDLE.
; * CLC ;INDICATE NOT TO USE SUMMARY REPORT YET.
; * BR 60$ ;EXIT WITH CARRY 0.
; * BIS BITTBL(R5),ERSMRF ;SET THE ERROR SUMMARY FLAG FOR LINE.
; * SEC ;INDICATE TO USE SUMMARY REPORT.
; * PASS ;RESTORE GPRS.
; * JSR PC,@(SP) ;RETURN TO PREG05 SUBRT.
; *
; * RTS PC
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020124 004567 163646
020130 010401
020132 012702 177777
020136 005003
020140 012704 020162
020144 004767 000130
020150 103002
020152 004767 000136
020156
020156 004736
020160 000207
020162 177777

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.SBTTL GLOBAL SUBROUTINE - DELAY -
;*****
;* - DELAY SUBROUTINE -
;* THIS SUBROUTINE IS USED TO DELAY A VARIABLE NUMBER OF MILLI-SECONDS.
;* INPUTS: R4 - CONTAINS THE NUMBER OF MS TO DELAY.
;* MSLCNT.
;* OUTPUTS: NONE.
;* CALLING SEQUENCE: JSR PC,DELAY
;* COMMENTS: IF NO HARDWARE CLOCK INTERRUPTS ARE OCCURRING, CONTROL-CS WILL
;* NOT BE HONORED FOR THE DURATION OF THE DELAY.
;* SUBORDINATE ROUTINES CALLED: NONE.
;*****
DELAY:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
;CALL REGISTER SAVE SUBRT.
MOV R4,R1 JSR R5,PREG05 ;PASS NUMBER OF MS DELAY AS TIME-OUT VALUE.
MOV #-1,R2 ;TELL MSLOOP ROUTINE TO CHECK ALL BITS.
CLR R3 ;TELL MSLOOP RTN TO CHECK FOR ALL BITS CLEAR.
MOV #62$,R4 ;TELL MSLOOP TO CHECK DUMMY NON-ZERO WORD.
JSR PC,MSLOOP ;DELAY THE REQUESTED # OF MS.
BCC 60$ ;EXIT ROUTINE IF WE TIMED-OUT.]
JSR PC,OOPS ;IF NO TIME-OUT, BAD PROGRAM OR HOST MACHINE.
60$: PASS ;RESTORE GPRS.
;RETURN TO PREG05 SUBRT.
RTS PC JSR PC,@(SP)+
62$: .WORD -1 ;DUMMY, NON-ZERO WORD.
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020164 004567 163606
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2762 020170 005102
2763 020172 040203
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2767 020174 005701
2768 020176 001011
2769 020200 011400
2770 020202 010067 000070
2771 020206 040200
2772 020210 020003
2773 020212 000261
2774 020214 001420

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.SBTTL GLOBAL SUBROUTINE - MSLGET -
;*****
;* - MILLI SECONDS LOOP WHICH RETURNS READ WORD AND REMAINING TIME -
;* THIS SUBROUTINE IS A GENERAL PURPOSE TEST LOOP SUBROUTINE. IT IS USED
;* TO VERIFY THAT A CERTAIN ACTION OCCURS BEFORE A TIME-OUT PERIOD. THE
;* CALLING ROUTINE PASSES IN WHICH BITS SHOULD BE SET AND CLEARED FOR THE
;* DESIRED CONDITION AND THE TIME-OUT VALUE IN MILLI-SECONDS.
;* THIS ROUTINE CHECKS FOR THE DESIRED CONDITION UPON ENTRANCE INTO THE
;* ROUTINE AND THEN ONCE EACH MILLI-SECOND THERE AFTER.
;* UPON RETURN, THE LAST WORD WHICH WAS READ TO CHECK FOR THE CONDITION
;* IS RETURNED BY THIS SUBROUTINE.
;*
;* INPUTS: R1 - TIME-OUT VALUE IN MILLI-SECONDS (UP TO 64K MS).
;* R2 - BIT MAP OF BITS TO TEST (1 INDICATES TO TEST THE BIT).
;* R3 - DESIRED STATES OF THE INDICATED FIELDS IN R2.
;* R4 - ADDRESS OF THE WORD TO TEST.
;* MSLCNT - MILLI SECOND SOFTWARE LOOP COUNT.
;*
;* OUTPUTS: R0 - THE LAST WORD WHICH WAS READ TO CHECK FOR THE CONDITION.
;* R1 - REMAINING NUMBER OF MS IN TIME-OUT TIME.
;* CARRY - SUCCESS FLAG (SET IF CONDITION IS MET BEFORE TIME-OUT).
;*
;* CALLING SEQUENCE: JSR PC,MSLGET
;*
;* COMMENTS: THIS ROUTINE WORKS WITH OR WITHOUT A HARDWARE CLOCK, BUT THE
;* CALIBRATION IS ONLY GUARENTEED WHEN A LINE CLOCK IS AVAILABLE
;* ON THE SYSTEM.
;* THIS ROUTINE CAN BE USED AS A DELAY ROUTINE, BY SPECIFYING THE
;* DESIRED DELAY AS THE TIME-OUT AND SPECIFYING A CONDITION TO
;* LOOK FOR WHICH WILL NOT BE MET DURING THE DELAY.
;* IF A TIME-OUT VALUE OF 0 IS SPECIFIED, THIS ROUTINE CHECKS FOR
;* THE DESIRED CONDITION BEFORE RETURNING. IT INDICATES SUCCESS
;* IF THE CONDITION IS MET, FAILURE OTHERWISE.
;*
;* SUBORDINATE ROUTINES CALLED: NONE.
;*****
MSLGET:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
; JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
;*
;* SET UP MASK FOR REMOVING UNUSED BITS IN THE TEST WORD, AND CLEAR UNUSED
;* BITS IN THE DESIRED STATE WORD TO ALLOW DIRECT COMPARISON.
;--
COM R2 ;GET MASK OF UNUSED BITS.
BIC R2,R3 ;MASK OUT UNUSED BITS IN DESIRED STATE WORD.
;*
;* HANDLE THE TEST AND EXIT IF WE HAVE A 0 TIME-OUT VALUE.
;--
TST R1 ;TEST THE TIME-OUT VALUE FOR ZERO.
BNE 2$ ;IF NON-ZERO TIME-OUT, GO LOOP AND TEST.
MOV (R4),R0 ;GET THE WORD TO TEST BEFORE EXITING.
MOV R0,62$ ;SAVE VALUE SO WE CAN RETURN IT.
BIC R2,R0 ;MASK OUT UNTESTED BITS OF WORD.
CMP R0,R3 ;COMPARE AGAINST DESIRED STATE WORD.
SEC ;INDICATE SUCCESS IN CASE WORDS ARE EQUAL.
BEQ 6$ ;EXIT WITH SUCCESS IF WORDS ARE EQUAL.
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2775 020216 000241          CLC          ;INDICATE FAILURE (TIME-OUT).
2776 020220 000416          BR          6$          ;EXIT WITH FAILURE, WORDS AREN'T EQUAL.
2777                          ;+
2778                          ; NON-ZERO TIME-OUT VALUE. LOOP, WAITING FOR CONDITION OR TIME-OUT.
2779                          ;-
2780 020222 016705 162130 2$:  MOV      MSLCNT,R5      ;LOAD MS LOOP COUNT.
2781 020226 011400          4$:  MOV      (R4),R0      ;GET THE WORD TO TEST.
2782 020230 010067 000042  ;SAVE WORD IN CASE THIS IS THE LAST.
2783 020234 040200          ;MASK OUT UNTESTED BITS OF WORD.
2784 020236 020003          ;COMPARE AGAINST DESIRED STATE WORD.
2785 020240 000261          SEC          ;SET CARRY IN CASE OF SUCCESS.
2786 020242 001405          BEQ          6$          ;EXIT WITH SUCCESS IF WORDS ARE EQUAL.
2787 020244 005305          DEC          R5          ;COUNT DOWN THE INSIDE MS LOOP COUNT.
2788 020246 001367          BNE          4$          ;LOOP IF MS NOT UP.
2789 020250 005301          DEC          R1          ;DECREMENT THE MS TIME COUNT.
2790 020252 001363          BNE          2$          ;IF TIME NOT UP, LOOP TO COUNT ANOTHER MS.
2791 020254 000241          CLC          ;CLEAR CARRY, WE TIMED-OUT.
2792                          ;+
2793                          ; HAVE EITHER FOUND CONDITION, OR TIMED-OUT (POSSIBLY FROM 0 TIME-OUT VALUE).
2794                          ; RESTORE THE LAST CONTENTS READ FROM THE TEST WORD. EXIT ROUTINE.
2795                          ;-
2796 020256 016700 000014 6$:  MOV      62$,R0      ;PASS OUT THE LAST READ WORD.
2797 020262 010066 000002 60$:  PASS      R0,R1      ;RESTORE GPRS, EXCEPT THE FOLLOWING:
                                ;RO,ROSLOT(SP)      ;PUT RO IN STACK SLOT.
                                ;R1,R1SLOT(SP)      ;PUT R1 IN STACK SLOT.
                                ;PC,@(SP)+          ;RETURN TO PREGOS SUBRT.
                                ;RO - LAST READ WORD CHECKED FOR CONDITION.
                                ;R1 - REMAINING TIME (0 IF TIME-OUT OCCURED).
                                ;CARRY - SET IF SUCCESS, CLEAR IF TIME-OUT.
2798                          ;
2799                          ;
2800 020274 000207          RTS          PC
2801                          ;+
2802                          ; LOCAL STORAGE.
2803                          ;-
2804 020276 000000          62$:  .WORD      0          ;STORAGE FOR THE LAST READ WORD.

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020300 004567 163472

020304 004767 177654

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020310 004736
020312 000207

```
.SBTTL GLOBAL SUBROUTINE - MSLOOP -
:*****
:* - TEST LOOP SUBROUTINE -
:* THIS SUBROUTINE IS A GENERAL PURPOSE TEST LOOP SUBROUTINE. IT IS USED
:* TO VERIFY THAT A CERTAIN ACTION OCCURS BEFORE A TIME-OUT PERIOD. THE
:* CALLING ROUTINE PASSES IN WHICH BITS SHOULD BE SET AND CLEARED FOR THE
:* DESIRED CONDITION AND THE TIME-OUT VALUE IN MILLI-SECONDS.
:* THIS ROUTINE CHECKS FOR THE DESIRED CONDITION UPON ENTRANCE INTO THE
:* ROUTINE AND THEN ONCE EACH MILLI-SECOND THEREAFTER.
:*
:* INPUTS: R1 - TIME-OUT VALUE IN MILLI-SECONDS (UP TO 64K MS).
:* R2 - BIT MAP OF BITS TO TEST (1 INDICATES TO TEST THE BIT).
:* R3 - DESIRED STATES OF THE INDICATED FIELDS IN R2.
:* R4 - ADDRESS OF THE WORD TO TEST.
:* MSLCNT - MILLI SECOND SOFTWARE LOOP COUNT.
:*
:* OUTPUTS: CARRY - SUCCESS FLAG (SET IF CONDITION IS MET BEFORE TIME-OUT).
:*
:* CALLING SEQUENCE: JSR PC,MSLOOP
:*
:* COMMENTS: THIS ROUTINE WORKS WITH OR WITHOUT A HARDWARE CLOCK, BUT THE
:* CALIBRATION IS ONLY GUARENTEED WHEN A LINE CLOCK IS AVAILABLE
:* ON THE SYSTEM.
:* THIS ROUTINE CAN BE USED AS A DELAY ROUTINE, BY SPECIFYING THE
:* DESIRED DELAY AS THE TIME-OUT AND SPECIFYING A CONDITION TO
:* LOOK FOR WHICH WILL NOT BE MET DURING THE DELAY.
:* IF A TIME-OUT VALUE OF 0 IS SPECIFIED, THIS ROUTINE CHECKS FOR
:* THE DESIRED CONDITION BEFORE RETURNING. IT INDICATES SUCCESS
:* IF THE CONDITION IS MET, FAILURE OTHERWISE.
:*
:* SUBORDINATE ROUTINES CALLED: MSLGET.
:*****
MSLOOP:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
; JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
:*
:* CALLING THE MSLGET ROUTINE FROM THE MSLOOP ROUTINE ISOLATES THE CALLER OF
:* MSLOOP FROM THE RETURNED TEST WORD AND REMAINING TIME-OUT VALUES.
:*
; JSR PC,MSLGET ;CALL THE MULTI-PURPOSE MS LOOP AND SEARCH RTN.
60$: PASS ;RESTORE GPRS,
; JSR PC,@(SP); RETURN TO PREG05 SUBRT.
RTS PC ;CARRY - SET IF SUCCESS, CLEAR IF TIME-OUT.
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      020314 004567 163456
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2871 020320
      020320 104454
      020322 000145
      020324 020360
      020326 000000
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2873 020330
      020330 012746 020444
      020334 012746 000001
      020340 010600
      020342 104417
      020344 062706 000004
2874 020350
      020350 104422
2875 020352 000776
2876 020354
      020354 004736
      020356 000207
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2879 020360 110 117 123
      020363 124 040 103
      020366 117 115 120
      020371 125 124 105
      020374 122 040 110
      020377 101 122 104
      020402 127 101 122
      020405 105 040 117
      020410 122 040 123
      020413 117 106 124
      020416 127 101 122
      020421 105 040 102
      020424 125 107 040
      020427 105 116 103
      020432 117 125 116
      020435 124 105 122
    
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.SBTTL GLOBAL SUBROUTINE - OOPS -
; * *****
; * - PROGRAM ABORT SUBROUTINE. -
; * THIS SUBROUTINE IS USED TO ABORT THE PROGRAM WHEN A FATAL ERROR IS
; * DETECTED IN THE PROGRAM OR THE HOST SYSTEM HARDWARE. AN ERROR MESSAGE
; * IS PRINTED GIVING SOME INFORMATION ABOUT THE NATURE OF THE ABORT.
; *
; * INPUTS: R1 - ERROR CODE GIVING REASON FOR ABORT.
; *
; * OUTPUTS: AN ERROR MESSAGE IS PRINTED.
; * A LIST OF RETURN PC VALUES FOR ALL SUBROUTINE CALLS IS PRINTED.
; *
; * CALLING SEQUENCE: JSR PC,OOPS
; *
; * COMMENTS:
; *
; * SUBORDINATE ROUTINES CALLED: NONE.
; - - *****

OOPS:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
      JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
; REPORT "HOST COMPUTER HARDWARE OR SOFTWARE BUG ENCOUNTERED." ERROR.
      ERRSF 101,EM0101

; REPORT "PROGRAM HUNG, WAITING FOR A CONTROL-C."
      PRINTF #EM0102

      MOV #EM0102,-(SP)
      MOV #1,-(SP)
      MOV SP,R0
      TRAP C$PNTF
      ADD #4,SP
2$: BREAK ;LOOK FOR OPERATOR CONTROL-C INPUT.
      TRAP C$BRK
60$: BR 2$ ;INFINITE LOOP.
      PASS ;DON'T NEED THIS, BUT SOMEBODY MAY CHANGE THIS
      JSR PC,@(SP) ;RETURN TO PREG05 SUBRT.
      RTS PC ; ROUTINE IN THE FUTURE, SO BE CONSISTANT.

EM0101:: .ASCIZ /HOST COMPUTER HARDWARE OR SOFTWARE BUG ENCOUNTERED./
    
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	020443	000		
2880	020444	045	116	045
	020447	101	120	122
	020452	117	107	122
	020455	101	115	040
	020460	110	125	116
	020463	107	054	040
	020466	127	101	111
	020471	124	111	116
	020474	107	040	106
	020477	117	122	040
	020502	101	040	103
	020505	117	116	124
	020510	122	117	114
	020513	055	103	056
	020516	040	074	052
	020521	052	052	052
	020524	052	052	052
	020527	052	052	052
	020532	052	052	052
	020535	045	116	045
2881	020540	116	000	

EM0102:: .ASCIZ /N#APROGRAM HUNG, WAITING FOR A CONTROL-C. <*****N#N/

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020542 004567 163230
020546 012701 001000
020552 016704 161466

020556 011402
020560 100016

020562 012700 070000
020566 040200
020570 001006

020572 012700 000301
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020600 001002
020602 004767 001470

020606 005301
020610 001362
020612 000241
020614 000401
020616 000261

020620
020620 004736

020622 000207

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.SBTTL GLOBAL SUBROUTINE - PUFIFO -
;*****
;* - PURGE THE FIFO
;* THIS ROUTINE TRIES TO REMOVE ALL THE CHARACTERS FROM THE FIFO.
;* ANY BMP CODES THAT ARE FOUND ARE SAVED ON THE BMP CODE QUEUE.
;*
;* INPUTS: RBUFA- CONTAINS THE ADDRESS OF THE RECEIVER.
;*
;*
;* OUTPUTS: CARRY BIT - INDICATES THE STATE OF THE FIFO, SET:= PURGED.
;* BMPCQ - THE CONTENTS OF THE BMP CODE QUEUE MAY BE UPDATED.
;*
;* CALLING SEQUENCE: JSR PC,PUFIFO
;*
;* COMMENTS:
;*
;* SUBORDINATE ROUTINES CALLED: SAVBMP.
;*****
PUFIFO::SAVE
;SAVE CONTENTS OF GPRS R0 THRU R5.
;R5,PREG05 ;CALL REGISTER SAVE SUBRT.
;SET MAXIMUM TRY COUNT OF 512.
;GET ADDRESS OF THE RECEIVER BUFFER REGISTER.
JSR R5,PREG05
MOV #512.,R1
MOV RBUFA,R4
2$: MOV (R4),R2 ;GET THE CONTENTS OF THE RECEIVER BUFFER REG.
BPL 6$ ;EXIT IF THE FIFO IS EMPTY, DATA_VALID CLR.
;
; * CHECK IF THE READ CHARACTER IS ACTUALLY A BMP CODE.
; IF IT IS, THEN SAVE IT ON THE BMP CODE QUEUE TO BE REPORTED LATER.
;
; -
MOV #70000,R0 ;GENERATE A BIT MAP OF CHAR ERROR BITS
BIC R2,R0 ;WHICH ARE NOT SET FOR CHAR.
BNE 4$ ;THROW CHAR AWAY IF NOT BMP OR SELFTEST CODE.
;
; * CHECK IF THE READ DATA IS MODEM STATUS , BMP OR SELFTEST?.
;
; -
MOV #301,R0 ; CHECK IF BMP.
BIC R2,R0 ;TRY TO CLEAR BMP FLAGS IN THE READ DATA.
BNE 4$ ;IF IT IS MODEM OR SELFTEST CODE THROW IT AWAY.
JSR PC,SAVBMP ;SAVE BMP CODE ON THE QUEUE.
4$: DEC R1 ;DECREMENT THE TRY COUNT.
BNE 2$ ;LOOP TO TRY AGAIN.
CLC ;CLEAR CARRY, TO INDICATE FIFO NOT PURGED.
BR 60$ ;EXIT WITH CARRY CLEAR.
6$: SEC ;SET CARRY, TO INDICATE FIFO PURGED.
60$: PASS
;RESTORE GPRS.
;PC,@(SP)+ ;RETURN TO PREG05 SUBRT.
JSR PC,@(SP)+
RTS PC ;CARRY BIT, SET INDICATES FIFO PURGED.
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020624 004567 163146
020630 012767 016416 163136
020636 005704
020640 100001
020642 005102
020644 005005

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.SBTTL GLOBAL SUBROUTINE - RDPDR -
;*****
;* - READ AND VERIFY DATA PATTERN FROM DEVICE REGISTERS ROUTINE -
;* THIS ROUTINE READS AND VERIFIES THE ROTATED DATA PATTERN WHICH HAS
;* BEEN WRITTEN BY THE WDPDR SUBROUTINE.
;* EACH ACTIVE LINE'S REGISTER'S CONTENTS IS READ AND COMPARED WITH THE
;* WRITTEN DATA.
;* AFTER THE UNUSED AND READ ONLY (RO) BITS ARE MASKED OUT, ANY ERRORS ARE
;* REPORTED FROM THIS ROUTINE.
;* THIS ROUTINE WILL TAKE INTO ACCOUNT THE TYPE OF WRITE OPERATION WHICH
;* WAS PERFORMED BY THE WDPDR SUBROUTINE.
;*
;* INPUTS:      R2 - USED TO PASS IN THE DATA PATTERN TO BE ROTATED & VERIFIED.
;*              R3 - BYTE INDICATOR (- => LO BYTE, + => HI BYTE, 0 => BOTH).
;*              R4 - OPERATION TYPE INDICATOR (- => BIC, + => BIS, 0 => MOV).
;*              ACTLNS - BIT MAP OF ACTIVE LINES ON THE DEVICE UNDER TEST.
;*              CSRA - CONTAINS THE CSR ADDRESS OF THE DEVICE UNDER TEST.
;*              DRADRT - BASE ADDRESS OF DEVICE REGISTER ADDRESS TABLE.
;*              ERCNTB - LABEL AT BASE OF ERROR COUNTERS TABLE FOR LINES.
;*              ERRMSG - SET UP WITH THE PROPER ERROR MESSAGE FOR THIS TEST.
;*              ERRNBR - SET UP WITH THE PROPER ERROR NUMBER.
;*              LPRO - EQUATED TO LPR REG OFFSET FROM DEVICE CSR ADDRESS.
;*              NUMLNS - NUMBER OF LINES ON THE DEVICE UNDER TEST.
;*              NDERPT - NUMBER OF INDIVIDUAL DATA ERRORS TO REPORT ON A LINE.
;*              TXBFCO - EQUATED TO TBUFFCT REG OFFSET FROM DEVICE CSR ADDRESS.
;*              UNBTB - BASE ADDRESS OF THE UNUSED BIT TABLE.
;*
;* OUTPUTS:     ERROR MESSAGES MAY BE PRINTED AT THE OPERATOR'S CONSOLE.
;*              ERCNT - ERROR COUNTERS TABLE IS UPDATED FOR LINE UNDER TEST.
;*              ERRBLK - CONTENTS DESTROYED.
;*              ERSMRF - ERROR SUMMARY FLAGS BIT SET IF LINE IN SUMMARY MODE.
;*              UUT CSR - ALL BITS CLEARED, EXCEPT IND.ADR.REG FIELD DESTROYED.
;*
;* CALLING SEQUENCE:  JSR    PC,RDPDR
;*
;* COMMENTS:         FOR BYTE ACCESSES, ONLY THE SPECIFIED BYTE IS VERIFIED.
;*
;* SUBORDINATE ROUTINES CALLED: ER1601,ROLDAP.
;-- *****
RDPDR:: SAVE          ;SAVE CONTENTS OF GPRS R0 THRU R5.
                JSR    R5,PREG05 ;CALL REGISTER SAVE SUBRT.
                MOV    #ER1601,ERRBLK ;SET UP THE ADDRESS OF THE ERROR REPORT RTN.
;*
;* DETERMINE WHETHER REGISTER DATA SHOULD BE INVERTED FROM DATA PATTERN.
;--
                TST    R4          ;CHECK THE OPERAND TYPE INDICATOR.
                BPL    2$          ;BIC WRITE PERFORMED? NO, USE STANDARD DATA.
                COM    R2          ;YES, INVERT THE DATA PATTERN.
;*
;* SET UP OUTER LOOP.
;--
2$: CLR    R5          ;CLEAR LINE COUNTER TO SELECT LINE 0.
;*
;* THE OUTER LOOP FOLLOWS. EACH PASS THROUGH THIS LOOP READS AND COMPARES DATA
;* FROM ALL OF THE DEVICE REGISTERS FOR A PARTICULAR LINE IF THE LINE IS ACTIVE.
;--
```

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2989 020646 010267 000222      4$:  MOV    R2,R0          ;SAVE THE OUTER LOOP DATA PATTERN.
2990 020652 010577 161364      MOV    R5,@CSRA       ;SET CSR IND.ADR.REG FIELD TO THIS LINE.
2991 020656 010500              MOV    R5,R0
2992 020660 006300              ASL    R0
2993 020662 036067 002410 161340 BIT    BITBL(R0),ACTLNS
2994 020670 001467              BEQ    16$            ;IS THE LINE ACTIVE? NO, SKIP THE LINE.
2995 020672 012703 000004      MOV    @LPRO,R3       ;YES, INITIALIZE REGISTER OFFSET FOR LPR.
2996
2997      ;
2998      ; THE INNER LOOP FOLLOWS. EACH PASS THROUGH THIS LOOP READS AND COMPARES
2999      ; DATA FROM A DEVICE REGISTER.
3000 020676 010204              6$:  MOV    R2,R4          ;SAVE THE INNER LOOP DATA PATTERN.
3001 020700 046302 002370      BIC    UNBITB(R3),R2  ;REMOVE UNUSED BITS FROM EXPECTED DATA.
3002 020704 016300 002242      MOV    DRADRT(R3),R0
3003 020710 005766 000010      TST    R3SLOT(SP)    ;CHECK THE ACCESS TYPE INDICATOR.
3004 020714 001002              BNE    8$            ;BYTE ACCESS? YES, GO PERFORM BYTE READ.
3005 020716 011001              MOV    (R0),R1       ;NO, PERFORM WORD READ OF DEVICE REGISTER.
3006 020720 000416              BR     12$
3007 020722 100410              8$:  BMI    10$          ;LOW BYTE ACCESS? YES, GO DO LOW BYTE READ.
3008 020724 005200              INC    R0             ;HIGH BYTE ACCESS. FORM HIGH BYTE ADDRESS.
3009 020726 111001              MOVB   (R0),R1       ;READ THE HI BYTE OF THE DUT REGISTER.
3010 020730 000301              SWAB   R1            ;PUT HI BYTE BACK INTO THE HI BYTE.
3011 020732 042701 000377      BIC    @377,R1       ;REMOVE THE UNUSED BYTE IN ACTUAL DATA.
3012 020736 042702 000377      BIC    @377,R2       ;REMOVE THE UNUSED BYTE IN EXPECTED DATA.
3013 020742 000405              BR     12$
3014 020744 111001              10$: MOVB   (R0),R1       ;READ THE LOW BYTE OF THE DUT REGISTER.
3015 020746 042701 177400      BIC    @177400,R1    ;REMOVE THE UNUSED BYTE.
3016 020752 042702 177400      BIC    @177400,R2    ;FORM EXPECTED LOW BYTE FOR COMPARISON.
3017
3018 020756 046301 002370      12$: BIC    UNBITB(R3),R1  ;REMOVE UNUSED BITS FROM ACTUAL DATA.
3019 020762 020102              CMP    R1,R2         ;COMPARE ACTUAL AND EXPECTED DATA.
3020 020764 001414              BEQ    14$          ;ACTUAL = EXPECTED? YES, SKIP ERROR.
3021 020766 004767 177054      JSR    PC,CNTERR     ;NO, COUNT THE ERROR, CHECK FOR ERROR SUMMARY.
3022 020772 103411              BCS   14$          ;USE ERROR SUMMARY? YES, SKIP ERROR.
3023
3024 020774              ;NO, REPORT "BAD BIT(S) IN DEVICE XXXXX REGISTER FOR LINE NN (D)."  

      020774 104460      ERROR
3025
3026      ; TRAP C$ERROR
3027      ;EXIT THIS ROUTINE AND SET THE "EXIT ON ERROR" FLAG, IF EXTENDED ERROR
3028      ;REPORTING HAS NOT BEEN REQUESTED.
3029 020776 032767 000100 161220      ;
3030 021004 001004              BIT    @BIT06,OPTION ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
3031 021006 012767 000001 161270      BNE    14$          ;BRANCH IF IT HAS.
3032 021014 000425              MOV    @1,EXOERR     ;SET THE EXIT ON ERROR FLAG.
3033              BR     60$      ;EXIT THE ROUTINE.
3034 021016 010402              14$: MOV    R4,R2          ;RESTORE THE INNER LOOP DATA PATTERN.
3035 021020 004767 000444      JSR    PC,ROLDAP     ;ROTATE DATA PATTERN LEFT, NOT THROUGH CARRY.
3036 021024 062703 000002      ADD    @2,R3         ;SET REGISTER OFFSET TO THE NEXT REGISTER.
3037 021030 020327 000006      CMP    R3,@FSLSO    ;CHECK THAT THIS IS NOT THE FIFOSIZE/DATA REG.
3038 021034 001002              BNE    15$          ;AVOID ALTERING THE OFFSET IF IT ISN'T
3039 021036 062703 000002      ADD    @2,R3         ;POINT AT THE NEXT REGISTER.
3040 021042 020327 000016      15$: CMP    R3,@TXBFCO ;COMPARE REG OFFSET WITH OFFSET OF LAST REG.
3041 021046 003713              BLE    6$            ;LOOP IF NOT ALL REG DONE FOR THIS LINE.
3042
3043      ;
3044      ; BACK INTO THE OUTER LOOP. NOW SET UP FOR NEXT LINE. LOOP IF NOT DONE.
    
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 3082 021076
 021076 004567 162674
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 3086 021102 012705 000020
 3087 021106 012702 167410
 3088 021112 032704 000001
 3089 021116 001001
 3090 021120 005004
 3091 021122
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 3095 021122 010400
 3096 021124 004767 001272
 3097 021130 016701 162634
 3098 021134 010004
 3099 021136 005404
 3100 021140 005002
 3101 021142 026627 000012 000002
 3102 021150 001401
 3103 021152 005102
 3104 021154 005003
 3105 021156 005000
 3106 021160 026627 000012 177776
 3107 021166 001001
 3108 021170 005100
 3109 021172 004767 001224
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.SBTTL GLOBAL SUBROUTINE - REGTST -
;*****
;* - REGISTERS TEST SUBROUTINE -
;* SUBROUTINE TO TEST THE DEVICE UNDER TEST (DUT) REGISTERS. THE USED
;* BITS OF THE REGISTERS ARE EITHER ALL CLEARED OR ALL SET AND THEN THE
;* DATA PATTERN IS WRITTEN AND VERIFIED USING EITHER WORD OR BYTE
;* ACCESSES IN READ/WRITE OR READ/MODIFY/WRITE MODE.
;*
;* INPUTS: R3 - BYTE INDICATOR (- => LOW, + => HIGH, 0 => BOTH BYTES).
;* R4 - ACCESS MODE (-1 => SET THEN BIC, 1 => CLEAR THEN BIS,
;* (-2 => SET THEN MOV, +2 CLEAR THEN MOV).
;* ERRNBR - SET UP WITH INITIAL ERROR NUMBER.
;*
;* OUTPUTS: GPRS0 - GPR SAVE AREA 0 IS DESTROYED.
;* DEVICE UNDER TEST REGISTERS ARE WRITTEN.
;* ERROR MESSAGES MAY BE PRINTED AT THE OPERATORS CONSOLE.
;*
;* CALLING SEQUENCE: JSR PC,REGTST
;*
;* COMMENTS: THIS ROUTINE LOOP 16 TIMES WRITING THE SAME DATA PATTERN
;* ROTATED LEFT ONCE EACH ITERATION.
;* THIS ROUTINE CAN REPORT ERRORS INITIAL ERRNBR THRU INITIAL+2.
;*
;* SUBORDINATE ROUTINES CALLED: RDPDR,ROLDAP,SWAPO,WDPDR
;*****
REGTST:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
; JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
;
; SET UP THE GPRS FOR THE WRITING OF THE DATA PATTERN.
;
; MOV #16,R5 ;SET UP LOOP COUNTER TO COUNT 16 ITERATIONS.
; MOV #167410,R2 ;INITIALIZE THE DATA PATTERN.
; BIT #BIT0,R4 ;TEST FOR R/W ACCESS.
; BNE 2$ ;R/M/W ACCESS? YES, R4 IS ALL SET UP.
; CLR R4 ;NO. INDICATE R/W ACCESS.
2$:
;
; SET UP THE GPRS FOR THE CLEARING OR SETTING OF ALL THE USED BITS.
;
; MOV R4,R0 ;PASS OPERATION TYPE INDICATOR AROUND SWAPO.
; JSR PC,SWAPO ;GET ALTERNATE GPR SET IN R1 THRU R5.
; MOV ERRNBR,R1 ;SAVE THE INITIAL ERROR NUMBER.
; MOV R0,R4
; NEG R4 ;SET UP OP TYPE FOR CLEARING OR SETTING.
; CLR R2 ;SET UP CLEAR WRITE PATTERN.
; CMP R4,SLOT(SP),#2 ;TEST FOR CLEAR THEN MOV TEST SEQUENCE.
; BEQ 4$ ;CLEAR THEN MOV? YES, LEAVE WRITE PAT CLEAR.
; COM R2 ;NO, SET ALL BITS OF WRITE PATTERN.
; CLR R3 ;INDICATE THAT WORD ACCESSES SHOULD BE USED.
; CLR R0 ;SET ALTERNATE BYTE EXPECTED DATA PAT TO CLEAR.
; CMP R4,SLOT(SP),#-2 ;TEST FOR SET THEN MOV TEST SEQUENCE.
; BNE 6$ ;SET THEN MOV? YES, LEAVE ALT BYTE PAT CLEAR.
; COM R0 ;NO, SET ALT BYTE EXPECTED DATA PAT TO ALL 1'S.
; JSR PC,SWAPO ;RESTORE SWAPPED GPR VALUES TO R1 THRU R5.
6$:
;
; START OF DATA PATTERN LOOP.
    
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3112
3113 021176      8$:
3114
3115      ;*
3116      ; SET OR CLEAR ALL THE USED BITS OF THE DEVICE REGISTERS FOR ALL LINES.
3117      ; VERIFY THAT ALL THE BITS WERE SET OR CLEARED CORRECTLY.
3118 021176 004767 001220      ;-
3119 021202 004767 002136      JSR    PC,SWAPO      ;GET ALTERNATE GPRS FOR SETTING INTIAL STATES.
3120 021206 010167 162556      JSR    PC,WDPDR      ;GO CLEAR ALL USED REGISTER BITS, ALL LINES.
3121 021212 004767 177406      MOV    R1,ERRNBR     ;SET UP ERROR NUMBER TO INITIAL ERRNBR.
3122      JSR    PC,RDPDR     ;VERIFY ALL USED REGISTER BITS, ALL LINES.
3123      ;*
3124      ;EXIT THIS ROUTINE IF AN ERROR HAS BEEN FOUND AND EXTENDED ERROR REPORTING
3125      ;HAS BEEN REQUESTED, I.E. EXOERR IS NON-ZERO.
3126 021216 005767 161062      ;-
3127 021222 001035      TST    EXOERR        ;HAS AN ERROR BEEN FOUND ?
3128      BNE    60$      ;EXIT THIS ROUTINE IF IT HAS.
3129 021224 004767 001172      JSR    PC,SWAPO      ;RESTORE MAIN GPRS CONTENTS.
3130
3131      ;*
3132      ; WRITE DATA PATTERNS, ALL LOWER BYTE USED BITS, ALL REGISTERS, ALL LINES.
3133      ; VERIFY THAT THE DATA PATTERN WAS WRITTEN CORRECTLY.
3134 021230 004767 002110      ;-
3135 021234 005267 162530      JSR    PC,WDPDR      ;WRITE DATA PATTERN TO DEVICE REGISTERS.
3136 021240 004767 177360      INC    ERRNBR        ;SET ERROR NUMBER TO INITIAL+1.
3137      JSR    PC,RDPDR     ;VERIFY DATA PATTERN IN ALTERED BYTE(S).
3138      ;*
3139      ;EXIT THIS ROUTINE IF AN ERROR HAS BEEN FOUND AND EXTENDED ERROR REPORTING
3140      ;HAS BEEN REQUESTED.
3141 021244 005767 161034      ;-
3142 021250 001022      TST    EXOERR        ;HAS AN ERROR BEEN FOUND ?
3143      BNE    60$      ;EXIT THIS ROUTINE IF IT HAS.
3144 021252 005703      TST    R3            ;CHECK THE BYTE INDICATOR.
3145 021254 001414      BEQ    10$          ;WORD ACCESS? YES, SKIP SECOND BYTE CHECK.
3146
3147      ;*
3148      ; CHECK THAT THE ALTERNATE (UNMODIFIED) BYTE IS CLEAR OR SET AS EXPECTED.
3149 021256 010201      ;-
3150 021260 010002      MOV    R2,R1        ;SAVE THE DATA PATTERN.
3151 021262 005403      MOV    R0,R2        ;GET THE ALTERNATE BYTE EXPECTED DATA.
3152 021264 005267 162500      NEG    R3            ;INDICATE THAT OTHER BYTE IS TO BE CHECKED.
3153 021270 004767 177330      INC    ERRNBR        ;SET ERROR NUMBER TO INITIAL+2.
3154      JSR    PC,RDPDR     ;VERIFY DATA PATS IN OTHER BYTES OF REGISTERS.
3155      ;*
3156      ;EXIT THIS ROUTINE IF AN ERROR HAS BEEN FOUND AND EXTENDED ERROR REPORTING
3157      ;HAS BEEN REQUESTED.
3158 021274 005767 161004      ;-
3159 021300 001006      TST    EXOERR        ;HAS AN ERROR BEEN FOUND ?
3160      BNE    60$      ;EXIT THIS ROUTINE IF IT HAS.
3161 021302 005403      NEG    R3            ;RESTORE BYTE INDICATOR.
3162 021304 010102      MOV    R1,R2        ;RESTORE DATA PATTERN.
3163
3164      ;*
3165      ; PEPAARE THE NEXT DATA PATTERN AND LOOP IF NOT DONE.
3166 021306 004767 000156      ;-
3167 021312 005305      10$: JSR    PC,ROLDAP     ;ROTATE DATA PATTERN LEFT, NOT THROUGH CARRY.
3168 021314 003330      DEC    R5            ;COUNT THIS ITERATION OF THE LOOP.
3169      BGT    8$        ;ALL PATTERNS DONE? NO, LOOP.
```

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3169  
3170 021316 016767 161126 162444 60$: MOV GPRSOB,ERRNBR ;YES, RESTORE ERROR NUMBER AND EXIT.  
3171 021324 004736 PASS ;GET THE ERROR NUMBR FROM GPR SWAP STORAGE.  
021324 004736 ;RESTORE GPRS.  
3172 021326 000207 RTS PC JSR PC,0(SP)+ ;RETURN TO PREG05 SUBRT.
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021330
021330 004567 162442
021334 005767 161122
021340 001404

021342 012767 016722 162424

021350
021350 104460

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021352 004736
021354 000207

```
.SBTTL GLOBAL SUBROUTINE - REPSMR -
;+ *****
;+ - REPORT ERROR SUMMARY ROUTINE -
;+ THIS SUBROUTINE REPORTS AN ERROR SUMMARY FOR THOSE LINES WHICH HAVE
;+ EXCEEDED THE NUMBER OF INDIVIDUAL ERRORS TO REPORT FOR A SINGLE LINE
;+ IN A SINGLE TEST. THIS PARAMETER CAN BE SPECIFIED BY THE OPERATOR IF
;+ HE/SHE ANSWERS THE SOFTWARE PARAMETER QUESTIONS.
;+
;+ INPUTS:   ERCNTB - LABEL AT BASE OF LINE ERROR COUNTERS TABLE.
;+           ERRMSG - ADDRESS OF PRIMARY ERROR MESSAGE.
;+           ERRNBR - ERROR NUMBER OF ERRORS IN THIS ROUTINE.
;+           ERSMRF - "REPORT ERROR SUMMARY FOR LINE" FLAGS.
;+
;+ OUTPUTS:  ERRBLK - ADDRESS OF ERROR REPORTING ROUTINE (DESTROYED).
;+           SUMMARY MESSAGES MAY BE PRINTED AT THE OPERATOR CONSOLE.
;+
;+ CALLING SEQUENCE:  JSR   PC,REPSMR
;+
;+ COMMENTS:  IF NO LINES HAVE EXCEEDED THE MAXIMUM NUMBER OF INDIVIDUAL
;+           ERRORS TO REPORT, NO MESSAGES ARE PRINTED BY THIS ROUTINE.
;+           ERROR SUMMARIES IN THIS ROUTINE ARE REPORTED AS ERRORS.
;+           THE CONTENTS OF ERRBLK ARE DESTROYED.
;+
;+ SUBORDINATE ROUTINES CALLED:
;+ - - - - -
REPSMR:: SAVE
;SAVE CONTENTS OF GPRS R0 THRU R5.
;CALL REGISTER SAVE SUBRT.
TST   ERSMRF   JSR
BEQ   60$     ;CHECK THE "PRINT LINE ERROR SUMMARY" FLAGS.
;EXIT WITHOUT ACTION IF NO SUMMARY FLAGS SET.
;+
;+ WE HAVE SOME ERROR SUMMARIES TO REPORT.
;+
;+ MOV   @ER9004,ERRBLK ;SELECT ERROR REPORTING ROUTINE.
;+
;+ REPORT
;+ "ERROR SUMMARY REPORT FOR LINES WITH EXCESSIVE NUMBERS OF ERRORS:"
;+
;+ ERROR
;+
;+ TRAP   C$ERROR
60$:  PASS
;RESTORE GPRS.
RTS   PC     JSR   PC,@(SP) ;RETURN TO PREG05 SUBRT.
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3241 021356
021356 004567 162414
3242 021362 012702 000040
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3248 021366 016704 160650
3249 021372 030214
3250 021374 001406
3251 021376 005003
3252 021400 012701 011610
3253 021404 004767 176554
3254 021410 103012
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3261 021412 010277 160624
3262 021416 004767 000722
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3268 021422 005003
3269 021424 012701 011610
3270 021430 004767 176530
3271 021434 103410

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.SBTTL GLOBAL SUBROUTINE - RESETT -
;*****
; - RESET DEVICE UNDER TEST -
; THIS SUBROUTINE IS USED TO RESET THE DUT TO A KNOWN STATE.
; IF RESET DOES NOT SUCCESSFULLY COMPLETE, IE. TIME-OUT OCCURS, THEN
; AN ABORT TEST ERROR MESSAGE IS REPORTED.
;
; INPUTS: CSRA - CONTAINS THE ADDRESS OF THE CSR
; TXBFCA - CONTAINS ADDRESS OF DUT DMA BUFFER COUNT REGISTER.
; ERRTBL - ERRTP,ERNBR,AND ERRMSG SET UP CORRECTLY.
;
; OUTPUTS: THE DUT PERFORMS ITS RESET FUNCTION INTO A KNOWN STATE.
; CARRY - CLEAR INDICATES THE TEST IS TO BE ABORTED.
; ERRLK - VALUE MAY BE DESTROYED.
; IESTAT - TX AND RX INTERRUPT FLAGS ARE CLEARED.
; TX AND RX INTERRUPT ENABLE BITS IN THE DUT'S CSR ARE CLEARED.
;
; CALLING SEQUENCE: JSR PC,RESETT
;
; COMMENTS: THIS SUBROUTINE CAN REPORT ERRORS WITH NUMBERS INITIAL ERNBR
; THIS ROUTINE DOES NOT DESTROY THE VALUE OF ERNBR.
;
; SUBORDINATE ROUTINES CALLED: DELAY,MSLGET.
;*****
RESETT:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
; R5,PREG05 ;CALL REGISTER SAVE SUBRT.
MOV #BIT05,R2 ;SET BIT MASK OF MASTER RESET BIT.
;
; TEST THE STATE OF THE MASTER RESET BIT IN THE CSR.
; IF MR IS SET THEN WAIT FOR SELF-TEST TO COMPLETE.
; IF TIME-OUT OCCURS, REPORT THE ERROR AND PASS-OUT ABORT TEST INDICATOR.
;
MOV CSRA,R4 ;GET THE ADDRESS OF THE DUT'S CSR.
BIT R2,(R4) ;CHECK STATE OF MASTER RESET BIT.
BEG 2$ ;DON'T DELAY IF MR IS ALREADY CLEAR.
CLR R3 ;SET UP DESIRED STATE OF MASTER RESET BIT.
MOV #5000.,R1 ;PASS TIME-OUT VALUE OF 5 SECONDS.
JSR PC,MSLGET ;WAIT FOR SELF-TEST TO COMPLETE, MR CLEAR.
BCC 4$ ;GO REPORT ERROR IF TIMEOUT OCCURRED.
;
; SET MASTER RESET BIT IN CSR. CLEAR TX AND RX ENABLE BITS, ETC.
; SKIP THE SELFTEST.
; TIME-OUT OF 5 SECS. JUST IN CASE THE SELF-TEST EXECUTES.
;
2$: MOV R2,@CSRA ;SET MASTER RESET BIT, DISABLE TX AND RX INTS.
JSR PC,SKPSTS ;TRY TO SKIP THE SELFTEST.
;
; SET SELF-TEST TIME-OUT OF 5 SECONDS, AND WAIT FOR M.R TO CLEAR.
; IF TIME-OUT OCCURS, THEN REPORT THE FATAL ERROR AND PASS-OUT THE ABORT
; TEST INDICATOR.
;
CLR R3 ;SET UP DESIRED STATE OF MASTER RESET BIT.
MOV #5000.,R1 ;PASS TIME-OUT VALUE OF 5 SECONDS.
JSR PC,MSLGET ;WAIT FOR SELF-TEST TO COMPLETE, MR CLEAR.
BCS 6$ ;SKIP ERROR REPORT IF MR CLEARED IN TIME.
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3276 021436 012701 012221
3277 021442 012767 016526 162324
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3280 021450
      021450 104460
3281 021452 000241
3282 021454 000403
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3287 021456 005067 160626
3288 021462 000261
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3290 021464
      021464 004736
3291
3292 021466 000207
3293
; *
; SET UP ERROR MESSAGE TO REPORT "FATAL ERROR FOUND DURING RESET, TEST ABORTED".
; INDICATE TEST IS TO BE ABORTED BY CLEARING THE CARRY BIT.
; -
4$:   MOV     #EM1601,R1      ;PASS ERROR MESSAGE TO REPORT.
      MOV     #ER1603,ERRBLK ;PASS ADDRESS OF ERROR HANDLING ROUTINE.
      ;REPORT ERROR "TIME-OUT OCCURRED WAITING FOR MASTER RESET TO CLEAR"
      ; "TEST ABORTED"
      ERROR
;
      >>>> ERROR <<<<<
;
      CLC
      BR     60$             ;INDICATE TEST IS TO BE ABORTED.
;EXIT THIS SUBROUTINE, ABORT TEST INDICATOR.
; *
; CLEAR TX AND RX INTERRUPT ENABLE STATUS FLAGS IN IESTAT.
; EXIT WITH CONTINUE TEST INDICATOR SET (IE,CARRY SET).
; -
6$:   CLR     IESTAT        ;CLEAR TX AND RX INTERRUPT STATUS FLAGS.
      SEC
;INDICATE SUCCESS, CONTINUE TEST.
; -
60$:  PASS
;RESTORE GPRS, PASS THE FOLLOWING INTACT:
;PC,@(SP)-
;RETURN TO PREG05 SUBRT.
;CARRY BIT:IF CLEAR,INDICATES ABORT TEST.
      JSR
      RTS     PC

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021470
021470 004567 162302
021474 005702
021476 100001
021500 000261
021502 006102
021504
021504 010266 000006
021510 004736
021512 000207

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.SBTTL GLOBAL SUBROUTINE - ROLDAP. -
:*****
:* - ROTATE LEFT DATA PATTERN
:* THIS ROUTINE ROTATES THE PASSED INPUT DATA PATTERN LEFT,WITHOUT GOING
:* THROUGH THE CARRY.THE CARRY IS INITIALLY SET OR CLEARED DEPENDING
:* UPON THE STATE OF THE MSB OF THE DATA PATTERN,BEFORE A ROL INSTRUCTION
:* IS EXECUTED.
:*
:* INPUTS: R2 - CONTAINS THE DATA PATTERN TO BE ROTATED
:*
:* OUTPUTS: R2 - CONTAINS THE ROTATED DATA PATTERN
:*
:* CALLING SEQUENCE: JSR PC,ROLDAP
:*
:* COMMENTS:
:*
:* SUBORDINATE ROUTINES CALLED: NONE
:*****
ROLDAP::SAVE
                TST R2 JSR ;SAVE CONTENTS OF GPRS R0 THRU R5.
                BPL 2$ ;R5,PREG05 ;CALL REGISTER SAVE SUBRT.
                SEC ;CHECK MSB, AND CLEAR CARRY.
                ROL R2 ;BRANCH IF CLEAR.
2$:              ;SET CARRY IF MSB SET
60$:            PASS R2 ;ROTATE DATA PATTERN LEFT
                MOV R2,R2SLOT(SP) ;RESTORE GPRS,EXCEPT ;PUT R2 IN STACK SLOT.
                JSR PC,@(SP) ;RETURN TO PREG05 SUBRT.
                ;R2 - CONTAINS THE ROTATED DATA PATTERN
                RTS PC

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      021514 004567 162256
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3358 021522 016705 162242
3359 021526 017702 160512
3360 021532 100422
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3364 021534 010567 162230
3365 021540 012701 015127
3366 021544 012767 017036 162222
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3371 021552
      021552 104460
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3375 021554 032767 000100 160442
3376 021562 001003
3377 021564 012767 000001 160512

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.SBTTL GLOBAL SUBROUTINE - RSTRPT -
; * *****
; * - REPORT ANY RESET ERRORS ROUTINE -
; * THIS ROUTINE DETERMINES IF ANY ERROR CODES ARE AMONG THE DIAGNOSTIC
; * CODES REPORTED PLACED IN THE DUT RECEIVED CHARACTER FIFO BY THE
; * SELF-TEST. IF ANY NON BMP ERROR CODES ARE FOUND, OR IF OTHER ERRORS
; * ARE ENCOUNTERED, APPROPRIATE ERRORS ARE REPORTED. ANY BMP CODES THAT
; * ARE FOUND, ARE PLACED ON THE BMP CODE QUEUE TO BE REPORTED LATER.
; * THIS ROUTINE ALSO PURGES THE DUT FIFO LOOKING FOR ANY CHARACTERS
; * OR MODEM STATUS CODES. IF ANY ARE FOUND, ERRORS ARE REPORTED.
; *
; * INPUTS: ERRMSG - ADDRESS OF THE PRIMARY ERROR MESSAGE.
; *          ERRNBR - ERROR NUMBER OF FIRST ERROR REPORTED BY THIS ROUTINE.
; *          NUMLNS - EQUATED TO THE NUMBER OF LINE ON THE DUT.
; *          RBUFA - CONTAINS ADDRESS OF THE DUT RECEIVER FIFO.
; *
; * OUTPUTS: CARRY - SUCCESS FLAG (SET IF FIFO CLEARED SUCCESSFULLY).
; *          ERRBLK - ADDRESS OF THE ERROR REPORT ROUTINE (DESTROYED).
; *          ERROR MESSAGES CAN BE PRINTED AT THE OPERATORS CONSOLE.
; *
; * CALLING SEQUENCE: JSR PC,RSTRPT
; *
; * COMMENTS: THIS SUBROUTINE CAN REPORT ERRORS WITH NUMBERS INITIAL ERRNBR
; *           THRU INITIAL ERRNBR+4.
; *           THIS ROUTINE DOES NOT DESTROY THE VALUE OF ERRNBR.
; *
; * SUBORDINATE ROUTINES CALLED: ER0503,ER9007,ER9008,SAVBMP.
; * *****
RSTRPT:: SAVE
; *          JSR R5,PREG05 ;SAVE CONTENTS OF GPRS R0 THRU R5.
; *          ;CALL REGISTER SAVE SUBRT.
; *
; * READ CORRECT NUMBER (NUMBER OF LINE ON DUT) OF CHARS FROM THE FIFO.
; * VERIFY THAT EACH CHAR IS A SELFTEST SUCCESS CODE.
; *
; *          CLR R3 ;CLEAR THE CODE COUNTER.
; *          MOV ERRNBR,R5 ;SAVE ERRNBR FOR RESTORATION LATER.
2$: ; *          MOV @RBUFA,R2 ;READ A CHAR FROM THE DUT FIFO.
; *          BMI 4$ ;SKIP ERROR IF DATA.VALID SET FOR CHAR.
; *
; * WE EXPECT A SELFTEST CODE, BUT THIS FIFO SLOT IS EMPTY.
; *
; *          MOV R5,ERRNBR ;RESTORE ERROR NUMBER TO INITIAL VALUE.
; *          MOV @EM9018,R1 ;PASS ERROR MESSAGE INFO TO ER9007 ROUTINE.
; *          MOV @ER9007,ERRBLK ;SELECT PROPER ERROR REPORT ROUTINE.
; *
; * REPORT ERROR WITH NUMBER INITIAL ERRNBR.
; * "NO SELFTEST CODE IN SELFTEST CODE FIFO SLOT FOR LINE NN AFTER RESET."
; *
; *          ERROR ; >>>> ERROR <<<<<
; *                                     TRAP C$ERROR
; *
; * EXIT THIS ROUTINE IF EXTENDED ERROR REPORTING HAS NOT BEEN REQUESTED.
; *
; *          BIT @BIT06,OPTION ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
; *          BNE 3$ ;AVOID SET THE FLAG IF IT HAS.
; *          MOV #1,EXOERR ;SET THE EXIT ON ERROR FLAG

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3381 (21572 000261
3382 (21574 000167 000406
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3386 021600 012700 070001
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3388 021606 001042
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3393 021610 032702 000200
3394 021614 001462
3395 021615 120227 000203
3396 021622 001457
3397 021624 120227 000201
3398 021630 001454
3399 021632 012700 000300
3400 021636 040200
3401 021640 001003
3402 021642 004767 000430
3403 021646 000445
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3407 021650 010567 162114
3408 021654 005267 162110
3409 021660 012701 015154
3410 021664 012767 017126 162102
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3415 021672
021672 104460
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3419 021674 032767 000100 160322
3420 021702 001027
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3422 021704 012767 000001 160372
3423 021712 000534
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3428 021714 010567 162050
3429 021720 062767 000002 162042
3430 021726 012701 015137
3431 021732 012767 017036 162034
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;+
; INIDICATE "SUCCESS" (BECAUSE FIFO IS PURGED), AND EXIT THIS ROUTINE.
;-
3$: SEC ;SET SUCCESS FLAG.
JMP 60$ ;EXIT ROUTINE.
;+
; DETERMINE IF THIS IS NOT A SELFTEST CODE.
;-
4$: MOV #70001,R0 ;GENERATE BIT MAP OF ANY CLEAR ERROR BITS OR
BIC R2,R0 ; BIT 0 WHICH ARE CLEAR.
BNE 8$ ;GO TO REPORT ERROR IF THIS IS NOT A TEST CODE.
;+
; WE HAVE A TEST CODE (EITHER BMP OR SELFTEST CODE).
; DETERMINE WHAT TYPE OF CODE WE HAVE.
;-
BIT #BIT7,R2 ;TEST ROM VERSION CODE INDICATOR BIT.
BEQ 10$ ;SKIP ERRORS IF SELFTEST ROM VERSION CODE.
CMPB R2,#203 ;CHECK IF SKIP SELF TEST CODE.
BEQ 10$ ;SKIP ERROR REPORT IF SKIP SELF TEST CODE FOUND
CMPB R2,#201 ;CHECK IF NULL CODE PRESENT.
BEQ 10$ ;SKIP ERROR REPORT IF SELF TEST NULL CODE.
MOV #300,R0 ;TEST CODE TYPE BITS FOR BOTH CODE
BIC R2,R0 ; TYPE BITS SET (INDICATING BMP CODE).
BNE 6$ ;IF IT IS NOT A BMP CODE GO REPORT ERROR.
JSR PC,SAVBMP ;SAVE THE BMP CODE ON THE QUEUE.
BR 10$ ;GO GET THE NEXT CHARACTER FROM THE FIFO.
;+
; WE HAVE A SELFTEST ERROR CODE.
;-
6$: MOV R5,ERRNBR ;RESTORE ERROR NUMBER TO INITIAL VALUE.
INC ERRNBR ;CALCULATE INITIAL ERROR NUMBER PLUS 1.
MOV #EM9020,R1 ;PASS ERROR MESSAGE INFO TO ER9008 ROUTINE.
MOV #ER9008,ERRBLK ;SELECT PROPER ERROR REPORT ROUTINE.
;+
; REPORT ERROR WITH NUMBER INITIAL ERRNBR + 1.
; "UNEXPECTED SELFTEST ERROR CODE FOR LINE NN IN FIFO AFTER RESET:"
;-
ERROR ; >>>> ERROR <<<<<.
TRAP C$ERROR
;+
; EXIT THIS ROUTINE IF EXTENDED ERROR REPORTING HAS NOT BEEN REQUESTED.
;-
BIT #BIT06,OPTION ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
BNE 10$ ;AVOID SET THE FLAG IF IT HAS AND GO TO
;THE END OF THE LOOP.
MOV #1,EXOERR ;SET THE "EXIT ON ERROR" FLAG
BR 50$ ;EXIT THE ROUTINE WITH FAILURE SINCE THE FIFO
;IS NOT PRUGED.
;+
; WE HAVE A NON-SELFTEST CODE (EITHER BMP CODE OR DATA CHAR).
;-
8$: MOV R5,ERRNBR ;RESTORE ERROR NUMBER TO INITIAL VALUE.
ADD #2,ERRNBR ;CALCULATE INITIAL ERROR NUMBER PLUS 2.
MOV #EM9019,R1 ;PASS ERROR MESSAGE INFO TO ER9007 ROUTINE.
MOV #ER9007,ERRBLK ;SELECT PROPER ERROR REPORT ROUTINE.
;+
; REPORT ERROR WITH NUMBER: INITIAL ERRNBR + 2.

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3436 021740 104460
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3440 021742 032767 000100 160254
3441 021750 001004
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3443 021752 012767 000001 160324
3444 021760 000511
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3449 021762 005203
3450 021764 020327 000010
3451 021770 002656
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3455 021772 012704 000022
3456 021776 010567 161766
3457 022002 062767 000003 161760
3458 022010 012767 017126 161756
3459 022016 017702 160222
3460 022022 000261
3461 022024 100070
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3466 022026 012700 070000
3467 022032 040200
3468 022034 001403
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3472 022036 012701 015200
3473 022042 000423
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3478 022044 032702 000001
3479 022050 001003
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3483 022052 012701 015217
3484 022056 000415
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3489 022060 032702 000200

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```

; "NON-SELFTTEST CODE IN SELFTTEST CODE FIFO SLOT FOR LINE NN AFTER RESET."
;-
ERROR
;
;>>>> ERROR <<<<<.
TRAP C$ERROR
;+
; EXIT THIS ROUTINE IF EXTENDED ERROR REPORTING HAS NOT BEEN REQUESTED.
;-
BIT #BIT06,OPTION ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
BNE 10$ ;AVOID SET THE FLAG IF IT HAS AND GO TO
;THE END OF THE LOOP.
MOV #1,EXOERR ;SET THE "EXIT ON ERROR" FLAG
BR 50$ ;EXIT THE ROUTINE WITH FAILURE.
;+
; END OF LOOP, LOOP IF NOT ALL CHARS HAVE BEEN READ FROM THE FIFO.
;-
10$: INC R3 ;SET CODE COUNTER FOR NEXT ITERATION OF LOOP.
CMP R3,#8. ;TEST FOR ALL CODES READ.
BLT 2$ ;LOOP IF NOT CHARS READ FROM FIFO.
;+
; PURGE THE FIFO UNTIL DATA.VALID IS CLEAR OR UNTIL TOO MANY CHARS ARE READ.
;-
MOV #18.,R4 ;INITIALIZE THE CHARACTER COUNTER.
MOV R5,ERRNBR ;GET INITIAL VALUE OF THE ERROR NUMBER.
ADD #3,ERRNBR ;CALCULATE ERROR NUMBER OF NEXT ERROR.
MOV #ER9008,ERRBLK ;SELECT PROPER ERROR REPORT ROUTINE.
12$: MOV @RBUFA,R2 ;READ A CHARACTER FROM THE DUT FIFO.
SEC ;INDICATE SUCCESS IN CASE DATA.VALID IS CLEAR.
BPL 60$ ;EXIT ROUTINE WITH SUCCESS IF DATA.VALID CLEAR.
;+
; WE HAVE A CHARACTER.
; DETERMINE IF CHARACTER IS A DATA CHARACTER.
;-
MOV #70000,R0 ;TEST BITS 12 THRU 14 OF THE
BIC R2,R0 ; CODE READ FROM THE DUT FIFO.
BEQ 14$ ;SKIP THIS ERROR IF CODE IS NOT A DATA CHAR.
;+
; WE HAVE AN UNEXPECTED DATA CHARACTER: SET UP AND GO TO REPORT ERROR.
;-
MOV #EM9022,R1 ;SELECT ERROR MSG INFO FOR ER0808 ROUTINE.
BR 22$ ;GO TO REPORT THIS ERROR.
;+
; WE HAVE AN UNEXPECTED CODE.
; DETERMINE IF THE CODE IS A MODEM STATUS CODE.
;-
14$: BIT #BIT0,R2 ;TEST MODEM STATUS INDICATOR BIT OF CODE.
BNE 16$ ;SKIP THIS ERROR IF NOT MODEM STATUS CODE.
;+
; WE HAVE A MODEM STATUS CODE: SET UP AND GO TO REPORT ERROR.
;-
MOV #EM9023,R1 ;SELECT ERROR MSG INFO FOR ER0808 ROUTINE.
BR 22$ ;GO TO REPORT THIS ERROR.
;+
; WE HAVE AN ONBOARD TEST CODE.
; DETERMINE IF THIS CODE IS A BMP CODE.
;-
16$: BIT #BIT7,R2 ;TEST THE ROM VERSION BIT OF THE CODE.

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3490 022064 001404          BEQ      18$          ;GOTO SET UP FOR SELFTEST CODE IF ROM VERSION.
3491 022066 012700 000300    MOV      #300,R0
3492 022072 040200          BIC      R2,R0        ;TEST THE ERROR TYPE BITS OF THE CODE.
3493 022074 001403          BEQ      20$          ;SKIP THIS ERROR IF BMP CODE.
3494
3495          ;+
3496          ; WE HAVE A SELFTEST CODE: SET UP AND GO TO REPORT ERROR.
3497 022076 012701 015241    18$:    MOV      #EM9024,R1    ;SELECT ERROR MSG INFO FOR ER0808 ROUTINE.
3498 022102 000403          BR       22$          ;GO TO REPORT THIS ERROR.
3499
3500          ;+
3501          ; WE HAVE A BMP CODE: SAVE IT ON THE QUEUE.
3502 022104 004767 000166    20$:    JSR      PC,SAVBMP    ;SAVE THE BMP CODE ON THE QUEUE.
3503 022110 000411          BR       24$          ;
3504
3505          ;+
3506          ; REPORT THE ERROR WITH ERROR NUMBER OF INITIAL ERRNBR + 3.
3507          ; "UNEXPECTED XXX XXXX FOR LINE NN IN FIFO AFTER RESET:"
3508 022112          22$:    ERROR          ;
3509 022112 104460          ;          >>>> ERROR <<<<<.
3510          ;          TRAP      C$ERROR
3511          ;+
3512          ; EXIT THIS ROUTINE IF EXTENDED ERROR REPORTING HAS NOT BEEN REQUESTED.
3513 022114 032767 000100 160102  BIT      #BIT06,OPTION ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
3514 022122 001004          BNE      24$          ;AVOID SETTING THE FLAG IF IT HAS AND GO TO
3515 022124 012767 000001 160152  MOV      #1,EXOERR    ;THE END OF THE LOOP.
3516 022132 000424          BR       50$          ;SET THE "EXIT ON ERROR" FLAG
3517          ;EXIT THE ROUTINE WITH FAILURE.
3518
3519          ;+
3520          ; END OF LOOP.
3521          ; COUNT THE CHARACTER WE JUST RECEIVED, AND CHECK FOR TOO MANY RECEIVED.
3522 022134 005304          24$:    DEC      R4          ;COUNT THIS CHARACTER.
3523 022136 001327          BNE      12$          ;LOOP IF NOT TOO MANY CHARACTERS PURGED.
3524
3525          ;+
3526          ; WE READ TOO MANY VALID CHARACTERS WHILE TRYING TO PURGE THE FIFO.
3527          ; REPORT ERROR AND EXIT WITHOUT SUCCESS.
3528          ; "FIFO WILL NOT PURGE (DATA.VALID STUCK SET), REMAINDER OF TEST SKIPPED."
3529 022140 012701 015016          MOV      #EM9017,R1    ;SELECT PROPER ERROR MESSAGE.
3530 022144 010567 161620          MOV      R5,ERRNBR    ;GET INITIAL ERROR NUMBER.
3531 022150 062767 000004 161612  ADD      #4,ERRNBR    ;CALCULATE INITIAL ERRNBR + 4.
3532 022156 012767 016136 161610  MOV      #ER0503,ERRBLK ;SELECT PROPER ERROR REPORT ROUTINE.
3533          ;PRINT ERROR REPORT.
3534 022164          ERROR          ;
3535 022164 104460          ;          >>>> ERROR <<<<<.
3536          ;          TRAP      C$ERROR
3537          ;+
3538          ; EXIT THIS ROUTINE IF EXTENDED ERROR REPORTING HAS NOT BEEN REQUESTED.
3539 022166 032767 000100 160030  BIT      #BIT06,OPTION ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
3540 022174 001003          BNE      50$          ;AVOID SETTING THE FLAG IF IT HAS.
3541 022176 012767 000001 160100  MOV      #1,EXOERR    ;SET THE "EXIT ON ERROR" FLAG
3542 022204 000241          50$:    CLC          ;CLEAR THE SUCCESS FLAG.
3543
3544 022206          60$:    PASS          ;RESTORE GPRS,

```


022206 004736
3545 022210 000207

RTS PC

JSR

PC,@(SP).
; CARRY - SUCCESS FLAG (SET IF FIFO IS PURGED).

; RETURN TO PREG05 SUBRT.


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3574 .SBTTL GLOBAL SUBROUTINE - RXIE1 -
3575 ;* *****
3576 ;* - RECEIVER INTERRUPT ENABLE -
3577 ;* THIS ROUTINE IS USED TO ENABLE RECEIVER INTERRUPTS IN THE DHU11.
3578 ;*
3579 ;* INPUTS: NONE.
3580 ;*
3581 ;* OUTPUTS: THE RX.INT.ENBL BIT IS SET IN THE DUT CSR.
3582 ;* IESTST -CONTAINS THE UPDATED STATUS OF THE TX AND RX INTERRUPT
3583 ;* ENABLE BITS.
3584 ;*
3585 ;* CALLING SEQUENCE: JSR PC,RXIE1
3586 ;*
3587 ;* COMMENTS: THE CONTENTS OF THE INDIRECT ADDRESS REGISTER FIELD IN
3588 ;* THE DUT CSR ARE DESTROYED.
3589 ;*
3590 ;* SUBORDINATE ROUTINES CALLED: NONE.
3591 ;* - *****
3592
3593 022252 052767 000100 160030 RXIE1:: BIS #BIT06,IESTAT ;SET RX.INT.ENBL BIT IN IESTAT.
3594 022260 042767 137677 160022 BIC #137677,IESTAT ;CLEAR ALL OTHER BITS, EXCEPT TX AND RX I.E.
3595 022266 016777 160016 157746 MOV IESTAT,@CSRA ;ENABLE RX INTERRUPTS.
3596 022274 000207 RTS PC
    
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.SBTTL GLOBAL SUBROUTINE - SAVBMP -
; * *****
; * - SAVE BMP CODES ROUTINE -
; * THIS ROUTINE SAVES THE PARAMETER PASSED IN, ONTO THE BMP CODE QUEUE
; * TOGETHER WITH THE NUMBER OF THE CURRENTLY EXECUTING TEST.
; *
; * INPUTS: R2 - CONTAINS THE BMP CODE THAT IS TO BE PLACED ON THE QUEUE.
; * BMPCQP - CONTAINS ADDRESS OF NEXT LOCATION IN THE BMP QUEUE.
; * BMPCQB - LABEL AT BASE OF THE BMP CODE QUEUE.
; * BMPCQE - LABEL OF NEXT LOCATION AFTER THE END OF THE BMP QUEUE.
; * TSTNUM - CONTAINS THE NUMBER OF THE CURRENT TEST.
; *
; * OUTPUTS: BMPCQP - INCREMENTED BY 4.
; * THE CONTENTS OF THE BMP CODE QUEUE ARE UPDATED.
; *
; * CALLING SEQUENCE: JSR PC,SAVBMP
; *
; * COMMENTS: IF THE OVERFLOW OCCURS THEN THE LAST LOCATION WILL BE
; * OVERWRITTEN BY ANY SUBSEQUENT ATTEMPTS TO UPDATE THE QUEUE.
; *
; * SUBORDINATE ROUTINES CALLED: NONE.
; - - *****

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3621 022276
      022276 004567 161474
3622 022302 016704 160216
3623 022306 116724 160012
3624 022312 005204
3625 022314 042702 177400
3626 022320 010224
3627 022322 020427 002726
3628 022326 103402
3629 022330 162704 000004
3630 022334 010467 160164
3631
3632 022340
      022340 004736
3633 022342 000207

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SAVBMP:: SAVE
;SAVE CONTENTS OF GPRS R0 THRU R5.
;R5,PREG05 ;CALL REGISTER SAVE SUBRT.
;GET THE POINTER TO THE NEXT LOCATION IN QUEUE.
;SAVE THE CURRENT TEST NUMBER ON THE QUEUE.
;INCREMENT THE POINTER TO GIVE AN EVEN ADDRESS.
;CLEAR THE UNWANTED BITS FROM THE BMP CODE.
;SAVE THE BMP CODE ON THE QUEUE.
;CHECK IF OVERFLOW WILL OCCUR THE NEXT TIME.
;GO SAVE THE POINTER IF WE WILL NOT OVERFLOW.
;RESET THE POINTER TO THE LAST LOCATION IN QUE.
;SAVE THE POINTER.

2$: MOV R4,BMPCQP
   MOV R4,TSTNUM
   INC R4
   BIC #177400,R2
   MOV R2,(R4)
   CMP R4,#BMPCQE
   BLO 2$
   SUB #4,R4
   MOV R4,BMPCQP

60$: PASS

RTS PC JSR PC,@(SP) ;RESTORE GPRS.
;RETURN TO PREG05 SUBRT.

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022344 004567 161426
3656 022350 012704 000012
3657 022354 004767 175544
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3661 022360 012701 000060
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3664 022364 012703 052525
3665 022370 005301
3666 022372 016704 157644
3667 022376 010124
3668 022400 010324
3669 022402 020467 157652
3670 022406 103774
3671 022410 032701 000017
3672 022414 001365
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3674 022416
022416 004736
3675 022420 000207

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.SBTTL GLOBAL SUBROUTINE - SKPSTS -
; * *****
; * - SKIP SELFTEST ROUTINE -
; * THIS SUBROUTINE IS USED TO SKIP THE SELFTEST AFTER A DUT RESET HAS BEEN
; * INITIATED. IT MUST BE ENTERED IMMEDIATELY AFTER SETTING THE DUT MASTER
; * RESET ROUTINE OR AFTER THE EXECUTION OF A BUS RESET (BECAUSE OF TIMING
; * CONSIDERATIONS).
; *
; * INPUTS: CSRA - CONTAINS ADDRESS OF THE DUT CSR.
; * TXBFCA - CONTAINS ADDRESS OF DUT DMA BUFFER COUNT REGISTER.
; *
; * OUTPUTS: SKIP SELFTEST CODES ARE WRITTEN TO THE DUT REGISTERS.
; *
; * CALLING SEQUENCE: JSR PC,SKPSTS
; *
; * COMMENTS:
; *
; * SUBORDINATE ROUTINES CALLED: DELAY.
; - - *****
SKPSTS:: SAVE JSR ;SAVE CONTENTS OF GPRS R0 THRU R5.
MOV #10.,R4 ;R5,PREG05 ;CALL REGISTER SAVE SUBRT.
JSR PC,DELAY ;PASS DELAY VALUE OF 10 MILLI-SECONDS.
;DELAY FOR 10 MILLI-SECONDS.
; *
; * WRITE SKIP SELF-TEST CODE (52525) TO ALL THE INDEXED DUT REGISTERS.
; -
MOV #NUMLNS!BIT05,R1 ;FORM IND.ADR.REG FIELD (PLUS M.R. BIT) WORD.
;THE ABOVE INCLUSION OF THE M.R. BIT IS NECESSARY BECAUSE OF THE
; LACK OF A M.R. BIT WRITE LOCK-OUT ON THE DHU-11.
MOV #52525,R3 ;INITIALISE THE SKIP SELF-TEST CODE.
4$: DEC R1 ;SELECT THE NEXT SET OF DEVICE REGISTERS.
MOV CSRA,R4 ;GET THE ADDRESS OF THE CSR OF THE DUT.
MOV R1,(R4)+ ;SELECT A BANK OF DUT REGISTERS.
6$: MOV R3,(R4)+ ;WRITE THE CODE TO A DUT REGISTER.
CMP R4,TXBFCA ;COMPARE POINTER WITH LAST REGISTER ADDRESS.
BLO 6$ ;LOOP IF NOT ALL REGS DONE IN THIS BANK.
BIT #17,R1 ;TEST FOR IND.ADR.REG FIELD DECREMENTED TO 0.
BNE 4$ ;LOOP UNTIL ALL REGISTERS CONTAIN THE CODE.
60$: PASS ;RESTORE GPRS.
JSR PC,@(SP)+ ;RETURN TO PREG05 SUBRT.
RTS PC
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3699 022422 010046
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3703 022424 010146
3704 022426 010246
3705 022430 010346
3706 022432 010446
3707 022434 010546
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3711 022436 012700 002450
3712 022442 012001
3713 022444 012002
3714 022446 012003
3715 022450 012004
3716 022452 012005
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3720 022454 012640
3721 022456 012640
3722 022460 012640
3723 022462 012640
3724 022464 012640
3725
3726 022466 012600
3727
3728 022470 000207

```
.SBTTL GLOBAL SUBROUTINE - SWAPO -
;+ *****
;+ - SWAP GPRS WITH GPR SET 0 ROUTINE -
;+ THIS SUBROUTINE SWAPS THE PRESENT CONTENTS OF GPRS R1 THRU R5 WITH
;+ THE CONTENTS OF THE NUMBER ZERO GPR SAVE AREA. THE CONTENTS OF R0
;+ ARE NOT ALTERED BY THIS SUBROUTINE.
;+
;+ INPUTS: GPR CONTENTS R1 THRU R5.
;+ GPRS0B - LABEL AT BASE OF GPR SAVE AREA NUMBER ZERO.
;+
;+ OUTPUTS: R1 THRU R5 CONTAIN THE PREVIOUS CONTENTS OF GPR SAVE AREA
;+ ZERO WORDS 1 THRU 5 RESPECTIVELY.
;+ GPRS0 - GPR SAVE AREA 0 WORDS 1 THRU 5, CONTAIN PREVIOUS
;+ CONTENTS OF GPRS R1 THRU R5 RESPECTIVELY.
;+
;+ CALLING SEQUENCE: JSR PC,SWAPO
;+
;+ COMMENTS: THE STATE OF THE CARRY FLAG IS NOT ALTERED BY THIS ROUTINE.
;+
;+ SUBORDINATE ROUTINES CALLED: NONE.
;-- *****

SWAPO:: MOV R0,-(SP) ;SAVE THE CONTENTS OF R0.
;+
;+ LOAD THE STACK FROM THE GPRS.
;--
MOV R1,-(SP) ;SAVE THE CONTENTS OF R1.
MOV R2,-(SP) ;SAVE THE CONTENTS OF R2.
MOV R3,-(SP) ;SAVE THE CONTENTS OF R3.
MOV R4,-(SP) ;SAVE THE CONTENTS OF R4.
MOV R5,-(SP) ;SAVE THE CONTENTS OF R5.
;+
;+ LOAD THE GPRS FROM THE GPR SAVE AREA 0.
;--
MOV @GPRS0B,R0 ;GET THE BASE ADDRESS OF GPR SAVE AREA 0.
MOV (R0)+,R1 ;LOAD R1 WITH GPR SAVE AREA 0 WORD 1.
MOV (R0)+,R2 ;LOAD R1 WITH GPR SAVE AREA 0 WORD 2.
MOV (R0)+,R3 ;LOAD R1 WITH GPR SAVE AREA 0 WORD 3.
MOV (R0)+,R4 ;LOAD R1 WITH GPR SAVE AREA 0 WORD 4.
MOV (R0)+,R5 ;LOAD R1 WITH GPR SAVE AREA 0 WORD 5.
;+
;+ LOAD THE GPR SAVE AREA 0 FROM THE STACK.
;--
MOV (SP)+,-(R0) ;LOAD GPR SAVE AREA 0 WORD 5 WITH SAVED R5.
MOV (SP)+,-(R0) ;LOAD GPR SAVE AREA 0 WORD 4 WITH SAVED R4.
MOV (SP)+,-(R0) ;LOAD GPR SAVE AREA 0 WORD 3 WITH SAVED R3.
MOV (SP)+,-(R0) ;LOAD GPR SAVE AREA 0 WORD 2 WITH SAVED R2.
MOV (SP)+,-(R0) ;LOAD GPR SAVE AREA 0 WORD 1 WITH SAVED R1.
MOV (SP)+,R0 ;RESTORE THE INITIAL VALUE OF R0.
RTS PC
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022604 004567 161166
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3787 022616 016702 157434
3788 022622 005202
3789 022624 012703 000020
3790 022630 016704 157454
3791 022634 005005
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3795 022636 010477 157400
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3803 022650 030100
3804 022652 001402
3805 022654 142712 000200
3806 022660 005204
3807 022662 006301
3808 022664 005303
3809 022666 001363
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3811 022670
022670 010566 000014
022674 004736
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3813 022676 000207

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.SBTTL GLOBAL SUBROUTINE - TXDSBL -
; * *****
; * - TRANSMITTER DISABLE -
; * THIS SUBROUTINE IS USED TO DISABLE TRANSMISSION ON SELECTED LINES BY,
; * CLEARING THE ASSOCIATED TX.ENABLE BIT ON THE DUT.
; *
; * INPUTS: R5 - BIT'S SET CORRESPOND TO LINES ON WHICH TO CLEAR TX.ENABLE.
; * CSRA - CONTAINS THE ADDRESS OF THE DUT CSR.
; * IESTAT - CONTAINS THE STATE OF TXIE AND RXIE BITS IN THE CSR.
; * NUMLNS - EQUATED TO BE THE MAXIMUM NUMBER OF LINES AVAILABLE.
; * TXAD2A - CONTAINS THE ADDRESS OF THE TBUFAD2 REGISTER.
; *
; * OUTPUTS: R5 - BIT'S SET INDICATE THE INITIAL STATES OF ALL TX.ENABLE BITS.
; * TBUFAD2 - THE STATE OF THE TX.ENABLE BIT MAY BE ALTERED.
; * THE CONTENTS OF THE IND.ADD.REG FIELD IN THE CSR ARE DESTROYED.
; *
; * CALLING SEQUENCE: JSR PC,TXDSBL
; *
; * COMMENTS:
; *
; * SUBORDINATE ROUTINES CALLED: NONE.
; - - *****

TXDSBL:: SAVE
; SAVE CONTENTS OF GPRS R0 THRU R5.
; R5,PREG05 ;CALL REGISTER SAVE SUBRT.
JSR
; COPY BIT MAP OF LINES TO DISABLE TRANSMISSION.
MOV R5,R0
; INITIALIZE THE SELECTED LINE BIT MASK.
MOV #BIT0,R1
; GET THE ADDRESS OF THE TBUFAD2 REGISTER.
MOV TXAD2A,R2
; GET THE ADDRESS OF THE MSBYTE OF TBUFAD2 REG.
INC R2
; GET MAXIMUM LINE NUMBER PLUS ONE.
MOV #NUMLNS,R3
; GET THE STATES OF THE INT ENABLE BITS.
MOV IESTAT,R4
; LOG POSSIBLE TX DISABLED ON ALL LINES.
CLR R5

; *
; * SELECT EVERY LINE IN TURN, AND LOG THE STATE OF EACH TX.ENABLE BIT.
; -
2$: MOV R4,@CSRA ;WRITE TO DUT CSR TO SELECT LINE REGISTERS.
TSTB (R2) ;CHECK STATE OF TX.ENABLE BIT ON SELECTED LINE.
BPL 4$ ;SKIP NEXT INSTRUCTION IF TX.ENABLE CLEAR.
BIS R1,R5 ;LOG TX ENABLE BIT SET FOR SELECTED LINE.

; *
; * CLEAR TX.ENABLE ON LINES THAT HAVE A CORRESPONDING BIT SET IN THE TX DISABLE
; * LINE BIT MAP.
; -
4$: BIT R1,R0 ;CHECK STATE OF DISABLE LINE BIT MAP.
BEQ 6$ ;BRANCH IF THIS LINE TO REMAIN UNALTERED.
BICB #BIT7,(R2) ;CLEAR TX.ENABLE BIT ON SELECTED LINE.
; PREPARE TO SELECT REGISTERS FOR NEXT LINE.
6$: INC R4
ASL R1 ;SHIFT BIT MAP FOR NEXT LINE.
DEC R3 ;DECREMENT LINE NUMBER.
BNE 2$ ;LOOP TO CHECK NEXT LINE.

; *
; * RESTORE GPRS,EXCEPT
; * R5,R5SLOT(SP) ;PUT R5 IN STACK SLOT.
; * PC,@(SP). ;RETURN TO PREG05 SUBRT.
; * R5 - PREVIOUS STATES OF ALL TX.ENABLE BITS.
60$: PASS R5
MOV R5,R5SLOT(SP)
JSR PC,@(SP)

RTS PC
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      022700 004567 161072
3839 022704 010500
3840 022706 012701 000001
3841 022712 016702 157340
3842 022716 005202
3843 022720 012703 000020
3844 022724 016704 157360
3845 022730 005005
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3849 022732 010477 157304
3850 022736 105712
3851 022740 100401
3852 022742 050105
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3857 022744 030100
3858 022746 001402
3859 022750 152712 000200
3860 022754 005204
3861 022756 006301
3862 022760 005303
3863 022762 001363
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3865 022764
      022764 010566 000014
      022770 004736
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3868 022772 000207
    
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.SBTTL GLOBAL SUBROUTINE - TXENBL -
;*****
;* - TRANSMITTER ENABLE -
;* THIS SUBROUTINE IS USED TO ENABLE TRANSMISSION ON SELECTED LINES BY
;* SETTING THE ASSOCIATED TX.ENABLE BIT ON THE DUT.
;*
;* INPUTS:      R5 - BIT'S SET CORRESPOND TO LINES ON WHICH TO SET TX.ENABLE.
;*              CSRA - CONTAINS THE ADDRESS OF THE DUT CSR.
;*              IESTAT - CONTAINS THE STATE OF TXIE AND RXIE BITS IN THE CSR.
;*              NUMLNS - EQUATED TO BE THE MAXIMUM NUMBER OF LINES AVAILABLE.
;*              TXAD2A - CONTAINS THE ADDRESS OF THE TBUFAD2 REGISTER.
;*
;* OUTPUTS:     R5 - BIT'S SET INDICATE PREVIOUSLY DISABLED LINES.
;*              TBUFAD2 - THE STATE OF THE TX.ENABLE BIT MAY BE ALTERED.
;*              THE CONTENTS OF THE IND.ADD.REG FIELD IN THE CSR ARE DESTROYED.
;*
;* CALLING SEQUENCE:  JSR      PC,TXENBL
;*
;* COMMENTS:
;*
;* SUBORDINATE ROUTINES CALLED: NONE.
;*****
TXENBL:: SAVE
;SAVE CONTENTS OF GPRS R0 THRU R5.
      JSR      R5,PREG05 ;CALL REGISTER SAVE SUBRT.
      MOV     R5,R0      ;COPY BIT MAP OF LINES TO ENABLE.
      MOV     @BIT0,R1   ;INITIALIZE THE SELECTED LINE BIT MASK.
      MOV     TXAD2A,R2  ;GET THE ADDRESS OF THE TBUFAD2 REGISTER.
      INC     R2         ;GET THE ADDRESS OF THE MSBYTE OF TBUFAD2 REG.
      MOV     @NUMLNS,R3 ;GET MAXIMUM LINE NUMBER.
      MOV     IESTAT,R4  ;GET THE STATES OF THE INT ENABLE BITS.
      CLR     R5        ;CLEAR TX.ENABLE BIT LOG OF DISABLED LINES.
;
; SELECT EVERY LINE IN TURN,AND LOG ANY TX.ENABLE BIT THAT IS CLEAR.
2$:   MOV     R4,@CSRA   ;WRITE TO DUT CSR TO SELECT LINE REGISTERS.
      TSTB   (R2)       ;CHECK STATE OF TX.ENABLE BIT ON SELECTED LINE.
      BMI    4$         ;SKIP NEXT INSTRUCTION IF TX.ENABLE SET.
      BIS    R1,R5      ;LOG TX ENABLE BIT CLEAR FOR SELECTED LINE.
;
; SET TX.ENABLE ON LINES THAT HAVE A CORRESPONDING BIT SET IN THE TX ENABLE
; LINE BIT MAP.
4$:   BIT     R1,R0      ;CHECK STATE OF TX.ENABLE LINE BIT MAP.
      BEQ    6$         ;BRANCH IF THIS LINE TO REMAIN UNALTERED.
      BISB   @BIT7,(R2) ;ENABLE TRANSMISSION ON SELECTED LINE.
6$:   INC     R4         ;PREPARE TO SELECT REGISTERS FOR NEXT LINE.
      ASL    R1         ;SHIFT BIT MAP FOR NEXT LINE.
      DEC    R3         ;DECREMENT LINE NUMBER.
      BNE    2$        ;LOOP TO CHECK NEXT LINE.
;
60$:  PASS    R5        ;RESTORE GPRS,EXCEPT
      MOV    R5,R5SLOT(SP) ;PUT R5 IN STACK SLOT.
      JSR   PC,@(SP)      ;RETURN TO PREG05 SUBRT.
;R5 - LINE BIT MAP CORRESPONDING TO THE
; PREVIOUS LINES THAT WERE DISABLED.
      RTS    PC
    
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3888 022774 010046
3889 022776 104440
023000 010046
3890 023002 012700 000340
023006 104441
3891 023010 042767 177677 157272
3892 023016 016777 157266 157216
3893 023024 012600
023026 104441
3894 023030 012600
3895 023032 000207

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.SBTTL GLOBAL SUBROUTINE - TXIEO -
;*****
;* - TRANSMITTER INTERRUPT DISABLE -
;* THIS ROUTINE IS USED TO DISABLE TRANSMITTER INTERRUPTS IN THE DHU11.
;*
;* INPUTS: NONE.
;*
;* OUTPUTS: THE TX.INT.ENBL BIT IS CLEARED IN THE DUT CSR.
;* IESTST -CONTAINS THE UPDATED STATUS OF THE TX AND RX INTERRUPT
;* ENABLE BITS.
;*
;* CALLING SEQUENCE: JSR PC,TXIEO
;*
;* COMMENTS: THE CONTENTS OF THE INDIRECT ADDRESS REGISTER FIELD IN
;* THE DUT CSR ARE DESTROYED.
;*
;* SUBORDINATE ROUTINES CALLED: NONE.
;*****
TXIEO:: MOV RO,-(SP) ;SAVE CONTENTS OF RO ON THE STACK.
GETPRI -(SP) ;SAVE CURRENT PROCESSOR PRIORITY ON THE STACK.
TRAP C$GPRI
MOV RO,-(SP)
3890 SETPRI @PRI07 ;IGNORE ANY INTERRUPTS THAT MAY BE GENERATED.
MOV @PRI07,RO
TRAP C$SPRI
3891 BIC #177677,IESTAT ;CLEAR TX.INT.ENBL BIT IN IESTAT.
3892 MOV IESTAT,@CSRA ;DISABLE TX INTERRUPTS.
3893 SETPRI (SP)+ ;ENABLE INTERRUPTS TO THE PROCESSOR AGAIN.
MOV (SP)+,RO
TRAP C$SPRI
MOV (SP)+,RO
RTS PC ;RESTORE RO.
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3916 023034 052767 040000 157246
3917 023042 042767 137677 157240
3918 023050 016777 157234 157164
3919 023056 000207

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.SBTTL GLOBAL SUBROUTINE - TXIE1 -
; * *****
; * - TRANSMITTER INTERRUPT ENABLE -
; * THIS ROUTINE IS USED TO ENABLE TRANSMITTER INTERRUPTS IN THE DHU11.
; *
; * INPUTS: NONE.
; *
; * OUTPUTS: THE TX.INT.ENBL BIT IS SET IN THE DUT CSR.
; * IESTST -CONTAINS THE UPDATED STATUS OF THE TX AND RX INTERRUPT
; * ENABLE BITS.
; *
; * CALLING SEQUENCE: JSR PC,TXIE1
; *
; * COMMENTS: THE CONTENTS OF THE INDIRECT ADDRESS REGISTER FIELD IN
; * THE DUT CSR ARE DESTROYED.
; *
; * SUBORDINATE ROUTINES CALLED: NONE.
; -- *****
TXIE1:: BIS #BIT14,IESTAT ;SET TX.INT.ENBL BIT IN IESTAT.
        BIC #137677,IESTAT ;CLEAR ALL BITS EXCEPT TX RX I.E BITS.
        MOV IESTAT,@CSRA ;ENABLE TX INTERRUPTS.
        RTS PC

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3944 023060
      023060 004567 160712
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3948 023064 010204
3949 023066 160104
3950 023070 103403
3951 023072 012701 177777
3952 023076 000442
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3956 023100 005004
3957 023102 000241
3958 023104 006001
3959 023106 006004
3960 023110 012700 000020
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3964 023114 010246
3965 023116 010346
3966 023120 160403
3967 023122 005602
3968 023124 103402
3969 023126 160102
3970 023130 103003
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3976 023134 012602

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.SBTTL GLOBAL SUBROUTINE - UNSDIV -
;+ *****
;+ - UNSIGNED DIVIDE ROUTINE -
;+ THIS SUBROUTINE IS USED TO DIVIDE A 32 BIT UNSIGNED DIVIDEND BY A
;+ 16 BIT UNSIGNED DIVISOR GIVING A 16 BIT QUOTIENT. ALL NUMBERS ARE
;+ CONSIDERED TO BE UNSIGNED. A SUCCESS FLAG IS NOT SET ON RETURN IF
;+ THE QUOTIENT WAS TOO BIG TO BE CONTAINED IN 16 BITS.
;+
;+ INPUTS: R1 - THE DIVISOR, UNSIGNED, 16 BITS.
;+ R2 - MOST SIGNIFICANT WORD OF THE DIVIDEND, UNSIGNED, 16 BITS.
;+ R3 - LEAST SIGNIFICANT WORD OF THE DIVIDEND, UNSIGNED, 16 BITS.
;+
;+ OUTPUTS: R1 - QUOTIENT, UNSIGNED, 16 BITS (177777 IF OVERFLOW).
;+ CARRY - SUCCESS FLAG, SET IF COMPLETE QUOTIENT FITS IN 16 BITS.
;+
;+ CALLING SEQUENCE: JSR PC,UNSDIV
;+
;+ COMMENTS: IF THE DIVISOR IS 0 THE QUOTIENT IS RETURNED AS ALL ONES
;+ (177777) AND THE CARRY IS CLEAR REGARDLESS OF THE DIVIDEND.
;+
;+ SUBORDINATE ROUTINES CALLED: NONE.
;-- *****
UNSDIV:: SAVE JSR ;SAVE CONTENTS OF GPRS R0 THRU R5.
;R5,PREG05 ;CALL REGISTER SAVE SUBRT.
;+
; CHECK FOR QUOTIENT GREATER THAN 16 BITS CONDITION.
;--
MOV R2,R4 ;GET MSW OF DIVIDEND FOR SUBTRACT.
SUB R1,R4 ;SUBTRACT DIVISOR FROM MSW OF DIVIDEND.
BCS 2$ ;IF IT DIDN'T GO, WE HAVE QUOTIENT < 16 BITS.
MOV #1,R1 ;SET QUOTIENT TO ALL ONES (177777).
BR 60$ ;EXIT WITH CARRY CLEAR.
;+
; SET UP COUNTERS AND VARIOUS WORKING GPRS.
;--
2$: CLR R4 ;CLEAR THE LSW OF THE DIVISOR.
CLC ;CLEAR CARRY FOR THE SHIFT OF THE DIVISOR.
ROR R1 ;DIVISOR BY
ROR R4 ;2(UNSIGNED)
MOV #16,,R0 ;SET UP INITIAL SHIFT COUNT TO 16.
;+
; THE SUBTRACT AND SHIFT LOOP.
;--
4$: MOV R2,-(SP) ;SAVE MSWORD OF DIVIDEND.
MOV R3,-(SP) ;SAVE LSWORD OF DIVIDEND.
SUB R4,R3 ;LSWORD DIVIDEND - LSWORD OF DIVISOR.
SBC R2 ;MSWORD DIVIDEND - BORROW
BCS 6$ ;IF BORROW FROM BORROW SUBTRACT, IT DIDN'T GO.
SUB R1,R2 ;MSWORD DIVIDEND - MSWORD OF DIVISOR.
BCC 8$ ;IF NO BORROW, IT WENT, CARRY IS CLEAR.
;+
; IT DIDN'T GO, SO WE SHIFT A 1 INTO THE QUOTIENT (COMPLEMENTED LATER).
; CARRY IS SET.
;--
6$: MOV (SP)+,R3 ;RESTORE LSWORD OF DIVIDEND.
MOV (SP)+,R2 ;RESTORE MSWORD OF DIVIDEND.

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3977 023136 000401          BR      10$          ;GOTO SHIFT 1 INTO THE QUOTIENT.
3978
3979          ;+
3980          ; IT WENT, SO WE RESTORE THE STACK AND SHIFT A 0 INTO QUOTIENT (WILL BE
3981          ; COMPLEMENTED LATER).  CARRY IS CLEAR.
3982 023140 012626      8$:  MOV      (SP)+,(SP)+      ;POP THE SAVED DIVIDEND OFF OF THE STACK.
3983
3984          ;+
3985          ; SHIFT THE RESULT OF THE SUBTRACT ATTEMPT INTO THE QUOTIENT SHIFT REG.
3986 023142 006105      10$:  ROL      R5          ;SHIFT NEXT BIT INTO THE INVERTED QUOTIENT.
3987 023144 000241          CLC          ;DIVIDE THE
3988 023146 006001          ROR      R1          ; DEVISOR BY
3989 023150 006004          ROR      R4          ; 2 (UNSIGNED).
3990 023152 005300          DEC      R0          ;COUNT THIS SHIFT AND SUBTRACT.
3991 023154 001357          BNE     4$          ;LOOP FOR ANOTHER SHIFT & SUB IF NOT DONE.
3992 023156 005105          COM     R5          ;GET QUOTIENT FROM INVERTED QUOTIENT.
3993
3994          ;+
3995          ; NOW WE EITHER ROUND UP OR LEAVE QUOTIENT ALONE.
3996 023160 000241          CLC          ;CLEAR THE CARRY FOR THE SHIFT OF THE DIVIDEND.
3997 023162 006103          ROL     R3          ;MULTIPLY LSWORD OF DIVIDEND BY 2, MSWORD IS 0.
3998 023164 103402          BCS     12$        ;IF CARRY FROM SHIFT, ROUND UP.
3999 023166 160403          SUB     R4,R3      ;SUBTRACT DIVISOR FROM DIVIDEND.
4000 023170 103403          BCS     14$        ;IF BORROW, DON'T ROUND UP.
4001
4002          ;+
4003          ; ROUND UP, EXTRA SUBTRACT WENT.
4004 023172 005205      12$:  INC     R5          ;INCREMENT THE QUOTIENT BY ONE.
4005 023174 001001          BNE     14$        ;IF NO OVERFLOW, WE LEAVE THE ROUND UP.
4006 023176 005305          DEC     R5          ;DON'T LET ROUNDING CAUSE OVERFLOW.
4007
4008          ;+
4009          ; ALL DONE, PASS QUOTIENT AND EXIT.
4010 023200 010501      14$:  MOV     R5,R1      ;PASS QUOTIENT BACK IN R1.
4011 023202 000261          SEC          ;INDICATE NO OVERFLOW.
4012
4013 023204          60$:  PASS     R1          ;RESTORE GPRS, LEAVE THE FOLLOWING INTACT:
4014 023204 010166 000004          MOV     R1,R1SLOT(SP) ;PUT R1 IN STACK SLOT.
4015 023210 004736          JSR     PC,@(SP)+    ;RETURN TO PREG05 SUBRT.
4014
4015 023212 000207          RTS     PC          ;R1 - 16 BIT, UNSIGNED QUOTIENT.
                          ;CARRY - SET INDICATES NO OVERFLOW (SUCCESS).
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.SBTTL GLOBAL SUBROUTINE - WAIBIC -
;*****
;* - WAIT FOR BIT CLEAR ROUTINE -
;* THIS SUBROUTINE WAITS FOR THE SPECIFIED BIT TO BECOME CLEAR. IF THE
;* SPECIFIED BIT GOES TO A CLEAR STATE WITHIN THE SPECIFIED TIME-OUT
;* PERIOD A SUCCESS INDICATION IS RETURNED BY THIS ROUTINE.
;* THE LAST VALUE WHICH IS READ LOOKING FOR THE CONDITION IS RETURNED TO
;* ALLOW THE USE OF THIS ROUTINE TO LOOK FOR DESTRUCTIVE READ CONDITIONS.
;*
;* INPUTS: R1 - TIME-OUT VALUE AND BIT NUMBER INDICATION:
;*          BITS 15 THRU 12 - NUMBER OF BIT TO TEST (RANGE 0 THRU 15).
;*          BITS 11 THRU 0 - TIME-OUT VALUE IN MILLI-SECONDS (4095 MAX).
;*          R2 - ADDRESS OF WORD CONTAINING THE BIT TO TEST.
;*          MSLCNT.
;*
;* OUTPUTS: R2 - THE LAST WORD WHICH WAS READ TO CHECK FOR THE CONDITION.
;*          CARRY - SUCCESS FLAG (CARRY SET IF BIT CLR BEFORE TIME-OUT).
;*
;* CALLING SEQUENCE:  MOV    #130040,R1    ;PASS BIT 11 (13 OCTAL) AND
;*                   ; 32 (40 OCTAL) MS DELAY.
;*                   MOV    @LABEL,R2    ;TEST BIT IN WORD AT "LABEL".
;*                   JSR    PC,WAIBIC    ;WAIT 32 MS FOR BIT 11 TO CLR.
;*
;* COMMENTS:
;*
;* SUBORDINATE ROUTINES CALLED: MSLGET.
;*****

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WAIBIC:: SAVE
;SAVE CONTENTS OF GPRS R0 THRU R5.
;R5,PREGOS ;CALL REGISTER SAVE SUBRT.
;SET UP THE ADDRESS PARAMETER FOR MSLGET.
        JSR
        MOV    R2,R4
        MOV    R1,R2
        BIC    #170000,R1
        BIC    #7777,R2
        SWAB  R2
        ASR   R2
        ASR   R2
        ASR   R2
        MOV   BITTBL(R2),R2
        CLR  R3
        JSR  PC,MSLGET
;SEPERATE DELAY COUNT OUT OF PASSED PARAMETER.
;SEPERATE LINE NUMBER FIELD OF PASSED PARAM.
;PUT LINE NUMBER FIELD IN LSBYTE.
;SHIFT THE LINE NUMBER FIELD INTO THE PROPER
; POSITION TO USE IT AS A WORD TABLE OFFSET
; FOR THE TABLE LOOKUP OF THE LINE BIT MAP.
;GET BIT MAP OF LINE TO TEST FROM TABLE.
;INDICATE THAT THE BIT SHOULD BE CLR.
;WAIT FOR THE BIT TO BE CLR WITHIN TIME-OUT.
; CARRY IS CORRECT UPON MSLGET RETURN.
;PASS LAST VALUE READ AS OUTPUT PARAMETER.
;RESTORE GPRS, EXCEPT THE FOLLOWING:
;R2,R2SLOT(SP) ;PUT R2 IN STACK SLOT.
;R3,PC, @ (SP). ;RETURN TO PREGOS SUBRT.
; R2 - LAST VALUE READ LOOKING FOR CONDITION.
; CARRY - SUCCESS FLAG (SET IF BIT FOUND CLR).
        MOV   R0,R2
        PASS  R2
        MOV   R2,R2SLOT(SP)
        JSR  PC,@(SP)
        RTS  PC

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023214 004567 160556
023220 010204
023222 010102
023224 042701 170000
023230 042702 007777
023234 000302
023236 006202
023240 006202
023242 006202
023244 016202 002410
023250 005003
023252 004767 174706
023256 010002
023260 010266 000006
023264 004736

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023270 004567 160502
023274 010204
023276 010102
023300 042701 170000
023304 042702 007777
023310 000302
023312 006202
023314 006202
023316 006202
023320 016202 002410
023324 010203
023326 004757 174632
023332 010002
023334 010266 000006
023340 004736
023342 000207

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.SBTTL GLOBAL SUBROUTINE - WAIBIS -
;*****
;* - WAIT FOR BIT SET ROUTINE -
;* THIS SUBROUTINE WAITS FOR THE SPECIFIED BIT TO BECOME SET. IF THE
;* SPECIFIED BIT GOES TO A SET STATE WITHIN THE SPECIFIED TIME-OUT
;* PERIOD A SUCCESS INDICATION IS RETURNED BY THIS ROUTINE.
;* THE LAST VALUE WHICH IS READ LOOKING FOR THE CONDITION IS RETURNED TO
;* ALLOW THE USE OF THIS ROUTINE TO LOOK FOR DESTRUCTIVE READ CONDITIONS.
;*
;* INPUTS: R1 - TIME-OUT VALUE AND BIT NUMBER INDICATION:
;*          BITS 15 THRU 12 - NUMBER OF BIT TO TEST (RANGE 0 THRU 15).
;*          BITS 11 THRU 0 - TIME-OUT VALUE IN MILLI-SECONDS (4095 MAX).
;*          R2 - ADDRESS OF WORD CONTAINING THE BIT TO TEST.
;*          MSLCNT.
;*
;* OUTPUTS: R2 - THE LAST WORD WHICH WAS READ TO CHECK FOR THE CONDITION.
;*          CARRY - SUCCESS FLAG (CARRY SET IF BIT SET BEFORE TIME-OUT).
;*
;* CALLING SEQUENCE:  MOV    #130040,R1    ;PASS BIT 11 (13 OCTAL) AND
;*                   ; 32 (40 OCTAL) MS DELAY.
;*                   MOV    #LABEL,R2    ;TEST BIT IN WORD AT "LABEL".
;*                   JSR    PC,WAIBIS    ;WAIT 32 MS FOR BIT 11 TO SET.
;*
;* COMMENTS:
;*
;* SUBORDINATE ROUTINES CALLED: MSLGET.
;*****
WAIBIS:: SAVE
;SAVE CONTENTS OF GPRS R0 THRU R5.
;R5,PREG05 ;CALL REGISTER SAVE SUBRT.
;SET UP THE ADDRESS PARAMETER FOR MSLGET.
        JSR
        MOV    R2,R4
        MOV    R1,R2
        BIC    #170000,R1
        BIC    #7777,R2
        SWAB  R2
        ASR   R2
        ASR   R2
        ASR   R2
        MOV    BITTBL(R2),R2
        MOV    R2,R3
        JSR    PC,MSLGET
;SEPERATE DELAY COUNT OUT OF PASSED PARAMETER.
;SEPERATE LINE NUMBER FIELD OF PASSED PARAM.
;PUT LINE NUMBER FIELD IN LSBYTE.
;SHIFT THE LINE NUMBER FIELD INTO THE PROPER
; POSITION TO USE IT AS A WORD TABLE OFFSET
; FOR THE TABLE LOOKUP OF THE LINE BIT MAP.
;GET BIT MAP OF LINE TO TEST FROM TABLE.
;INDICATE THAT THE BIT SHOULD BE SET.
;WAIT FOR THE BIT TO BE SET WITHIN TIME-OUT.
; CARRY IS CORRECT UPON MSLGET RETURN.
;PASS LAST VALUE READ AS OUTPUT PARAMETER.
;RESTORE GPRS, EXCEPT THE FOLLOWING:
        MOV    R2,R2SLOT(SP) ;PUT R2 IN STACK SLOT.
        JSR    PC,B(SP). ;RETURN TO PREG05 SUBRT.
; R2 - LAST VALUE READ LOOKING FOR CONDITION.
; CARRY - SUCCESS FLAG (SET IF BIT FOUND SET).
        RTS    PC

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023344
023344 004567 160426

023350 005005

023352 010204

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.SBTTL GLOBAL SUBROUTINE - WDPDR -
;*****
;* - WRITE DATA PATTERN TO DEVICE REGISTERS -
;* THIS ROUTINE WRITES A ROTATED DATA PATTERN TO EACH OF THE 6 DEVICE
;* REGISTERS OF EACH ACTIVE LINE OF THE DEVICE UNDER TEST.
;* THE DATA PATTERN IS ROTATED ONCE AFTER EACH WRITE TO A DEVICE REGISTER
;* ON A PARTICULAR LINE. THE STARTING DATA PATTERN FOR EACH LINE
;* IS ROTATED ONCE AFTER WRITING ALL THE REGISTERS ON A PARTICULAR
;* LINE. THIS LEADS TO THE FOLLOWING DATA PATTERN:
;* LINE 0, REGISTER 0 - SHIFTED 0 BIT POSITIONS
;* LINE 0, REGISTER 1 - SHIFTED 1 BIT POSITION
;*
;* LINE 1, REGISTER 0 - SHIFTED 1 BIT POSITION
;* LINE 2, REGISTER 1 - SHIFTED 2 BIT POSITIONS
;*
;* ANY BITS FIELDS IN THE DEVICE REGISTERS THAT CANNOT BE ALTERED
;* ARE MASKED OUT OF THE DATA PATTERN BEFORE IT IS WRITTEN.
;* THIS ROUTINE WILL USE EITHER MOV, MOVB, BIS, BISB, BIC, OR BICB
;* INSTRUCTIONS. THE UPPER OR LOWER BYTE CAN BE SPECIFIED FOR WRITING.
;*
;* INPUTS: R2 - USED TO PASS IN THE DATA PATTERN TO BE ROTATED & WRITTEN.
;* R3 - BYTE INDICATOR (- => LO BYTE, + => HI BYTE, 0 => BOTH).
;* R4 - OPERATION TYPE INDICATOR (- => BIC, + => BIS, 0 => MOV).
;* ACTLNS - BIT MAP OF THE ACTIVE LINES ON THE DEVICE UNDER TEST.
;* CSRA - CONTAINS THE CSR ADDRESS OF THE DEVICE UNDER TEST.
;* DRADRT - BASE ADDRESS OF DEVICE REGISTER ADDRESS TABLE.
;* LPRO - EQUATED TO LPR REG OFFSET FROM DEVICE CSR ADDRESS.
;* NUMLNS - NUMBER OF LINES ON THE DEVICE UNDER TEST.
;* TXBFCO - EQUATED TO TBUFFCT REG OFFSET FROM DEVICE CSR ADDRESS.
;* UNBTB - BASE ADDRESS OF THE UNUSED BIT TABLE.
;*
;* OUTPUTS: DEVICE REGISTERS ON ALL ACTIVE DEVICE LINES ARE MODIFIED.
;*
;* CALLING SEQUENCE: JSR PC.WDPDR
;*
;* COMMENTS: THIS ROUTINE DOES NOT WRITE DATA TO THE FOLLOWING REGISTERS.
;* RBUF
;* RXTIMER
;* STAT
;* FIFOSIZE
;* FIFODATA
;*
;* THE CSR IS CLEARED EXCEPT FOR THE IND.ADR.REG FIELD.
;*
;* SUBORDINATE ROUTINES CALLED: ROLDAP.
;*****
WDPDR:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
; JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
; SET UP OUTER LOOP WHICH WRITES THE DATA PATTERN TO EACH LINE'S REGISTERS
;
; CLR R5 ;CLEAR LINE COUNTER TO SELECT LINE 0.
;
; THE OUTER LOOP FOLLOWS. EACH PASS THROUGH THIS LOOP WRITES DATA TO ALL OF
; THE DEVICE REGISTERS FOR A PARTICULAR LINE IF IT IS ACTIVE.
;
;
; 2: MOV R2,R4 ;SAVE THE OUTER LOOP DATA PATTERN.
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4165 023354 010577 156662      MOV    R5,@CSRA      ;SET CSR IND.ADR.REG FIELD TO THIS LINE.
4166 023360 006305              ASL    R5             ;TURN LINE NUMBER INTO A WORD OFFSET.
4167 023362 036567 002410 156640  BIT    BITTBL(R5),ACTLNS
4168 023370 001456              BEQ    20$           ;LINE ACTIVE? NO, SKIP THIS LINE.
4169 023372 012701 000004      MOV    @LPRO,R1     ;YES, INITIALIZE THE REGISTER OFFSET.
4170
4171      ;+
4172      ; THE INNER LOOP FOLLOWS. EACH PASS THROUGH THIS LOOP WRITES DATA TO A
4173      ; DEVICE REGISTER.
4174 023376 010200              ;+
4175 023400 046100 002370      4$:-  MOV    R2,R0
4176 023404 016103 002242      BIC    UNBITT(R1),R0 ;CLEAR BIT FIELDS FOR UNUSED REGISTER BITS.
4177 023410 005766 000010      MOV    DRADRT(R1),R3 ;GET THE ADDRESS OF THE DEVICE REGISTER.
4178 023414 003402              TST    R3SLOT(SP)   ;CHECK THE OPERAND TYPE INDICATOR.
4179 023416 005203              BLE    6$           ;HIGH BYTE? NO, SKIP HIGH BYTE ADDRESS SET UP.
4180 023420 000300              INC    R3           ;YES, SET THE REG ADDRESS TO THE HIGH BYTE.
4181 023422 005766 000010      SWAB   R0           ;MOVE HIGH BYTE DATA INTO THE LOW BYTE.
4182 023426 001412              6$:-  TST    R3SLOT(SP) ;CHECK THE OPERAND TYPE INDICATOR.
4183      BEQ    12$       ;WORD ACCESS? YES, GO PERFORM WORD ACCESS.
4184      ;+
4185      ; PERFORM BYTE ACCESS TO THE SPECIFIED BYTE OF THE SPECIFIED REGISTER.
4186 023430 005766 000012      ;+
4187 023434 100403              TST    R4SLOT(SP)   ;NO, CHECK THE ACCESS TYPE INDICATOR.
4188 023436 001404              BMI    8$           ;USE BIC? YES, GO PERFORM BICB INSTRUCTION.
4189 023440 150013              BEQ    10$          ;USE MOV? YES, GO PERFORM MOV B INSTRUCTION.
4190 023442 000415              BISB   R0,(R3)      ;NEITHER. PERFORM BISB ACCESS TO REGISTER.
4191 023444 140013              BR     18$
4192 023446 000413              8$:-  BICB   R0,(R3) ;PERFORM BICB ACCESS TO REGISTER.
4193 023450 110013              BR     18$
4194 023452 000411              10$:- MOV B   R0,(R3) ;PERFORM MOV B ACCESS TO REGISTER.
4195      BR     18$
4196      ;+
4197      ; PERFORM WORD ACCESS TO THE SPECIFIED REGISTER.
4198 023454 005766 000012      ;+
4199 023460 100403              TST    R4SLOT(SP)   ;CHECK THE ACCESS TYPE INDICATOR.
4200 023462 001404              BMI    14$          ;USE BIC? YES, GO PERFORM BIC INSTRUCTION.
4201 023464 050013              BEQ    16$          ;USE MOV? YES, GO PERFORM MOV INSTRUCTION.
4202 023466 000403              BIS    R0,(R3)     ;NEITHER. PERFORM BIS ACCESS TO REGISTER.
4203 023470 040013              BR     18$
4204 023472 000401              14$:- BIC    R0,(R3) ;PERFORM BIC ACCESS TO REGISTER.
4205 023474 010013              BR     18$
4206      16$:-  MOV    R0,(R3) ;PERFORM MOV ACCESS TO REGISTER.
4207      ;+
4208      ; PREPARE THE DATA PATTERN AND OFFSET FOR THE NEXT REGISTER ON THIS LINE.
4209 023476 004767 175766      18$:- JSR    PC,ROLDAP   ;ROTATE DATA PATTERN LEFT, NOT THROUGH CARRY.
4210 023502 062701 000002      ADD    @2,R1        ;INCREMENT OFFSET FOR NEXT REGISTER.
4211 023506 020127 000006      CMP    R1,@FSLSO    ;CHECK IF THIS IS THE FIFOSIZE/DATA REG
4212 023512 001002              BNE    19$          ;AVOID ALTERING THE OFFSET IF IT ISN'T.
4213 023514 062701 000002      ADD    @2,R1        ;AVOID TESTING THESE REGISTERS.
4214 023520 020127 000016      19$:-  CMP    R1,@TXBFCO ;COMPARE REG OFFSET WITH OFFSET OF LAST REG.
4215 023524 003724              BLE    4$           ;LOOP IF NOT ALL REG DONE FOR THIS LINE.
4216
4217      ;+
4218      ; BACK INTO THE OUTER LOOP. NOW SET UP FOR NEXT LINE. LOOP IF NOT DONE.
4219 023526 010402              ;+
4220 023530 004767 175734      20$:- MOV    R4,R2
4221 023534 006205              JSR    PC,ROLDAP   ;SET UP TO ROTATE THE DATA PATTERN.
                          ASR    R5       ;ROTATE THE DATA PATTERN.
                          ;CONVERT BACK TO LINE NUMBER FROM WORD OFFSET.

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4222 023536 005205
4223 023540 020527 000020
4224 023544 002702
4225
4226 023546
023546 004736
4227 023550 000207

60%:

INC R5
CMP R5,#NUMLNS
BLT 2#
PASS
RTS PC

JSR

;COUNT THIS LINE.
;COMPARE LINE COUNT WITH NUMBER OF LINES.
;LOOP IF SOME LINES NOT DONE.

;RESTORE GPRS.
PC,@(SP)+

;RETURN TO PREGOS SUBRT.


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4229 .SBTTL GLOBAL SUBROUTINE - WTWLNC -
4230 ;* *****
4231 ;* - LINE CONTROL REGISTER SETUP ROUTINE -
4232 ;* THIS SUBROUTINE IS USED TO SET THE DEVICE UNDER TEST (DUT) LINE
4233 ;* CONTROL REGISTERS (LNCTRL) TO THE SPECIFIED STATE. ONLY THE LNCTRLS
4234 ;* FOR THE SPECIFIED LINES ARE ALTERED.
4235 ;*
4236 ;* INPUTS: R0 - NEW LINE PARAMETERS.
4237 ;* R5 - BIT MAP OF LINES TO BE ALTERED.
4238 ;* CSRA - CONTAINS ADDRESS OF THE DUT CSR.
4239 ;* IESTAT - CONTAINS THE CURRENT STATE OF THE TX AND RX INTERRUPT
4240 ;* ENABLE BITS IN THE CSR.
4241 ;* LNCTRA - CONTAINS ADDRESS OF THE DUT LNCTRL REGISTERS.
4242 ;*
4243 ;* OUTPUTS: LNCTRL - SPECIFIED DUT LINE CONTROL REGISTERS ARE ALTERED.
4244 ;*
4245 ;* CALLING SEQUENCE: JSR PC,WTWLNC
4246 ;*
4247 ;* COMMENTS:
4248 ;*
4249 ;* SUBORDINATE ROUTINES CALLED: ALTFLD.
4250 ;* - - - - -
4251
4252 023552 WTWLNC:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
023552 004567 160220 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
4253 ;*
4254 ; SET UP THE PARAMETERS FOR THE CALL TO ALTFLD.
4255 ; -
4256 023556 016701 156470 MOV LNCTRA,R1 ;SET UP THE REGISTER ADDRESS PARAMETER.
4257 023562 010002 MOV R0,R2 ;SET UP THE DESIRED REGISTER CONTENTS.
4258 023564 010503 MOV R5,R3 ;SET UP THE BIT MAP OF LINES TO ALTER.
4259 023566 012704 177777 MOV #-1,R4 ;SELECT ALL REGISTER BITS TO BE ALTERED.
4260 ;*
4261 ; CALL THE SUBROUTINE WHICH ALTERS THE REGISTER CONTENTS.
4262 ; -
4263 023572 004767 173656 JSR PC,ALTFLD ;ALTER THE REGISTER CONTENTS.
4264 ;*
4265 023576 004736 60%: PASS ;RESTORE GPRS.
023576 000207 JSR PC,@(SP) ;RETURN TO PREG05 SUBRT.
4266 023600 RTS PC

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4291 023602
023602 004567 160170
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4295 023606 016701 156434
4296 023612 010002
4297 023614 010503
4298 023616 012704 177777
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4302 023622 004767 173626
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4304 023626
023626 004736
4305 023630 000207

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.SBTTL GLOBAL SUBROUTINE - WTWLPR -
; * *****
; * - LINE PARAMETER REGISTER SETUP ROUTINE -
; * THIS SUBROUTINE IS USED TO SET THE DEVICE UNDER TEST (DUT) LINE
; * PARAMETER REGISTERS (LPR) TO THE SPECIFIED STATE. ONLY THE LPRS FOR
; * THE SPECIFIED LINES ARE ALTERED.
; *
; * INPUTS: R0 - NEW LINE PARAMETERS.
; * R5 - BIT MAP OF LINES TO BE ALTERED.
; * CSRA - CONTAINS ADDRESS OF THE DUT CSR.
; * IESTAT - CONTAINS THE CURRENT STATE OF THE TX AND RX INTERRUPT
; * ENABLE BITS IN THE CSR.
; * LPRA - CONTAINS ADDRESS OF THE DUT LPR.
; *
; * OUTPUTS: LPR - SPECIFIED DUT LINE PARAMTER REGISTERS ARE ALTERED.
; *
; * CALLING SEQUENCE: JSR PC,WTWLPR
; *
; * COMMENTS:
; *
; * SUBORDINATE ROUTINES CALLED: ALTFLD.
; - *****
WTWLPR:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
; JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
; *
; * SET UP THE PARAMETERS FOR THE CALL TO ALTFLD.
; -
; * MOV LPRA,R1 ;SET UP THE REGISTER ADDRESS PARAMETER.
; * MOV R0,R2 ;SET UP THE DESIRED REGISTER CONTENTS.
; * MOV R5,R3 ;SET UP THE BIT MAP OF LINES TO ALTER.
; * MOV #-1,R4 ;SELECT ALL REGISTER BITS TO BE ALTERED.
; *
; * CALL THE SUBROUTINE WHICH ALTERS THE REGISTER CONTENTS.
; -
; * JSR PC,ALTFLD ;ALTER THE REGISTER CONTENTS.
; *
60$: PASS ;RESTORE GPRS.
; JSR PC,@(SP), ;RETURN TO PREG05 SUBRT.
RTS PC
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023632		
023632	004567	160140
023636	016701	156452
023642	005201	
023644	102001	
023646	005301	
023650	010167	156440
023654		
023654	004736	
023656	000002	

```

.SBTTL  INTERRUPT SERVICE ROUTINE      - CACHRX -
; * *****
; * - CATCH RECEIVER INTERRUPT.
; * THIS ROUTINE IS USED IN SEVERAL TESTS, TO LOG A COUNT OF THE
; * NUMBER OF RECEIVER INTERRUPTS THAT OCCUR.
; *
; * INPUTS:      CSRA - CONTAINS THE ADDRESS OF THE CSR.
; *              RXINTC - HOLDS THE COUNT OF THE NUMBER OF RX INTERRUPTS
; *                  THAT OCCURRED.
; *
; * OUTPUTS:     RXINTC - CONTAINS THE UPDATED INTERRUPT COUNT.
; *
; * CALLING SEQUENCE:  PUT THE ADDRESS OF THE LABEL CACHRX IN THE VECTOR
; *                  LOCATION.
; *
; * COMMENTS:
; *
; * SUBORDINATE ROUTINES CALLED: NONE
; * - - *****
CACHRX::SAVE
;SAVE CONTENTS OF GPRS R0 THRU R5.
;R5,PREG05 ;CALL REGISTER SAVE SUBRT.
      JSR
      MOV     RXINTC,R1 ;GET THE RECEIVER INTERRUPT COUNT
      INC     R1        ;INCREMENT THE COUNT
      BVC    2$        ;BRANCH IF NO OVERFLOW OCCURRED
      DEC     R1        ;RESET THE COUNT TO 177777
      MOV     R1,RXINTC ;SAVE NEW COUNT VALUE
      PASS
      JSR
      ;RESTORE GPRS.
      ;RETURN TO PREG05 SUBRT.
      RTI
  
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023660 004567 160112
4359 023664 016701 156436
4360 023670 005201
4361 023672 102001
4362 023674 005301
4363 023676 010167 156424
4364 023702
023702 004736
4365 023704 000002

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.SBTTL INTERRUPT SERVICE ROUTINE - CACHTX -
; * *****
; * - CATCH TRANSMITTER INTERRUPT.
; * THIS ROUTINE IS USED IN SEVERAL TESTS, TO LOG A COUNT OF THE
; * NUMBER OF TRANSMISSION INTERRUPTS THAT OCCUR.
; *
; * INPUTS: CSRA - CONTAINS THE ADDRESS OF THE CSR.
; * TXINTC - HOLDS THE COUNT OF THE NUMBER OF TX INTERRUPTS
; * THAT OCCURRED.
; *
; * OUTPUTS: TXINTC - CONTAINS THE UPDATED INTERRUPT COUNT.
; *
; * CALLING SEQUENCE: PUT THE ADDRESS OF THE LABEL CACHTX IN THE VECTOR
; * LOCATION.
; *
; * COMMENTS:
; *
; * SUBORDINATE ROUTINES CALLED: NONE
; - - *****
CACHTX::SAVE
;SAVE CONTENTS OF GPRS R0 THRU R5.
R5,PREG05 ;CALL REGISTER SAVE SUBRT.
MOV TXINTC,R1 ;GET THE TRANSMISSION INTERRUPT COUNT
INC R1 ;INCREMENT THE COUNT
BVC 2$ ;BRANCH IF NO OVERFLOW OCCURRED
DEC R1 ;RESET THE COUNT TO 177777
2$: MOV R1,TXINTC ;SAVE NEW COUNT VALUE
60$: PASS ;RESTORE GPRS.
RTI JSR PC,@(SP)+ ;RETURN TO PREG05 SUBRT.

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023706 005767 156432
023712 001402
023714 005367 156424
023720 005767 156422
023724 001402
023726 005367 156414
023732 005367 156412
023736 001006
023740 016767 156406 156402
023746 010046
023750 104422
023752 012600
023754 000002

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.SBTTL INTERRUPT SERVICE ROUTINE - CLKINT -
; * *****
; * THIS ROUTINE IS EXECUTED CLKHRZ TIMES PER SECOND. IT DECREASES THE
; * TWO TIMER COUNTERS DOWN TO ZERO.
; *
; * INPUTS: TIMER1 - TIMER COUNTER #1.
; *          TIMER2 - TIMER COUNTER #2.
; *          TIMER3 - TIMER COUNTER FOR CALL OF BREAK MACRO.
; *
; * OUTPUTS: THE 2 TIMER COUNTERS ARE DECREMENTED IF THEY ARE NOT ZERO.
; *
; * CALLING SEQUENCE: PUT #CLKINT IN THE CLOCK INTERRUPT VECTOR SLOT.
; *                   PUT THE DESIRED TIME PERIOD (SECONDS TIMES CLKHRZ) IN
; *                   EITHER TIMER1 OR TIMER2 AND POLL THE RESPECTIVE TIMER
; *                   COUNTER TO DETECT ITS GOING TO 0 ON TIME-OUT.
; *
; * COMMENTS: THE 2 COUNTERS WILL NOT WRAPAROUND BUT WILL STOP AT 0. THIS
; *            ALLOWS THE DETECTION OF A TIME-OUT ANY TIME AFTER THE TIME-OUT
; *            HAS OCCURRED UNTIL THE TIMER COUNTER IS SET TO ANOTHER VALUE.
; *
; * SUBORDINATE ROUTINES CALLED: NONE.
; - - *****
CLKINT:: TST    TIMER1    ;CHECK FOR TIMER1 AT ZERO.
        BEQ    2$        ;BRANCH TO LEAVE IT AT ZERO IF IT IS ZERO.
        DEC    TIMER1    ;DECREMENT TIME COUNT.
2$:     TST    TIMER2    ;CHECK FOR TIMER2 AT ZERO.
        BEQ    4$        ;BRANCH TO LEAVE IT ALONE IF IT'S ALREADY ZERO.
        DEC    TIMER2    ;DECREMENT TIME COUNT.
4$:     DEC    TIMER3    ;DECREMENT THE BREAK COUNT.
        BNE    60$      ;EXIT IF NOT TIME TO CALL BREAK.
        MOV    BCOUNT,TIMER3 ;SET UP TIME TILL NEXT BREAK.
        MOV    RO,-(SP)  ;SAVE CONTENTS OF RO FROM BREAK MACRO.
        BREAK          ;CHECK FOR OPERATOR CONTROL/C.
60$:   MOV    (SP),RO    ;RESTORE CONTENTS OF RO.
        RTI

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TRAP C\$BRK

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023756	004567	160014	
023756	017700	156256	
023762	016701	156322	
023772	005201		
023774	001402		
023776	010167	156312	
024002	016701	156310	
024006	052701	000001	
024012	032767	000001	156310
024020	001402		
024022	052701	040000	
024026	010167	156264	
024032	004736		
024034	000002		

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.SBTTL  INTERRUPT SERVICE ROUTINE          - RXBRRT -
; ** *****
;* - BR LEVEL TEST RECEIVE INTERRUPT SERVICE ROUTINE -
;* THIS SERVICE ROUTINE HANDLES RECEIVE INTERRUPTS DURING THE INTERRUPT
;* BR LEVEL TEST. THIS ROUTINE COUNTS THE INTERRUPT AND SETS A FLAG
;* TO INDICATE THAT THE INTERRUPT HAS OCCURRED. IT ALSO CHECKS THE
;* FLAG WHICH INDICATES THAT A TX INTERRUPT HAS OCCURRED. IF THE TX
;* INTERRUPT FLAG IS SET, THIS ROUTINE SETS AN INTERRUPT ORDER ERROR
;* FLAG INDICATING THAT A TRANSMIT INTERRUPT WAS SERVICED BEFORE A
;* SIMULTANEOUS RECEIVE INTERRUPT.
;*
;* INPUTS:      RXINTC - HOLDS THE COUNT OF THE NUMBER OF RX INTERUPTS.
;*              RXINTF - RX INTERRUPT FLAGS.
;*
;* OUTPUTS:     RXINTC - CONTAINS THE UPDATED INTERUPT COUNT.
;*              RXINTF - RX INT FLAGS:
;*                  (BIT 0 SET, BIT 14 SET IF TXINTF BIT 0 IS SET.)
;*
;* CALLING SEQUENCE:  PUT THE ADDRESS OF THE LABEL RXBRRT IN THE VECTOR
;* LOCATION.
;*
;* COMMENTS:      NOTE: THE FIFO IS NOT PURGED BY THIS ROUTINE.
;*
;* SUBORDINATE ROUTINES CALLED: NONE.
;-- *****
RXBRRT:: SAVE
;SAVE CONTENTS OF GPRS R0 THRU R5.
;R5,PREG05 ;CALL REGISTER SAVE SUBRT.
MOV  @RBUFA,R0 ;READ THE CHAR OUT OF THE FIFO.
MOV  RXINTC,R1 ;GET THE INTERRUPT COUNT.
INC  R1 ;INCREMENT THE COUNT.
BEQ  2$ ;BYPASS UPDATING COUNT IF OVERFLOW OCCURRED.
MOV  R1,RXINTC ;SAVE NEW COUNT VALUE.
2$:  MOV  RXINTF,R1 ;GET THE RX INTERRUPT FLAGS.
BIS  @BIT0,R1 ;SET THE RX INTERRUPT HAS OCCURRED FLAG.
BIT  @BIT0,TXINTF ;TEST THE "TX INT HAS OCCURRED" FLAG.
BEQ  4$ ;SKIP SETTING ERROR FLAG IF NO TX INT.
BIS  @BIT14,R1 ;SET THE INTERRUPT ORDER ERROR FLAG.
4$:  MOV  R1,RXINTF ;UPDATE THE RX INTERRUPT FLAGS.
60$: PASS ;RESTORE GPRS.
;RTI ;RETURN TO PREG05 SUBRT.
RTI
  
```



```

4445 .SBTTL INTERRUPT SERVICE ROUTINE - RXINPT -
4446 ;** *****
4447 ;* - RECEIVE CHARACTER INPUT INTERRUPT SERVICE ROUTINE -
4448 ;* THIS SERVICE ROUTINE INPUTS A CHARACTER FROM THE DUT AND LOADS THE
4449 ;* CHAR (COMPLETE WITH STATUS FLAGS) INTO A RECEIVE CHAR BUFFER IN
4450 ;* MEMORY. THE INTERRUPT IS ALSO COUNTED. THE RECEIVE CHAR BUFFER IS
4451 ;* MONITORED TO ENSURE THAT IT DOES NOT OVERFLOW.
4452 ;*
4453 ;* INPUTS: BUFEND - LABELS THE END OF THE HOST MEMORY BUFFER.
4454 ;* BUFPTR - CONTAINS ADDRESS OF NEXT FREE BUFFER LOCATION.
4455 ;* CSRA - CONTAINS THE ADDRESS OF THE DUT CSR.
4456 ;* RBUFA - CONTAINS THE ADDRESS OF THE RBUF DUT REGISTER.
4457 ;* RXINTC - HOLDS THE COUNT OF THE NUMBER OF RX INTERRUPTS.
4458 ;* RXINTF - RX INTERRUPT FLAGS.
4459 ;*
4460 ;* OUTPUTS: BUFPTR - CONTAINS UPDATED ADDRESS OF NEXT FREE BUFFER LOCATION.
4461 ;* RXINTC - CONTAINS THE UPDATED INTERRUPT COUNT.
4462 ;* RXINTF - RX INT FLAGS (BIT 15 SET IF RX.DATA.AVAIL IS CLEAR).
4463 ;*
4464 ;* CALLING SEQUENCE: PUT THE ADDRESS OF THE LABEL RXINPT IN THE VECTOR
4465 ;* LOCATION.
4466 ;*
4467 ;* COMMENTS: IN CASE OF OVERFLOW OF THE MEMORY BUFFER, BUFPTR WILL BE
4468 ;* MAINTAINED EQUAL TO BUFEND AND THE WORD AT BUFPTR WILL BE
4469 ;* THE LAST WORD READ FROM THE DUT FIFO.
4470 ;* NOTE: THIS ROUTINE CAN DESTROY TX.ACTIONS BY READING THE CSR.
4471 ;*
4472 ;* SUBORDINATE ROUTINES CALLED: NONE.
4473 ;-- *****
4474
4475 024036 RXINPT:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
024036 004567 157734 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
4476 024042 017701 156174 ;READ THE CONTENTS OF THE CSR.
MOV @CSRA,R1 ;TEST RX.DATA.AVAIL BIT.
4477 024046 032701 000200 BIT #BIT7,R1 ;BRANCH AROUND SETTING FLAG IF BIT IS SET.
4478 024052 001003 BNE 2$ ;SET THE RX.DATA.AVAIL CLEAR FLAG.
4479 024054 052767 100000 156234 2$: MOV RXINTC,R1 ;GET THE INTERRUPT COUNT.
4480 024062 016701 156226 INC R1 ;INCREMENT THE COUNT.
4481 024066 005201 BEQ 4$ ;BYPASS UPDATING COUNT IF OVERFLOW OCCURRED.
4482 024070 001402 MOV R1,RXINTC ;SAVE NEW COUNT VALUE.
4483 024072 010167 156216 4$: MOV BUFPTR,R2 ;GET THE POINTER TO NEXT FREE BUFFER WORD.
4484 024076 016702 156200 MOV @RBUFA,(R2)+ ;READ A CHAR FROM THE FIFO INTO BUFFER.
4485 024102 017722 156136 CMP R2,BUFEND ;TEST FOR POINTER BEYOND END OF BUFFER.
4486 024106 020267 157614 BHIS 60$ ;SKIP THE PTR UPDATE IF PTR OUT OF BOUNDS.
4487 024112 103002 MOV R2,BUFPTR ;UPDATE THE BUFFER POINTER.
4488 024114 010267 156162 60$: PASS ;RESTORE GPRS.
4489 024120 JSR PC,@(SP)+ ;RETURN TO PREG05 SUBRT.
024120 004736
4490 024122 000002 RTI

```

```

4492 .SBTTL GLOBAL TRAP SERVICE ROUTINE - TP4RTN -
4493 :*****
4494 :* BUS TIME-OUT TRAP (004 TRAP) SERVICE ROUTINE -
4495 :* THIS ROUTINE DETERMINES IF THE 004 TRAP WAS CAUSED BY
4496 :* AN "EXPECTED" ERROR OR NOT BY EXAMINING THE RETURN PC VALUE ON THE
4497 :* STACK. IF THE TRAP IS UNEXPECTED, THIS ROUTINE JUMPS TO THE NORMAL
4498 :* DIAGNOSTIC SUPERVISOR 004 TRAP HANDLING ROUTINE.
4499 :*
4500 :*
4501 :* INPUTS: SP - POINTS TO THE PC WHERE THE TRAP OCCURED.
4502 :* ADRPTR - LABEL AT THE ADDRESS WHERE "EXPECTED" TRAPS OCCUR.
4503 :* TP4FLG - 004 TRAP FLAGS.
4504 :*
4505 :* OUTPUTS: TP4FLG - BIT 15 IS SET IF "EXPECTED" TRAP OCCURED.
4506 :*
4507 :* CALLING SEQUENCE: PUT ADDRESS POINTED TO BY TP4RTN IN 004 VECTOR.
4508 :* OCCURENCE OF 004 TRAP VECTORS TO THIS ROUTINE.
4509 :*
4510 :* COMMENTS: ANY 004 TRAP WHICH OCCURS AT AN ADDRESS OTHER THAN THAT LABELED
4511 :* ADRPTR WILL BE HANDLED BY THE NORMAL 004 TRAP SERVICE ROUTINE.
4512 :*
4513 :* SUBORDINATE ROUTINES CALLED: NONE.
4514 :*****
4515
4516 024124 021627 017764 TP4RTN:: CMP (SP),#ADRPTR ;COMPARE EXPECTED ADR AGAINST TRAP RET PC.
4517 024130 001402 BEQ 2$ ;IF THEY MATCH, CONTINUE THIS ROUTINE.
4518 024132 000177 156164 JMP @TP4VEC ;IF NOT, JUMP TO NORMAL 004 TRAP SERVICE RTN.
4519 024135 052767 100000 156154 2$: BIS #BIT15,TP4FLG ;SET THE 004 TRAP OCCURED FLAG.
4520 024144 000002 RTI ;ALL DONE, GO BACK TO THE TEST.

```



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4542
4543
4544
4545
4546 024146
      024146 004567 157624
4547 024152 016701 156150
4548 024156 005201
4549 024160 102001
4550 024162 005301
4551 024164 010167 156136
4552 024170 016703 156134
4553 024174 017702 156042
4554 024200 100402
4555 024202 052703 100000
4556 024206 052703 000001
4557 024212 010367 156112
4558 024216
      024216 004736
4559 024220 000002

```

```

.SBTTL INTERUPT SERVICE ROUTINE - TXINTR -
; * *****
; * - TRANSMIT INTERRUPT SERVICE ROUTINE -
; * THIS ROUTINE HANDLES A TRANSMIT INTERRUPT FROM THE DEVICE UNDER TEST
; * (DUT) BY COUNTING THE INTERRUPT AND READING THE DUT CSR TO CLEAR THE
; * INTERRUPT REQUEST. THIS ROUTINE ALSO SETS A FLAG TO INDICATE THAT
; * A TX INTERRUPT HAS OCCURRED AND SETS A FLAG IF THE TX.ACTION BIT IS
; * NOT SET IN THE READ CONTENTS OF THE DUT CSR.
; *
; * INPUTS: CSRA - CONTAINS THE ADDRESS OF THE CSR.
; * TXINTC - HOLDS THE COUNT OF THE NUMBER OF TX INTERUPTS.
; * TXINTF - TX INTERRUPT FLAGS.
; *
; * OUTPUTS: TXINTC - CONTAINS THE UPDATED TX INTERUPT COUNT.
; * TXINTF - TX INT FLAGS (BIT 0 SET, BIT 15 SET IF TX.ACTION CLR).
; *
; * CALLING SEQUENCE: PUT THE ADDRESS OF THE LABEL TXINTR IN THE VECTOR
; * LOCATION.
; *
; * COMMENTS:
; *
; * SUBORDINATE ROUTINES CALLED: NONE
; * - - - - -

```

```

TXINTR:: SAVE
;SAVE CONTENTS OF GPRS R0 THRU R5.
R5,PREG05 ;CALL REGISTER SAVE SUBRT.
MOV TXINTC,R1 ;GET THE TX INTERUPT COUNT.
INC R1 ;INCREMENT THE COUNT.
BVC 2$ ;BRANCH IF NO OVERFLOW OCCURRED.
DEC R1 ;RESET THE COUNT TO 177777.
2$: MOV R1,TXINTC ;SAVE NEW COUNT VALUE.
MOV TXINTF,R3 ;GET THE TX INTERRUPT FLAGS.
MOV @CSRA,R2 ;READ THE CSR.
BMI 4$ ;SKIP SETTING OF FLAG IF TX.ACTION IS SET.
BIS @BIT15,R3 ;SET THE TX.ACTION CLEAR FLAG.
4$: BIS @BIT0,R3 ;SET THE TX INT HAS OCCURRED FLAG.
MOV R3,TXINTF ;UPDATE THE TX INTERRUPT FLAGS.
60$: PASS ;RESTORE GPRS.
;RETURN TO PREG05 SUBRT.
JSR PC,@(SP).
RTI

```

4568
 4569
 4570
 4571
 4572
 4573
 4574
 4575
 4576
 4577 024222
 024222
 4578
 4579 024222
 024222 000167
 024224 000000
 4580
 4581
 4582
 4583 024226
 024226
 024226 104425

.SBTTL REPORT CODING SECTION

```

; **
; THE REPORT CODING SECTION CONTAINS THE
; "PRINTS" CALLS THAT GENERATE STATISTICAL REPORTS.
; **

```

BGNRPT

L1RPT::

EXIT RPT

```

.WORD JSJMP
.WORD L10017-2-

```

.EVEN

ENDRPT

L10017:

```

TRAP CSRPT

```


4585
4593
4594
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4596
4597
4598
4599
4600
4601
4602
4603
4604
4605
4606
4607

024230
024230
177777
177777
177777
024236

.SBTTL PROTECTION TABLE

; THIS TABLE IS USED BY THE RUNTIME SERVICES
; TO PROTECT THE LOAD MEDIA.
;--

BGNPROT

L\$PROT::

-1 ;OFFSET INTO P-TABLE FOR CSR ADDRESS
-1 ;OFFSET INTO P-TABLE FOR MASSBUS ADDRESS
-1 ;OFFSET INTO P-TABLE FOR DRIVE NUMBER

ENDPROT

```

4629
4630
4631
4632
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4634
4635
4636
4637
4638
4639
4640
4641
4642
4643 024236
      024236
4644
4645 024236 012700 000040
      024236 104447
4646 024244 103416
      024244
4647
4648 024246 012700 000037
      024246 104447
      024252
4649 024254 103556
      024254
4650
4651 024256 012700 000035
      024256 104447
      024262
4652 024264 103555
      024264
4653
4654 024266 012700 000036
      024266 104447
      024272
4655 024274 103161
      024274 100167 000540
4656 024276 000167 000540
4657 024302
4658 024302 104433
      024302
4659
4660
4661
4662 024304 012700 000114
      024304 104462
      024310 010001
      024312 012167 156014
4663 024314 012167 156012
4664 024320 012167 156010
4665 024324 012167 156006
4666 024330 026727 156002 000062
4667 024334 001004
4668 024342

```

```

.SBTTL INITIALIZE SECTION
; **
; *****
; * THIS SECTION CONTAINS THE CODE WHICH IS PERFORMED AT THE BEGINNING OF
; * EACH PASS OR AFTER A CONTINUE COMMAND.
; * THIS CODE PERFORMS THE FOLLOWING ACTIONS:
; *
; * MOVES THE INFORMATION HELD IN THE HARDWARE P-TABLE INTO THE GLOBAL
; * DATA AREA.
; *
; *****
; --
      BGNINIT
;SEE IF PROGRAM JUST STARTED, BR IF YES
      READEF @EF.START
;SEE IF PROGRAM JUST RESTARTED, BR IF YES
      READEF @EF.RESTART
;SEE IF THIS IS A NEW PASS, BR IF YES
      READEF @EF.NEW
;SEE IF PROGRAM WAS JUST CONTINUED
      READEF @EF.CONTINUE
NEWSTA:
      BRESET
; SET UP FOR LINE TIME CLOCK INTERRUPTS.
; --
      CLOCK L,R1
      MOV (R1),CLKCSR
      MOV (R1),CLKBRL
      MOV (R1),CLKVEC
      MOV (R1),CLKHRZ
      CMP CLKHRZ,#50
      BNE 2$

```

```

L$INIT::
      MOV @EF.START,R0
      TRAP C$REFG
      BCS NEWSTA
      MOV @EF.RESTART,R0
      TRAP C$REFG
      BCS NEWRES
      MOV @EF.NEW,R0
      TRAP C$REFG
      BCS NEWPAS
      MOV @EF.CONTINUE,R0
      TRAP C$REFG
      BCC GETPRM
      TRAP C$RESET
      MOV @'L,R0
      TRAP C$CLK
      MOV R0,R1
;STORE CLOCK CSR ADDRESS.
;STORE CLOCK BUS REQ INT LEVEL.
;STORE CLOCK INTERRUPT VECTOR.
;STORE CLOCK FREQUENCY.
;TEST FOR 50HZ LINE FREQUENCY.
;BRANCH IF CLOCK IS NOT 50HZ.

```



```

4669 024344 012767 000024 156002      MOV    #20.,MSTICK      ;INDICATE 20MS PER CLOCK TICK.
4670 024352 000403                BR      4$
4671 024354 012767 000021 155772 2$:  MOV    #17.,MSTICK      ;INDICATE 17 MS PER CLOCK TICK.
4672 024362 012767 000021 155772 4$:  SETVEC CLKVEC,#CLKINT,#PRI06 ;INITIALIZE CLOCK INTERRUPT VECTOR.
                                MOV    #PRI06,-(SP)
                                MOV    #CLKINT,-(SP)
                                MOV    CLKVEC,-(SP)
                                MOV    #3,-(SP)
                                TRAP   C$SVEC
                                ADD    #10,SP
4673 024410 016700 155726      MOV    CLKHRZ,RO        ;INITIALIZE THE BREAK COUNT
4674 024414 006300                ASL    RO                ; TO CAUSE A BREAK
4675 024416 010067 155730      MOV    RO,BCOUNT        ; EVERY 2 SECONDS.
4676 024422 012700 000240      SETPRI #PRI05           ;ALLOW CLOCK INTERRUPTS DISABLE OTHERS.
                                MOV    #PRI05,RO
                                TRAP   C$SPRI
4677                                ;*
4678                                ; ENABLE THE LINE TIME CLOCK (LTC) CHECKING TO MAKE SURE THAT THE CSR
4679                                ; IS ACCESSABLE.
4680                                ; FIRST SET UP TO CATCH ANY 004 TRAPS WHICH OCCUR:
4681                                ;-
4682 024430 016767 153350 155664      MOV    4,TP4VEC         ;SAVE THE EXISTING 004 TRAP VECTOR.
4683 024436 012767 024124 153340      MOV    #TP4RTN,4       ;SET 004 TRAP VECTOR TO OUR SERVICE RTN ADR.
4684                                ;*
4685                                ; ENABLE LTC CHECKING FOR 004 TRAP IN CASE CSR IS NOT THERE.
4686                                ;-
4687 024444 005067 155650                CLR    TP4FLG           ;CLEAR THE 004 TRAP FLAG.
4688 024450 012767 000100 155654      MOV    #BIT6,WORD1     ;SET UP TO SET BIT6 OF THE LTC CSR.
4689 024456 012700 002332                MOV    #WORD1,RO       ;SET UP WORD1 AS THE CKTRAP MOVE SOURCE.
4690 024462 016701 155646                MOV    CLKCSR,R1       ;SET UP LTC CSR AS DESTINATION FOR CKTRAP MOVE.
4691 024466 004767 173260                JSR    PC,CKTRAP        ;MOVE AND CHECK FOR TRAP.
4692 024472 016767 155624 153304      MOV    TP4VEC,4        ;RESTORE THE NORMAL 004 TRAP VECTOR.
4693 024500 103403                BCS    6$              ;IF NO TRAP, LTC IS THERE SO CONTINUE.
4694 024502 005067 155634                CLR    CLKHRZ          ;CLEAR LTC FREQUENCY WORD TO INDICATE NO LTC.
4695 024506 000402                BR     8$              ;BYPASS THE FOLLOWING CALIBRATION PROCEDURES.
4696                                ;*
4697                                ; CALIBRATE THE DELAY ROUTINE MILLI-SECOND DELAY COUNT VALUE.
4698                                ;-
4699 024510 004767 173012      6$:  JSR    PC,CALMSL
4700                                ;*
4701                                ; CHECK FOR MEMORY MANAGEMENT PRESENT ON THIS MACHINE.
4702                                ; IF MEM MGT IS PRESENT, DISABLE IT.
4703                                ;-
4704 024514 016767 153264 155600      8$:  MOV    4,TP4VEC         ;SAVE THE EXISTING 004 TRAP VECTOR.
4705 024522 012767 024124 153254      MOV    #TP4RTN,4       ;SET 004 TRAP VECTOR TO OUR SERVICE RTN ADR.
4706 024530 005067 155564                CLR    TP4FLG          ;CLEAR THE 004 TRAP FLAG.
4707 024534 005067 155572                CLR    WORD1           ;PREPARE TO CLEAR THE MEM MGT SRO REGISTER.
4708 024540 012700 002332                MOV    #WORD1,RO       ;SELECT CLEARED WORD AS CKTRAP RTN SOURCE.
4709 024544 016701 155610                MOV    MMSRO,R1        ;SELECT MEM MGT SRO REGISTER AS DESTINATION.
4710 024550 005067 155606                CLR    MMPRES          ;INDICATE NO MEM MGT PRESENT IN CASE IT ISN'T.
4711 024554 005067 155604                CLR    MMENAB          ;INDICATE MEM MGT IS NOT ENABLED.
4712 024560 004767 173166                JSR    PC,CKTRAP        ;CLEAR THE MEM MGT SRO REG AND CHECK FOR TRAP.
4713 024564 016767 155532 153212      MOV    TP4VEC,4        ;RESTORE THE NORMAL 004 TRAP VECTOR.
4714 024572 103003                BCC    10$            ;SKIP INDICATING MEM MGT PRESENT IF IT ISN'T.
4715 024574 012767 000001 155560      MOV    #1,MMPRES       ;INDICATE THAT MEM MGT IS PRESENT.
4716 024602 005067 155504      10$: CLR    PASCNT         ;CLR COUNTER USED IN REPORTING ROM VERSION #.
4717 024606 000167 000006                JMP    NEWPAS           ;SKIP AROUND THE BUS RESET, IT'S BEEN DONE.

```

```

4718
4719 024612          NEWRES: BRESET          ;RESET THE BUS TO PREVENT ILLEGAL INTERRUPTS.
      024612 104433          ;TRAP C$RESET
4720 024614 005067 155472          CLR      PASCNT          ;CLR COUNTER USED IN REPORTING ROM VERSION #.
4721 024620          NEWPAS:          ;
4722 024620 012767 177777 155410  MOV      # -1,UNITN      ;RESET LOGICAL DEVICE TO -1
4723          ;*
4724          ; INCREMENT THE PASS COUNTER, CORRECT FOR ANY OVERFLOW.
4725          ; THIS COUNTER IS USED IN THE ROM VERSION TEST.
4726          ;-
4727 024626 005267 155460          INC      PASCNT          ;INCREMENT THE PASS COUNTER.
4728 024632 001002          BNE      GETPRM          ;BRANCH IF WE HAVE NOT YET! OVERFLOWED.
4729 024634 005367 155452          DEC      PASCNT          ;SET PASS COUNT TO 177777 OCTAL.
4730
4731          ; GET THE HARDWARE PARAMETERS FOR THIS UNIT.
4732 024640          GETPRM:
4733 024640 005267 155372          INC      UNITN          ;INCREMENT LOGICAL DEVICE NUMBER
4734 024644 026767 155366 155140  CMP      UNITN,L$UNIT    ;SEE IF MAXIMUM UNIT NO. EXCEEDED
4735 024652 002362          BGE      NEWPAS          ;BR IF YES
4736
4737 024654          GPHARD UNITN,R1      ;GET P-TABLE POINTER INTO R1
      024654 016700 155356          ;
      024660 104442          ;
      024662 010001          ;
4738 024664          BCOMPLETE 30$      ;BR IF DEVICE AVAILABLE
      024664 103401          ;
4739 024666 000764          BR      GETPRM          ;SKIP THIS DEVICE
4740
4741
4742          ;***** HARDWARE PARAMETER MOVING CODE *****
4743 024670 012167 155346 30$:  MOV      (R1)+,CSRA      ;STORE DHU-11 CSR ADDRESS IN DEV.REG.ADDRESS TABLE
4744 024674 012102          MOV      (R1)+,R2        ;GET THE RX INTERRUPT VECTOR ADDRESS.
4745 024676 010267 155330          MOV      R2,RXVECA      ;STORE RX INT VECTOR ADDRESS.
4746 024702 062702 000004          ADD      #4,R2          ;CALCULATE TX INTERRUPT VECTOR ADDRESS.
4747 024706 010267 155322          MOV      R2,TXVECA      ;STORE TX INT VECTOR ADDRESS.
4748 024712 012167 155312          MOV      (R1)+,ACTLNS   ;STORE DHU-11 ACTIVE LINE BIT MAP
4749 024716 111167 155316          MOVB    (R1),BRLEVL     ;STORE DHU-11 INTERRUPT BUS REQUEST LEVEL
4750
4751          ;*
4752          ; CALCULATE DEVICE REGISTER ADDRESSES,AND PUT THEM IN THE
4753          ; DEVICE REGISTER ADDRESS TABLE.
4754          ;-
4754 024722 016701 155314          MOV      CSRA,R1        ;COPY CSR ADDRESS
4755 024726 005201          INC      R1             ;INCREMENT CSR ADDRESS
4756 024730 005201          INC      R1             ; COPY BY 2.
4757 024732 012703 000007          MOV      #7,R3         ;SET UP REGISTER COUNT
4758 024736 012702 002244          MOV      #RBUFA,R2     ;GET LOCATION WHERE RBUF ADDRESS GOES IN TABLE
4759 024742 010122          12$:  MOV      R1,(R2)+       ;STORE REGISTER ADDRESS IN TABLE
4760 024744 005201          INC      R1             ;INCREMENT REGISTER ADDRESS
4761 024746 005201          INC      R1             ; BY 2, FOR THE NEXT DEVICE REGISTER.
4762 024750 005303          DEC      R3             ;DECREMENT REGISTER COUNT
4763 024752 001373          BNE      12$           ;LOOP IF NOT DONE
4764
4765          ;*
4766          ; INITIALISE THE BMP CODE QUEUE.
4767          ;-
4768 024754 012700 002526          MOV      #BMPQCB,R0     ;GET THE START ADDRESS OF THE QUEUE.
4769 024760 012701 002726          MOV      #BMPQCE,R1     ;GET THE END ADDRESS OF THE QUEUE.

```



```

INITIALIZE SECTION
4770 024764 010067 155534
4771 024770 005020
4772 024772 020001
4773 024774 103775
4774
4775
4776
4777
4778 024776 032767 000020 155220
4779 025004 001416
4780 025006 026727 155000 000001
4781 025014 003412
4782 025016
      025016 016746 155214
      025022 012746 005433
      025026 012746 000002
      025032 010600
      025034 104417
      025036 062706 000006
4783 025042
4784
4785 025042 005067 155240
4786
4787
4788
4789 025046
      025046 012700 000340
      025052 104441
4790 025054
      025054
      025054 104411
4791
4792 000000

      14$: MOV R0,BMPCQP ;SET THE POINTER TO THE START OF THE QUEUE.
          CLR (R0)+ ;CLEAR OUT THE CONTENTS OF THE QUEUE.
          CMP R0,R1 ;CHECK IF END OF QUEUE HAS BEEN REACHED.
          BLO 14$ ;LOOP IF NOT ALL DONE.
;
; *
; REPORT THE UNIT NUMBER IF THE SOFTWARE P-TABLE QUESTION WAS ANSWERED YES,
; AND THE MAXIMUM UNIT NUMBER IS GREATER THAN 1.
; -
          BIT #BIT4,OPTION ;CHECK IF THE QUESTION WAS ANSWERED YES.
          BEQ 16$ ;SKIP REPORTING UNIT NUMBER IF IT IS DISABLED.
          CMP L$UNIT,#1 ;CHECK MAXIMUM NUMBER OF UNITS SELECTED.
          BLE 16$ ;DO NOT REPORT UNIT NUMBER IF MAX NUMBER < 1.
          PRINTF #MFUNIT,UNITN ;REPORT UNIT NUMBER.
          MOV UNITN,-(SP)
          MOV #MFUNIT,-(SP)
          MOV #2,-(SP)
          MOV SP,R0
          TRAP C$PNTF
          ADD #6,SP
      16$:
ENDIT: CLR CTRLCF ;CLR THE CTRL-C TEST ABORT FLAG.
;
; *
; SET THE PROCESSOR PRIORITY TO DISABLE ALL INTERRUPTS.
; -
          SETPRI #PRI07 ;SET PROCESSOR PRIORITY TO 7.
          MOV #PRI07,R0
          TRAP C$SPRI
L10021: TRAP C$INIT
          TNUM == 0 ;INITIALIZE THE ASSEMBLER TEST NUMBER VARIABLE.

```

4801
4802
4803
4804
4805
4806
4807
4808
4809
4810
4811
4812
4813
4814
4821
4822

025056
025056

025056
025056
025056 104461

.SBTTL AUTODROP SECTION

; THIS CODE IS EXECUTED IMMEDIATELY AFTER THE INITIALIZE CODE IF
; THE "ADR" FLAG WAS SET. THE UNIT(S) UNDER TEST ARE CHECKED TO
; SEE IF THEY WILL RESPOND. THOSE THAT DON'T ARE IMMEDIATELY
; DROPPED FROM TESTING.

BGNAUTO

L\$AUTO::

ENDAUTO

L10022: TRAP C\$AUTO

4831
4832
4833
4834
4835
4836
4837
4838
4839
4840
4841
4850
4851
4852
4853
4854
4855
4867
4868
4869
4870

025060
025060
025060 005767 155222
025064 001401
025066 104433
025070
025070 104432
025072 000002

025074
025074
025074 104412

.SBTTL CLEANUP CODING SECTION

; THE CLEANUP CODING SECTION CONTAINS THE CODING THAT IS PERFORMED
; AFTER THE HARDWARE TESTS HAVE BEEN PERFORMED.

BGNCLN

L\$CLEAN::

TST CTRLCF
BEQ 2\$
BRESET

;DID WE GET HERE BY CTRL-C FROM TEST?
;CTRL-C FROM TEST? NO, SKIP BUS RESET.
;YES, CLR ANY DMAS OR OUTSTANDING INTERRUPTS.
TRAP C\$RESET

2\$:

EXIT CLN

TRAP C\$EXIT
.WORD L10023-

.EVEN

ENDCLN

L10023:

TRAP C\$CLEAN

4879

4880

4881

4882

4883

4884

4885

4886

4887

4888 025076

025076

4897 025076

025076 010046

025100 012746 025122

025104 012746 000002

025110 010600

025112 104417

025114 062706 000006

4898 025120

025120 000427

4899

4900 025122

025122 045 101 040

025125 125 116 111

025130 124 045 104

025133 066 045 101

025136 040 104 122

025141 117 120 120

025144 105 104 040

025147 106 122 117

025152 115 040 106

025155 125 122 124

025160 110 105 122

025163 040 124 105

025166 123 124 111

025171 116 107 056

025174 045 116 000

4901

4902 025200

4903 025200

025200 000167

025202 000000

4904

4905

4906

4907 025204

025204 104453

.SBTTL DROP UNIT SECTION

;++

; THE DROP-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE
; TO NO LONGER BE TESTED.

!--

BGNDU

PRINTF #DROP,RO

L\$DU::
;REPORT UNIT THAT HAS BEEN DROPPED.

MOV RO,-(SP)
MOV #DROP,-(SP)
MOV #2,-(SP)
MOV SP,RO
TRAP C\$PNTF
ADD #6,SP

BR EDROP

;BRANCH AROUND THE MESSAGE.

DROP: .ASCIZ/%A UNIT#D6%A DROPPED FROM FURTHER TESTING.#N/

EDROP: .EVEN

EXIT DU

.WORD J\$JMP
.WORD L10024-2-

ENDDU

L10024:
TRAP C\$DU

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4940
4941

025206
025206

025206 000167
025210 000000

025212
025212
025212 104452

.SBTTL ADD UNIT SECTION

;;
; THE ADD-UNIT SECTION CONTAINS ANY CODE THE PROGRAMMER WISHES
; TO BE EXECUTED IN CONJUNCTION WITH THE ADDING OF A UNIT BACK
; TO THE TEST CYCLE.
;--

BGNAU

L\$AU::

; INSERT ADD CODE HERE. THIS CODE WILL BE EXECUTED AFTER
; AN "ADD" COMMAND. THE PURPOSE OF THIS CODE IS TO DO ANY
; HOUSEKEEPING THAT MAY BE NECESSARY AFTER A UNIT HAS BEEN ADDED.
; THIS SECTION IS OPTIONAL.

EXIT AU

.WORD J\$JMP
.WORD L10025-2-

.EVEN

ENDAU

L10025: TRAP C\$AU

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4951
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4955
4956
4957 025214
      025214
4958      000001
4959 025214 012767 000001 155102
4960 025222 012767 177777 155056
4961 025230 012767 000145 156532
4962 025236 012767 005560 156526
4963 025244 012767 015510 156522
4964
4965
4966
4967 025252 016767 152526 155042
4968 025260 012767 024124 152516
4969 025266 005005
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4975 025270 016700 154746
4976 025274 012701 025466
4977 025300 004767 172446
4978 025304 103402
4979 025306 052705 100001
4980 025312 042767 000017 000146 4$:
4981 025320 010100
4982 025322 016701 154714
4983 025326 004767 172420
4984 025332 103403
4985 025334 052705 100002
4986 025340 000434
4987
4988
4989
4990 025342 012702 000010
4991 025346 016767 154670 000110
4992 025354 016700 000104
4993 025360 012701 025466
4994 025364 004767 172362
4995 025370 103402
4996 025372 052705 100001
4997 025376 010100
4998 025400 016701 000060
    
```

```

.SBTTL  HARDWARE TEST          - ADRA -
; **
; *****
; *                               - REGISTER ADDRESS TEST -
; *
; * THIS TEST VERIFIES THAT THE DEVICE REGISTERS WILL RESPOND TO THE PROPER
; * UNIBUS HANDSHAKING SIGNALS WHEN ACCESSED. IF THE DHU11 DOES NOT RESPOND
; * TO THE ACCESS ATTEMPTS (IF THE DHU11 IS AT THE WRONG ADDRESS, FOR EXAMPLE)
; * THE 004 BUS TIME-OUT TRAP IS DETECTED BY THIS ROUTINE AND AN ERROR
; * IS REPORTED. THIS TEST IS PERFORMED ON LINE 0 ONLY.
; *
; *****
; --

      BGNTST

      T1::
      TNUM == TNUM + 1          ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
      MOV     #TNUM,TSTNUM      ;SET UP THE TEST NUMBER.          (1)
      MOV     #-1,CTRLCF        ;INDICATE THAT WE ARE IN A TEST.
      MOV     #101,ERRNBR       ;SET THE TEST ERROR NUMBER IN THE TABLE.
      MOV     #EM0103,ERRMSG    ;SET UP THE TEST FAILURE MESSAGE IN THE TABLE.
      MOV     #ER0101,ERRBLK    ;SET-UP THE ERROR ROUTINE IN THE ERROR TABLE.

; *
; * SET UP TO CATCH ANY 004 TRAPS WHICH OCCUR:
; *
      MOV     4,TP4VEC          ;SAVE THE EXISTING 004 TRAP VECTOR.
      MOV     #TP4RTN,4        ;SET 004 TRAP VECTOR TO OUR SERVICE RTN ADR.
      CLR     R5                ;CLEAR THE ERROR FLAGS.

; *
; * HERE BEGINS THE LOOP TO TEST THE REGISTERS FOR A LINE.
; * FIRST TEST THE CSR AND SET THE IND.ADR.REG (I.A.R) FIELD.
; *
      MOV     CSRA,R0           ;SET UP CSR AS THE CKTRAP MOVE SOURCE.
      MOV     #52$,R1           ;SET UP DESTINATION LOCATION FOR CKTRAP MOVE.
      JSR     PC,CKTRAP         ;MOVE AND CHECK FOR TRAP.
      BCS     4$                ;IF NO TRAP, BYPASS ERROR.
      BIS     #100001,R5        ;SET FATAL READ ERROR FLAGS.
      BIC     #17,52$          ;CLEAR THE I.A.R FIELD OF THE CSR DATA.
      MOV     R1,R0             ;USE OLD DESTINATION FOR SOURCE OF CKTRAP MOVE.
      MOV     CSRA,R1           ;SET UP CSR AS THE CKTRAP MOVE DESTINATION.
      JSR     PC,CKTRAP         ;MOVE AND CHECK FOR TRAP.
      BCS     6$                ;IF NO TRAP, BYPASS ERROR.
      BIS     #100002,R5        ;SET FATAL WRITE ERROR FLAGS.
      BR      40$               ;EXIT AND REPORT FATAL ERROR.

; *
; * NOW, WE TEST EACH REGISTER FOR THIS LINE.
; *
      MOV     #8$,R2            ;INIT REGISTER COUNTER TO 8.
      MOV     CSRA,50$          ;INITIALIZE THE REGISTER POINTER.
      MOV     50$,R0            ;SET UP REGISTER AS THE SOURCE FOR CKTRAP MOVE.
      MOV     #52$,R1           ;SET UP LOCAL STORAGE AS THE DES FOR CKTRAP.
      JSR     PC,CKTRAP         ;PERFORM THE MOVE, CHECK FOR TRAP.
      BCS     10$              ;IF NO TRAP, BYPASS THE SETTING OF ERROR FLAGS.
      BIS     #100001,R5        ;SET FATAL READ ERROR FLAGS.
      MOV     R1,R0             ;USE OLD DEST AS SRC FOR CKTRAP MOVE.
      MOV     50$,R1           ;SET UP REGISTER AS THE DEST FOR CKTRAP MOVE.
    
```



```

4999 025404 004767 172342      JSR    PC,CKTRAP      ;PERFORM THE MOVE, CHECK FOR TRAP.
5000 025410 103402              BCS    12$            ;IF NO TRAP, BYPASS THE SETTING OF ERROR FLAGS.
5001 025412 052705 100002      BIS    #100002,R5    ;SET FATAL WRITE ERROR FLAGS.
5002 025416 005267 000042      12$:  INC    50$      ;INCREMENT THE REGISTER
5003 025422 005267 000036      INC    50$          ; POINTER BY 2.
5004 025426 005302              DEC    R2            ;COUNT THE REGISTER.
5005 025430 001351              BNE    8$            ;LOOP TO TEST THE NEXT REGISTER ADDRESS.
5006
5007
5008                          ;+
5009                          ; DONE CHECKING DEVICE REGISTER ADDRESSES.
5010                          ; REPORT ANY ERRORS AND EXIT.
5011                          ;-
5012 025432 016767 154664 152344 40$:  MOV    TP4VEC,4      ;RESTORE THE NORMAL 004 TRAP VECTOR.
5013 025440 005705              TST    R5            ;CHECK THE ERROR FLAGS.
5014 025442 100012              BPL    60$          ;EXIT ROUTINE IF NO ERRORS.
5015
5016                          ;+
5017                          ; REPORT "DEVICE REGISTER ACCESS TEST FAILED"
5018 025444              ;-
5018 025444 104460              ERROR
5019
5020
5021 025446              DODU    UNITN          ;DROP THIS UNIT FROM FUTHER TESTING.
5021 025446 016700 154564              MOV    UNITN,RO
5021 025452 104451              TRAP  C$DODU
5022 025454 005067 154626              CLR    CTRLCF       ;INDICATE NO CTRL-C ABORT FROM TEST.
5023 025460              DOCLN          ;ABORT THIS SUB PASS.
5023 025460 104444              TRAP  C$DCLN
5024 025462 000402              BR     60$          ;
5025
5026                          ;***** LOCAL STORAGE. *****
5027 025464 000000      50$:  .WORD 0          ;STORAGE FOR THE SOURCE OR DEST OF THE CKTRAP MOVE.
5028 025466 000000      52$:  .WORD 0          ;STORAGE FOR THE SOURCE OR DEST OF THE CKTRAP MOVE.
5029
5030                          ;***** END *****
5031 025470 005067 154612      60$:  CLR    CTRLCF       ;INDICATE THAT WE ARE NOT WITHIN A TEST.
5032 025474              ENDTST
5032 025474              L10026:
5032 025474 104401              TRAP  C$ETST
    
```

```

5034 .SBTTL  HARDWARE TEST          - MRSTA -
5035 :* *****
5036 :* - MASTER RESET WITH SELFTEST TEST -
5037 :* THIS TEST VERIFIES THAT THE MASTER RESET BIT WILL CLEAR AFTER A DEVICE
5038 :* RESET AND THE PERFORMANCE OF THE DUT ROM BASED SELFTEST.
5039 :* *****
5040 :*
5041 025476      BGNTST
5042 025476      000002
5043 025476 012767 000002 154620      TNUM == TNUM + 1      ; INCREMENT THE ASSEMBLY TIME TEST COUNTER.
5044 025504 012767 177777 154574      MOV @TNUM,TSTNUM      ; SET UP THE TEST NUMBER. (2)
5045 025512      025512 012700 000240      MOV @-1,CTRLCF      ; INDICATE THAT WE ARE IN A TEST.
5046 025520      025516 104441      SETPRI @PRI05      ; ALLOW LTC INTERRUPTS.
5047 025526 012767 000001 156240      MOV @PRI05,R0
5048 025534 012767 005616 156236      TRAP C$SPRI
5049 025534 012767 016042 156232      MOV @1,ERRTYP      ; SET ERROR TYPE AS FATAL IN ERROR TABLE.
5050 :*
5051 :* WAIT UP TO 5 SECONDS FOR THE DUT MASTER RESET BIT TO CLEAR.
5052 025542 012701 011610      MOV @5000.,R1      ; TIME-OUT VALUE IS 5.0 SECONDS.
5053 025546 012702 000040      MOV @BIT05,R2      ; WAITING FOR MASTER RESET BIT.
5054 025552 005003      CLR R3      ; WAITING FOR BIT TO CLEAR.
5055 025554 016704 154462      MOV CSRA,R4      ; BIT IS IN THE DUT'S CSR.
5056 025560 004767 172400      JSR PC,MSLGET      ; WAIT FOR DUT_CSR_MR BIT TO CLEAR.
5057 025564 103410      BCS 2$      ; SKIP TO RESET DUT IF MR CLEAR.
5058 :*
5059 :* DUT MASTER RESET BIT DID NOT GO CLEAR. DEVICE MAY BE STUCK IN SOME
5060 :* ODD STATE. TRY TO RESET DEVICE WITH A BUS RESET.
5061 :*
5062 025566      BRESET      ; NO. TRY TO JOG DEVICE WITH BUS RESET.
5063 025566 104433      TRAP C$RESET
5064 025570 004767 174550      JSR PC,SKPSTS      ; TRY TO SKIP THE SELFTEST.
5065 025574 012701 011610      MOV @5000.,R1      ; TIME-OUT VALUE IS 5.0 SECONDS.
5066 025604 004767 172360      JSR PC,MSLGET      ; WAIT FOR DUT_CSR_MR BIT TO CLEAR.
5067 :*
5068 :* SET THE MASTER RESET BIT AND VERIFY THAT IT CLEARS WITHIN THE PROPER TIME.
5069 :*
5070 025606 012701 011610      2$: MOV @5000.,R1      ; TIME-OUT VALUE IS 5.0 SECONDS.
5071 025612 010214      MOV R2,(R4)      ; SET THE DUT MASTER RESET BIT.
5072 025614 004767 172344      JSR PC,MSLGET      ; WAIT FOR DUT_CSR_MR BIT TO CLEAR.
5073 025620 103010      BCC 4$      ; GO REPORT ERROR IF MR BIT DID NOT CLEAR.
5074 025622 012702 011610      MOV @5000.,R2
5075 025626 160102      SUB R1,R2      ; CALCULATE # OF MS FOR MR TO CLEAR.
5076 025630 001413      BEQ 6$      ; GO REPORT ERROR IF MR CLEAR IMMEDIATELY.
5077 025632 020227 000764      CMP R2,@500.
5078 025636 002417      BLT 8$      ; GO REPORT ERROR IF MR CLEAR IN < 1/2 SECOND.
5079 025640 000424      BR 60$      ; EXIT THE TEST WITHOUT ERROR.
5080 :*
5081 :* ERROR REPORTS:
5082 :*
5083 :* REPORT MR BIT WOULD NOT CLEAR AFTER A DUT RESET.
5084 025642 012767 000311 156120 4$: MOV @201.,ERRNBR      ; SET THE ERROR NUMBER IN ERROR TABLE.
5085 025650 012701 005651      MOV @EMO202,R1      ; SELECT ERROR MESSAGE.
5086 025654      ERROR      ; REPORT ERROR. >>>> ERROR #201 <<<<
    
```



```

5087 025654 104460
5088 025656 000415          BR      60$          ;EXIT THE TEST.          TRAP      C$ERROR
5089
5090 025660 012767 000312 156102 6$: ;REPORT MR BIT CLEAR IMMEDIATELY AFTER DUT RESET.
5091 025666 012701 006024          MOV     #202.,ERRNBR ;SET THE ERROR NUMBER IN ERROR TABLE.
5092 025672          MOV     #EM0203,R1 ;SELECT ERROR MESSAGE.
5093 025672 104460          ERROR          ;REPORT ERROR.          >>>>> ERROR #202 <<<<<
5094 025674 000406          BR      60$          ;EXIT THE TEST.          TRAP      C$ERROR
5095
5096 025676 012767 000313 156064 8$: ;REPORT MR CLEAR WITHIN 1/2 SECOND OF DUT RESET.
5097 025704 012701 006167          MOV     #203.,ERRNBR ;SET THE ERROR NUMBER IN ERROR TABLE.
5098 025710          MOV     #EM0204,R1 ;SELECT ERROR MESSAGE.
5099 025710 104460          ERROR          ;REPORT ERROR.          >>>>> ERROR #203 <<<<<
5100 025712          60$: SETPRI #PRI07          ;DISABLE ALL INTERRUPTS.          TRAP      C$ERROR
5101 025712 012700 000340          MOV     #PRI07,R0
5102 025716 104441          TRAP
5101 025720 005067 154362          CLR     CTRLCF          ;INDICATE THAT WE COMPLETED THE TEST.
5102 025724          ENDTST
5102 025724 104401          L10027:
5102 025724          TRAP      C$ETST

```

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5104 .SBTTL  HARDWARE TEST          - MRSSTA -
5105 :* *****
5106 :* - MASTER RESET WITH SKIP SELFTEST TEST -
5107 :* THIS TEST VERIFIES THAT THE MASTER RESET BIT WILL CLEAR AFTER A DEVICE
5108 :* RESET AND THE SKIPPING OF THE DUT ROM BASED SELFTEST.
5109 :*
5110 :* *****
5111 :* BGNTST
5112 :*
5113 :* TNUM == TNUM + 1          ; INCREMENT THE ASSEMBLY TIME TEST COUNTER.
5114 :* MOV @TNUM,TSTNUM          ; SET UP THE TEST NUMBER. (3)
5115 :* MOV @-1,CTRLCF           ; INDICATE THAT WE ARE IN A TEST.
5116 :* SETPRI @PRIOS           ; ALLOW LTC INTERRUPTS.
5117 :*
5118 :* MOV @PRIOS,R0            ; MOV @PRIOS,R0
5119 :* TRAP C$SPRI             ; TRAP C$SPRI
5120 :*
5121 :*
5122 :* MOV @1,ERRTP            ; SET ERROR TYPE AS FATAL IN ERROR TABLE.
5123 :* MOV @EM0301,ERRMSG      ; SET ERROR MESSAGE ADDRESS IN ERROR TABLE.
5124 :* MOV @ER0201,ERRBLK     ; SET ERROR ROUTINE ADDRESS IN ERROR TABLE.
5125 :*
5126 :*
5127 :*
5128 :*
5129 :* WAIT UP TO 5 SECONDS FOR THE DUT MASTER RESET BIT TO CLEAR.
5130 :*
5131 :*
5132 :* MOV @5000.,R1           ; TIME-OUT VALUE IS 5.0 SECONDS.
5133 :* MOV @BIT05,R2          ; WAITING FOR MASTER RESET BIT.
5134 :* CLR R3                 ; WAITING FOR BIT TO CLEAR.
5135 :* MOV CSRA,R4            ; BIT IS IN THE DUT'S CSR.
5136 :* JSR PC,MSLGET         ; WAIT FOR DUT_CSR_MR BIT TO CLEAR.
5137 :* BCS 2$                ; SKIP TO RESET DUT IF MR CLEAR.
5138 :*
5139 :* DUT MASTER RESET BIT DID NOT GO CLEAR. DEVICE MAY BE STUCK IN SOME
5140 :* ODD STATE. TRY TO RESET DEVICE WITH A BUS RESET.
5141 :*
5142 :* BRESET                 ; NO. TRY TO JOG DEVICE WITH BUS RESET.
5143 :* JSR PC,SKPSTS          ; TRY TO SKIP THE SELFTEST.
5144 :* MOV @5000.,R1          ; TIME-OUT VALUE IS 5.0 SECONDS.
5145 :* JSR PC,MSLGET         ; WAIT FOR DUT_CSR_MR BIT TO CLEAR.
5146 :* BCC 6$                ; GO REPORT ERROR IF MR BIT DID NOT CLEAR.
5147 :*
5148 :* SET THE MASTER RESET BIT, TRY TO SKIP THE SELFTEST, AND VERIFY THAT THE
5149 :* MR BIT CLEARS WITHIN 1/5 SECOND.
5150 :*
5151 :*
5152 :* 2$: MOV @200.,R1         ; TIME-OUT VALUE IS 1/5 SECOND.
5153 :* MOV R2,(R4)            ; SET THE DUT MASTER RESET BIT.
5154 :* JSR PC,SKPSTS          ; TRY TO SKIP THE SELFTEST.
5155 :* JSR PC,MSLGET         ; WAIT FOR DUT_CSR_MR BIT TO CLEAR.
5156 :* BCC 4$                ; GO FIND OUT WHAT IS WRONG IF MR NOT CLEAR.
5157 :* MOV @200.,R2          ; CALCULATE # OF MS FOR MR TO CLEAR.
5158 :* SUB R1,R2
5159 :* CMP R2,@10.
5160 :* BLT 8$                ; GO REPORT ERROR IF MR CLEAR IN < 10 MS.
5161 :* BR 60$                ; EXIT THE TEST WITHOUT ERROR.
5162 :*
5163 :* MR DID NOT CLEAR WITHIN 1/5 SECOND, SEE IF IT CLEARS WITHIN 5 SECONDS.
5164 :*
5165 :* 4$: MOV @4800.,R1       ; TIME-OUT VALUE IS 5 SECONDS MINUS 1/5 SECOND.
5166 :* JSR PC,MSLGET         ; WAIT FOR DUT_CSR_MR BIT TO CLEAR.
5167 :* BCS 10$               ; GO REPORT ERROR IF MR CLEARED FINALLY.

```



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5157
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5161 026106 012767 000455 155654 6$: ;REPORT MR BIT WOULD NOT CLEAR AFTER A DUT RESET.
5162 026114 012701 005651      MOV #0301.,ERRNBR ;SET THE ERROR NUMBER IN ERROR TABLE.
5163 026120      MOV #EM0202,R1 ;SELECT ERROR MESSAGE.
      ERROR ;REPORT ERROR. >>>> ERROR #0301 <<<<<
5164 026120 104460      TRAP C$ERROR
5164 026122 000415      BR 60$ ;EXIT THE TEST.
5165
5166
5167 026124 012767 000456 155636 8$: ;REPORT MR BIT CLEAR WITHIN 10 MS AFTER DUT RESET.
5168 026132 012701 006420      MOV #0302.,ERRNBR ;SET THE ERROR NUMBER IN ERROR TABLE.
5169 026136      MOV #EM0302,R1 ;SELECT ERROR MESSAGE.
      ERROR ;REPORT ERROR. >>>> ERROR #0302 <<<<<
5170 026140 104460      TRAP C$ERROR
      BR 60$ ;EXIT THE TEST.
5171
5172
5173 026142 012767 000457 155620 10$: ;REPORT MR CLEARED BETWEEN 1/5 SECOND AND 5 SECONDS OF DUT RESET.
5174 026150 012701 006560      MOV #0303.,ERRNBR ;SET THE ERROR NUMBER IN ERROR TABLE.
5175 026154      MOV #EM0303,R1 ;SELECT ERROR MESSAGE.
      ERROR ;REPORT ERROR. >>>> ERROR #0303 <<<<<
5176 026154 104460      TRAP C$ERROR
5177 026156      SETPRI #PRI07 ;DISABLE ALL INTERRUPTS.
      MOV #PRI07,RO
5177 026156 012700 000340      TRAP C$SPRI
      MOV 104441
5178 026164 005067 154116      CLR CTRLCF ;INDICATE THAT WE COMPLETED THE TEST.
5179 026170      ENDTST
      MOV 026170
      TRAP C$ETST
      MOV 104401
      L10030:

```

5181
5182
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5184
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5186
5187
5188
5189 026172
026172
5190 026172
026172 012700 000240
026176 104441
5191 000004
5192 026200 012767 000004 154116
5193 026206 012767 177777 154072
5194 026214 012767 000001 155544
5195 026222 012767 006737 155542
5196 026230 012767 016042 155536
5197
5198
5199
5200
5201 026236 012701 011610
5202 026242 012702 000040
5203 026246 005003
5204 026250 016704 153766
5205 026254 010214
5206 026256 004767 174062
5207 026262 004767 171676
5208 026266 103015
5209
5210
5211
5212
5213 026270 012400
5214 026272 012701 000006
5215 026276 011402
5216 026300 010200
5217 026302 042700 177476
5218 026306 020027 000201
5219 026312 001012
5220 026314 005301
5221 026316 001367
5222 026320 000415
5223
5224
5225
5226
5227
5228 026322 012767 000621 155440
5229 026330 012701 005651
5230 026334
026334 104460
5231 026336 000406
5232
5233

```

.SBTTL HARDWARE TEST - RXCHRA -
; * *****
; * - RBUF REGISTER RX CHARACTER FIELD TEST -
; * THIS TEST VERIFIES THAT THE RX CHARACTER FIELD OF THE DUT RBUF REGISTER
; * APPEARS TO BE FUNCTIONING CORRECTLY. THIS TEST USES THE CODES WHICH
; * SHOULD BE IN THE FIFO AFTER A BOARD RESET AND SKIP SELFTEST SEQUENCE.
; *
; * *****
; * -
BGNTST
; *
; * SET THE DUT CSR MASTER RESET (MR) BIT, PERFORM THE SKIP SELFTEST SEQUENCE,
; * AND WAIT UP TO 5 SECONDS FOR THE MR BIT TO CLEAR.
; *
; *
; * MOV #5000.,R1 ; TIME-OUT VALUE IS 5.0 SECONDS.
; * MOV #BIT05,R2 ; WAITING FOR MASTER RESET BIT.
; * CLR R3 ; WAITING FOR BIT TO CLEAR.
; * MOV CSRA,R4 ; BIT IS IN THE DUT'S CSR.
; * MOV R2,(R4) ; SET THE DUT MASTER RESET BIT.
; * JSR PC,SKPSTS ; SKIP THE SELFTEST.
; * JSR PC,MSLGET ; WAIT FOR DUT_CSR_MR BIT TO CLEAR.
; * BCC 4$ ; GO REPORT ERROR IF MR DID NOT CLEAR.
; *
; * READ 6 CHARACTERS FROM THE DUT AND VERIFY THAT THEY ARE VALID SELFTEST
; * CODES.
; *
; * MOV (R4),R0 ; INCREMENT POINTER TO POINT TO DUT RBUF REGSTR.
; * MOV #6,R1 ; INITIALIZE THE LOOP COUNTER.
2$: MOV (R4),R2 ; READ A CHARACTER FROM THE DUT RBUF REGISTER.
; * MOV R2,R0
; * BIC #177476,R0 ; REMOVE ALL BUT BITS SPECIFIC TO SELFTEST CODE.
; * CMP R0,#201 ; CHECK THAT BITS 0,6, AND 7 ARE CORRECT.
; * BNE 5$ ; GO REPORT ERROR IF CODE IS NOT SELFTEST CODE.
; * DEC R1 ; COUNT THIS LOOP ITERATION.
; * BNE 2$ ; LOOP IF NOT ALL LINES DONE.
; * BR 60$ ; EXIT TEST, NO ERROR FOUND.
; *
; * ERROR REPORTS:
; *
; * REPORT MR BIT WOULD NOT CLEAR AFTER A DUT RESET.
4$: MOV #0401.,ERRNBR ; SET THE ERROR NUMBER IN ERROR TABLE.
; * MOV #EMO202,R1 ; SELECT ERROR MESSAGE.
; * ERROR ; REPORT ERROR. >>>> ERROR #0401 <<<<
; * TRAP C$ERROR
; * BR 60$ ; EXIT THE TEST.
; *
; * REPORT IMPROPER CODE FOUND IN DUT RBUF AFTER RESET (SKIP SELFTEST).

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5242 .SBTTL HARDWARE TEST - RXFFDA -
5243 ;** *****
5244 ;* - RBUF REGISTER RX FLAG FIELD TEST -
5245 ;* THIS TEST VERIFIES THAT THE FIELD OF 3 FLAG BITS IN THE RBUF READS
5246 ;* AS ALL ONES WHEN THE SELFTEST CODES ARE BEING READ FROM THE DUT
5247 ;* AFTER A BOARD RESET AND SKIP SELFTEST SEQUENCE.
5248 ;*
5249 ;** *****
5250 026370 BGNTST
5251 026370 SETPRI #PRI05 ;ALLOW LTC INTERRUPTS. T5::
026370 012700 000240
026374 104441
5252 000005 MOV #PRI05,R0
5253 026376 012767 000005 153720 TRAP C$SPRI
5254 026404 012767 177777 153674
5255 026412 012767 000001 155346 TNUM == TNUM + 1 ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
5256 026420 012767 007165 155344 MOV #TNUM,TSTNUM ;SET UP THE TEST NUMBER. (5)
5257 026426 012767 016042 155340 MOV #-1,CTRLCF ;INDICATE THAT WE ARE WITHIN A TEST.
5258 MOV #1,ERRTYP ;SET ERROR TYPE AS FATAL IN ERROR TABLE.
5259 MOV #EM0501,ERRMSG ;SET ERROR MESSAGE ADDRESS IN ERROR TABLE.
5260 MOV #ER0201,ERRBLK ;SET ERROR ROUTINE ADDRESS IN ERROR TABLE.
5261 ;*
5262 ; SET THE DUT CSR MASTER RESET (MR) BIT, PERFORM THE SKIP SELFTEST SEQUENCE,
5263 ; AND WAIT UP TO 5 SECONDS FOR THE MR BIT TO CLEAR.
5264 ;-
5262 026434 012701 011610 MOV #5000.,R1 ;TIME-OUT VALUE IS 5.0 SECONDS.
5263 026440 012702 000040 MOV #BIT05,R2 ;WAITING FOR MASTER RESET BIT.
5264 026444 005003 CLR R3 ;WAITING FOR BIT TO CLEAR.
5265 026446 016704 153570 MOV CSRA,R4 ;BIT IS IN THE DUT'S CSR.
5266 026452 010214 MOV R2,(R4) ;SET THE DUT MASTER RESET BIT.
5267 026454 004767 173664 JSR PC,SKPSTS ;SKIP THE SELFTEST.
5268 026460 004767 171500 JSR PC,MSLGET ;WAIT FOR DUT_CSR_MR BIT TO CLEAR.
5269 026464 103013 BCC 4$ ;GO REPORT ERROR IF MR DID NOT CLEAR.
5270 ;*
5271 ; READ 8 CHARACTERS FROM THE DUT AND VERIFY THAT ALL 3 RX ERROR FLAGS ARE
5272 ; SET FOR EACH CHARACTERS.
5273 ;-
5274 026466 012400 MOV (R4),R0 ;INCREMENT POINTER TO POINT TO DUT RBUF REGSTR.
5275 026470 012701 000010 MOV #8.,R1 ;INITIALIZE THE LOOP COUNTER.
5276 026474 011402 2$: MOV (R4),R2 ;READ A CHARACTER FROM THE DUT RBUF REGISTER.
5277 026476 012700 070000 MOV #70000,R0
5278 026502 040200 BIC R2,R0 ;CALCULATE BIT MAP OF CLEAR RX ERROR FLAGS.
5279 026504 001012 BNE 6$ ;GO REPORT ERROR IF NOT ALL RX ERROR FLAGS SET.
5280 026506 005301 DEC R1 ;COUNT THIS LOOP ITERATION.
5281 026510 001371 BNE 2$ ;LOOP IF NOT ALL LINES DONE.
5282 026512 000415 BR 60$ ;EXIT TEST, NO ERROR FOUND.
5283
5284 ;*
5285 ; ERROR REPORTS:
5286 ;-
5287 ;REPORT MR BIT WOULD NOT CLEAR AFTER A DUT RESET.
5288 026514 012767 000765 155246 4$: MOV #0501.,ERRNBR ;SET THE ERROR NUMBER IN ERROR TABLE.
5289 026522 012701 005651 MOV #EM0202,R1 ;SELECT ERROR MESSAGE.
5290 026526 104460 ERROR ;REPORT ERROR. >>>> ERROR #0501 <<<<<
026526 000406 TRAP C$ERROR
5291 026530 000406 BR 60$ ;EXIT THE TEST.
5292
5293 ;REPORT ONE OR MORE RX ERROR FLAGS FOUND CLEAR WITH SELFTEST CODE.
5294 026532 012767 000766 155230 6$: MOV #0502.,ERRNBR ;SET THE ERROR NUMBER IN ERROR TABLE.

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5302 .SBTTL HARDWARE TEST - RDAA -
5303 ;* *****
5304 ;* - CSR RX DATA AVAILABLE BIT TEST -
5305 ;* THIS TEST VERIFIES THAT THE DUT CSR RX DATA AVAILABLE BIT IS SET BY THE
5306 ;* INCLUSION OF THE SELFTEST CODES IN THE DUT FIFO AND THAT THE BIT CLEARS
5307 ;* AFTER THE FIFO HAS BEEN EMPTIED.
5308 ;*
5309 ;* *****
5310 ;-- *****
5310 026562 BGNTST
5311 026562 SETPRI #PRI05 ;ALLOW LTC INTERRUPTS. T6::
5311 026562 012700 000240 ;
5311 026566 104441 ;
5312 000006 ;
5312 026570 012767 000006 153526 TNUM == TNUM + 1 ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
5313 026570 012767 000006 153526 MOV #TNUM,TSTNUM ;SET UP THE TEST NUMBER. (6)
5314 026576 012767 177777 153502 MOV #-1,CTRLCF ;INDICATE THAT WE ARE WITHIN A TEST.
5315 026604 012767 000001 155154 MOV #1,ERRTYP ;SET ERROR TYPE AS FATAL IN ERROR TABLE.
5316 026612 012767 007575 155152 MOV #EM0601,ERRMSG ;SET ERROR MESSAGE ADDRESS IN ERROR TABLE.
5317 026620 012767 016042 155146 MOV #ERO201,ERRBLK ;SET ERROR ROUTINE ADDRESS IN ERROR TABLE.
5318 ;
5319 ;*
5320 ; SET THE DUT CSR MASTER RESET (MR) BIT, PERFORM THE SKIP SELFTEST SEQUENCE,
5321 ; AND WAIT UP TO 5 SECONDS FOR THE MR BIT TO CLEAR.
5321 ;--
5322 026626 012701 011610 MOV #5000.,R1 ;TIME-OUT VALUE IS 5.0 SECONDS.
5323 026632 012702 000040 MOV #BIT05,R2 ;WAITING FOR MASTER RESET BIT.
5324 026636 005003 CLR R3 ;WAITING FOR BIT TO CLEAR.
5325 026640 016704 153376 MOV CSRA,R4 ;BIT IS IN THE DUT'S CSR.
5326 026644 010214 MOV R2,(R4) ;SET THE DUT MASTER RESET BIT.
5327 026646 004767 173472 JSR PC,SKPSTS ;SKIP THE SELFTEST.
5328 026652 004767 171306 JSR PC,MSLGET ;WAIT FOR DUT_CSR_MR BIT TO CLEAR.
5329 026656 103016 BCC 4$ ;GO REPORT ERROR IF MR DID NOT CLEAR.
5330 ;
5331 ;*
5332 ; CHECK THAT THE RX DATA AVAILABLE BIT IS SET.
5332 ;--
5333 026660 032714 000200 BIT #BIT7,(R4) ;TEST THE DUT RX.DATA.AVAIL BIT.
5334 026664 001422 BEQ 6$ ;GO REPORT ERROR IF BIT IS NOT SET.
5335 ;
5336 ;*
5337 ; READ CHARACTERS FROM THE DUT RX FIFO AND WAIT FOR RX.DATA.AVAIL TO GO CLEAR.
5337 ;--
5338 026666 012705 001130 MOV #600.,R5 ;ALLOW READING 600 CHARS BEFORE ERROR.
5339 026672 010403 MOV R4,R3
5340 026674 012300 MOV (R3)+,R0 ;CALCULATE THE RBUF ADDRESS.
5341 026676 011300 MOV (R3),R0 ;READ A CHARACTER FROM THE RX FIFO.
5342 026700 032714 000200 BIT #BIT7,(R4) ;TEST THE DUT RX.DATA.AVAIL BIT.
5343 026704 001427 BEQ 60$ ;EXIT TEST WITHOUT ERROR IF RX.DATA.AVAIL CLR.
5344 026706 005305 DEC R5 ;COUNT THE CHARACTER JUST READ.
5345 026710 001372 BNE 2$ ;LOOP IF NOT TOO MANY CHARS READ FROM FIFO.
5346 026712 000416 BR 8$ ;GO REPORT ERROR IF RX.DATA.AVAIL WOULDN'T CLR.
5347 ;
5348 ;*
5349 ; ERROR REPORTS:
5349 ;--
5350 ;
5351 ;*
5351 ; REPORT MR BIT WOULD NOT CLEAR AFTER A DUT RESET.
5352 026714 012767 001131 155046 4$: MOV #0601.,ERRNBR ;SET THE ERROR NUMBER IN ERROR TABLE.
5353 026722 012701 005651 MOV #EM0202,R1 ;SELECT ERROR MESSAGE.
5354 026726 104460 ERROR ;REPORT ERROR. >>>> ERROR #0601 <<<<<
; TRAP C$ERROR

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5355 026730 000415          BR      60$          ;EXIT THE TEST.
5356
5357
5358 026732 012767 001132 155030 6$: ;REPORT THAT RX.DATA.AVAIL BIT WAS NOT SET AFTER A RESET COMPLETION.
5359 026740 012701 007637          MOV      #0602.,ERRNBR ;SET THE ERROR NUMBER IN ERROR TABLE.
5360 026744          MOV      #EM0602,R1 ;SELECT ERROR MESSAGE.
      026744 104460          ERROR ;REPORT ERROR.          >>>> ERROR #0602 <<<<<
5361 026746 000406          BR      60$          ;EXIT THE TEST.
      026746          TRAP      C$ERROR
5362
5363
5364 026750 012767 001133 155012 8$: ;REPORT THAT RX.DATA.AVAIL BIT COULD NOT BE CLEARED BY PURGING FIFO.
5365 026756 012701 010017          MOV      #0603.,ERRNBR ;SET THE ERROR NUMBER IN ERROR TABLE.
5366 026762          MOV      #EM0603,R1 ;SELECT ERROR MESSAGE.
      026762 104460          ERROR ;REPORT ERROR.          >>>> ERROR #0603 <<<<<
5367          TRAP      C$ERROR
5368 026764          60$:  SETPRI #PRI07          ;DISABLE ALL INTERRUPTS.
      026764 012700 000340
      026770 104441
5369 026772 005067 153310          CLR      CTRLCF          ;INDICATE THAT WE COMPLETED THE TEST.
5370 026776          ENDTST
      026776 104401          L10033:
          TRAP      C$ETST

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5372 .SBTTL  HARDWARE TEST          - RDVA -
5373 ;** *****
5374 ;*          - RBUF RX DATA VALID BIT TEST -
5375 ;*          THIS TEST VERIFIES THAT THE DUT RBUF RX DATA VALID BIT IS SET BY THE
5376 ;*          INCLUSION OF THE SELFTST CODES IN THE DUT FIFO AND THAT THE BIT CLEARS
5377 ;*          AFTER THE FIFO HAS BEEN EMPTIED.
5378 ;*
5379 ;-- *****
5380 027000          BGNTST
5381 027000          SETPRI  #PRI05          ;ALLOW LTC INTERRUPTS.          T7::
5382 027000          012700  000240          ;
5383 027004          104441          ;
5384 027006          000007          ;
5385 027006          TNUM == TNUM + 1          ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
5386 027006          MOV  #TNUM,TSTNUM          ;SET UP THE TEST NUMBER.          (7)
5387 027014          MOV  #-1,CTRLCF          ;INDICATE THAT WE ARE WITHIN A TEST.
5388 027022          MOV  #1,ERRTYP          ;SET ERROR TYPE AS FATAL IN ERROR TABLE.
5389 027030          MOV  #EM0701,ERRMSG          ;SET ERROR MESSAGE ADDRESS IN ERROR TABLE.
5390 027036          MOV  #ER0201,ERRBLK          ;SET ERROR ROUTINE ADDRESS IN ERROR TABLE.
5391 ;
5392 ; SET THE DUT CSR MASTER RESET (MR) BIT, PERFORM THE SKIP SELFTST SEQUENCE,
5393 ; AND WAIT UP TO 5 SECONDS FOR THE MR BIT TO CLEAR.
5394 ;
5395 027044          012701  011610          ;
5396 027050          012702  000040          ;
5397 027054          005003          ;
5398 027056          016704  153160          ;
5399 027062          010214          ;
5400 027064          004767  173254          ;
5401 027070          004767  171070          ;
5402 027074          103012          ;
5403 ;
5404 ; CHECK THAT THE RX DATA VALID BIT IS SET.
5405 ;
5406 027076          012400          ;
5407 027100          005714          ;
5408 027102          100016          ;
5409 ;
5410 ; READ CHARACTERS FROM THE DUT RX FIFO AND WAIT FOR RX.DATA.VALID TO GO CLEAR.
5411 ;
5412 027104          012705  001130          ;
5413 027110          011400          ;
5414 027112          100027          ;
5415 027114          005305          ;
5416 027116          001374          ;
5417 027120          000416          ;
5418 ;
5419 ; ERROR REPORTS:
5420 ;
5421 ; REPORT MR BIT WOULD NOT CLEAR AFTER A DUT RESET.
5422 027122          012767  001275  154640  4$ : MOV  #0701.,ERRNBR          ;SET THE ERROR NUMBER IN ERROR TABLE.
5423 027130          012701  005651          ;
5424 027134          104460          ;
5425 027136          000415          ;
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5425
5426 027140 012767 001276 154622 6$: ;REPORT THAT RX.DATA.VALID BIT WAS NOT SET AFTER A RESET COMPLETION.
5427 027146 012701 010245      MOV #0702.,ERRNBR ;SET THE ERROR NUMBER IN ERROR TABLE.
5428 027152 104460      MOV #EM0702,R1 ;SELECT ERROR MESSAGE.
                                ERROR ;REPORT ERROR. >>>> ERROR #0702 <<<<<
5429 027154 000406      BR 60$ ;EXIT THE TEST. TRAP C$ERROR
5430
5431
5432 027156 012767 001277 154604 8$: ;REPORT THAT RX.DATA.VALID BIT COULD NOT BE CLEARED BY PURGING FIFO.
5433 027164 012701 010425      MOV #0703.,ERRNBR ;SET THE ERROR NUMBER IN ERROR TABLE.
5434 027170 104460      MOV #EM0703,R1 ;SELECT ERROR MESSAGE.
                                ERROR ;REPORT ERROR. >>>> ERROR #0703 <<<<<
5435
5436 027172 012700 000340 60$: SETPRI #PRI07 ;DISABLE ALL INTERRUPTS. TRAP C$SPRI
                                MOV #PRI07,R0
5437 027200 005067 153102      CLR CTRLCF ;INDICATE THAT WE COMPLETED THE TEST. TRAP C$SPRI
5438 027204 104401      ENDTST
                                L10034: TRAP C$ETST

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5440 .SBTTL HARDWARE TEST - RLNA -
5441 ;* *****
5442 ;* - RBUF RX LINE NUMBER FIELD TEST -
5443 ;* THIS TEST VERIFIES THAT THE DUT RBUF RX LINE NUMBER FIELD IS WORKING
5444 ;* CORRECTLY BY UTILIZING THE SELFTEST CODES WHICH ARE PUT IN THE RX
5445 ;* FIFO AFTER A BOARD RESET.
5446 ;*
5447 ;* *****
5448 027206 BGNTST
5449 027206 SETPRI #PRI05 ;ALLOW LTC INTERRUPTS. T8::
027206 012700 000240
027212 104441
5450 000010
5451 027214 012767 000010 153102 TNUM == TNUM + 1 ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
5452 027222 012767 177777 153056 MOV #TNUM,TSTNUM ;SET UP THE TEST NUMBER. (8)
5453 027230 012767 000001 154530 MOV #-1,CTRLCF ;INDICATE THAT WE ARE WITHIN A TEST.
5454 027236 012767 010610 154526 MOV #1,ERRTYP ;SET ERROR TYPE AS FATAL IN ERROR TABLE.
5455 MOV #EM0801,ERRMSG ;SET ERROR MESSAGE ADDRESS IN ERROR TABLE.
5456 ;*
5457 ; SET THE DUT CSR MASTER RESET (MR) BIT, PERFORM THE SKIP SELFTEST SEQUENCE,
5458 ; AND WAIT UP TO 5 SECONDS FOR THE MR BIT TO CLEAR.
5459 027244 012701 011610
5460 027250 012702 000040
5461 027254 005003
5462 027256 016704 152760
5463 027262 010214
5464 027264 004767 173054 JSR PC,SKPSTS ;SKIP THE SELFTEST.
5465 027270 004767 170670 JSR PC,MSLGET ;WAIT FOR DUT_CSR_MR BIT TO CLEAR.
5466 027274 103016 BCC 4$ ;GO REPORT ERROR IF MR DID NOT CLEAR.
5467 ;*
5468 ; READ CHARACTERS FROM THE DUT RX FIFO AND VERIFY THAT THE LINE NUMBERS ARE
5469 ; CORRECT.
5470 ; EIGHT CHARACTERS ARE READ FROM THE FIFO.
5471 ;*
5472 027276 005001
5473 027300 012400
5474 027302 011402
5475 027304 010203
5476 027306 000303
5477 027310 042703 177760
5478 027314 020301
5479 027316 001017
5480 027320 005201
5481 027322 020127 000010
5482 027326 001365
5483 027330 000423
5484
5485
5486 ;*
5487 ; ERROR REPORTS:
5488 ;*
5489 027332 012767 001441 154430 4$: ;REPORT MR BIT WOULD NOT CLEAR AFTER A DUT RESET.
5490 027340 012767 016136 154426 MOV #0801,ERRNBR ;SET THE ERROR NUMBER IN ERROR TABLE.
5491 027346 012701 005651 MOV #ER0503,ERRBLK ;SET ERROR ROUTINE ADDRESS IN ERROR TABLE.
5492 027352 MOV #EM0202,R1 ;SELECT ERROR MESSAGE.
027352 104460 ERROR ;REPORT ERROR. >>>> ERROR #0801 <<<<<
TRAP C$ERROR

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5493 027354 000411          BR      60$          ;EXIT THE TEST.
5494
5495
5496 027356 012767 001442 154404 6$: ;REPORT THAT RX LINE NUMBER FIELD IS WRONG FOR SELFTEST CODE.
5497 027364 012767 016136 154402      MOV    #0802.,ERRNBR ;SET THE ERROR NUMBER IN ERROR TABLE.
5498 027372 012701 010656          MOV    #ER0503,ERRBLK ;SET ERROR ROUTINE ADDRESS IN ERROR TABLE.
5499 027376          MOV    #EM0802,R1    ;SELECT ERROR MESSAGE.
          ERROR          ;REPORT ERROR.          >>>>> ERROR #0802 <<<<<
          TRAP          C#ERROR
5500
5501 027400          60$:  SETPRI  #PRI07          ;DISABLE ALL INTERRUPTS.
          027400 012700 000340
          027404 104441
5502 027406 005067 152674          CLR    CTRLCF          ;INDICATE THAT WE COMPLETED THE TEST.
          027412          TRAP          C#SPRI
          027412          L10035:
          027412 104401          TRAP          C#ETST
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5505 .SBTTL  HARDWARE TEST          - BMPCHK -
5506 ;* *****
5507 ;* - BMP CHECK TEST -
5508 ;* THIS TEST IS USED TO VERIFY THAT THE DUT DOES NOT IMMEDIATELY FAIL
5509 ;* THE ON-BOARD BACKGROUND-MONITOR PROGRAM, AND HENCE INVALIDATE
5510 ;* SUCCEEDING TESTS.
5511 ;* THIS TEST LOOKS FOR BMP CODES IN THE FIFO FOR A SET PERIOD IMMEDIATELY
5512 ;* AFTER THE SELF-TEST IS SKIPPED.
5513 ;* ANY BMP CODES THAT ARE FOUND ARE SAVED ON THE QUEUE AND ARE ALSO
5514 ;* REPORTED IN THIS TEST.
5515 ;*
5516 ;* *****
5517 027414 BGNTST
5518 027414 SETPRI #PRIOS          ;ALLOW LTC INTERRUPTS.      T9::
5519 027414 012700 000240          ;
5520 027420 104441
5521 000011          ;
5522 027422 012767 000011 152674  TNUM == TNUM + 1      ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
5523 027430 012767 177777 152650  MOV #TNUM,TSTNUM      ;SET UP THE TEST NUMBER.          (9)
5524 027436 012767 000001 154322  MOV #-1,CTRLCF      ;INDICATE THAT WE ARE WITHIN A TEST.
5525 027444 012767 001605 154316  MOV #1,ERRTP      ;SET ERROR TYPE AS FATAL IN ERROR TABLE.
5526 027452 012767 010726 154312  MOV #0901,ERRNBR   ;SET THE ERROR NUMBER.
5527 ;*
5528 ;* WAIT UP TO 3 SECONDS FOR THE DUT MASTER RESET BIT TO CLEAR.
5529 ;* IF TIME-OUT OCCURS, THEN EXIT THIS TEST.
5530 ;*
5531 027460 012701 005670          ;
5532 027464 012702 000040          ;
5533 027470 005003          ;
5534 027472 016704 152544          ;
5535 027476 004767 170462          ;
5536 027502 103027          ;
5537 ;*
5538 ;* MOV #3000,R1          ;TIME-OUT VALUE IS 3.0 SECONDS.
5539 ;* MOV #BIT05,R2      ;WAITING FOR MASTER RESET BIT.
5540 ;* CLR R3              ;WAITING FOR BIT TO CLEAR.
5541 ;* MOV CSRA,R4         ;BIT IS IN THE DUT'S CSR.
5542 ;* JSR PC,MSLGET      ;WAIT FOR DUT_CSR_MR BIT TO CLEAR.
5543 ;* BCC 50$            ;ABORT THE TEST IF MR DID NOT CLEAR.
5544 ;*
5545 ;* RESET THE DUT, SKIP THE SELF-TEST.
5546 ;*
5547 027504 010214          ;
5548 027506 004767 172632          ;
5549 ;*
5550 ;* MOV R2,(R4)         ;SET THE DUT MASTER RESET BIT.
5551 ;* JSR PC,SKPSTS      ;WRITE THE SKIP SELFTEST CODES TO THE DUT.
5552 ;*
5553 ;* WAIT FOR MASTER RESET TO CLEAR. DELAY FOR 500 MILLI-SECS BEFORE PURGING
5554 ;* THE FIFO.
5555 ;*
5556 027512 012704 000764          ;
5557 027516 004767 170402          ;
5558 027522 004767 171014          ;
5559 027526 103015          ;
5560 ;*
5561 ;* MOV #500,R4         ;TIME-OUT VALUE IS 500 MILLI-SECONDS.
5562 ;* JSR PC,DELAY       ;WAIT FOR BMP TO BEGIN EXECUTION.
5563 ;* JSR PC,PUFIFO      ;PURGE THE FIFO, SAVING ANY BMP CODES.
5564 ;* BCC 50$            ;ABORT THE TEST IF THE FIFO DID NOT CLEAR.
5565 ;*
5566 ;* REPORT THE ERROR IF ANY BMP CODES WERE FOUND.
5567 ;*
5568 027530 016702 152770          ;
5569 027534 012703 002526          ;
5570 027540 020203          ;
5571 027542 001414          ;
5572 ;*
5573 ;* MOV BMPCQP,R2      ;GET THE CONTENTS OF THE POINTER TO THE BMP Q.
5574 ;* MOV #BMPCQB,R3    ;GET THE START ADDRESS OF THE QUEUE.
5575 ;* CMP R2,R3         ;SEE IF THE POINTER HAS MOVED FROM THE BASE.
5576 ;* BEQ 60$           ;EXIT NO CODES IN THE QUEUE.
5577 ;*
5578 ;* THERE IS AT LEAST ONE BMP CODE IN THE QUEUE. REPORT THE ERROR.
5579 ;*
5580 ;* REPORT ERROR BMP CODE FOUND IN TEST NN, BMP CODE:NNNNNN"

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5559 027544 012701 010766          MOV    #EM0902,R1      ;PASS THE MESSAGE TO BE REPORTED.
5560 027550          ERRDF   0901,EM0901,ER9301 ;
      027550 104455          >>>>> ERROR #0901 <<<<<.
      027552 001605          TRAP   C$ERDF
      027554 010726          .WORD  901
      027556 017262          .WORD  EM0901
5561 027560 000405          .WORD  ER9301
5562          BR      60$
5563 027562 012767 001606 154200 50$:  MOV    #902.,ERRNBR   ;SET >>>>> ERROR #0902 <<<<<.
5564 027570 004767 172676          JSR    PC,TSABRT     ;REPORT NON-TEST RELATED ERROR.
5565          60$:
5566 027574          SETPRI #PRI07        ;DISABLE ALL INTERRUPTS.
      027574 012700 000340          MOV    #PRI07,R0
      027600 104441          TRAP   C$SPRI
5567 027602 005067 152500          CLR    CTRLCF        ;INDICATE THAT WE COMPLETED THE TEST.
5568 027606          ENDTST
      027606 104401          L10036:
      TRAP   C$ETST
    
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5580 027610
      027610
5581 027610
      027610 012700 000240
      027614 104441
5582      000012
5583 027616 012767 000012 152500
5584 027624 012767 177777 152454
5585 027632 012767 000001 154126
5586 027640 012767 011022 154124
5587 027646 012767 016136 154120
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5592 027654 012701 011610
5593 027660 012702 000040
5594 027664 005003
5595 027666 016704 152350
5596 027672 004767 170266
5597 027676 103037
5598
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5600
5601
5602
5603 027700 012701 000062
5604 027704 010214
5605 027706 004767 172432
5606 027712 004767 170246
5607 027716 103011
5608 027720 020127 000050
5609 027724 003015
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5616 027726 012767 001753 154034
5617 027734 004767 171554
5618 027740 000423
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5623 027742 012767 001751 154020

.SBTTL  HARDWARE TEST          - SKSELF -
; ** *****
; *          - SKIP SELF-TEST TEST -
; * THIS TEST VERIFIES THAT THE DUT SKIPS THE SELF-TEST WITHIN THE
; * TIME ALLOWED, AND THAT THE FIFO CONTAINS THE CORRECT CODES AFTER ITS
; * COMPLETION.
; * *****
; -- *****
      BGNTST
5581      SETPRI  @PRI05          ;ALLOW LTC INTERRUPTS.      T10::
      MOV      @PRI05,R0
      TRAP    C$SPRI
5582      TNUM == TNUM + 1      ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
5583      MOV      @TNUM,TSTNUM ;SET UP THE TEST NUMBER.      (10)
5584      MOV      @-1,CTRLCF   ;INDICATE THAT WE ARE WITHIN A TEST.
5585      MOV      @1,ERRTYP    ;SET ERROR TYPE AS FATAL IN ERROR TABLE.
5586      MOV      @EM1001,ERRMSG ;SET ERROR MESSAGE ADDRESS IN ERROR TABLE.
5587      MOV      @ER0503,ERRBLK ;SET ERROR ROUTINE ADDRESS IN ERROR TABLE.
5588
5589
5590
5591
5592      ; WAIT UP TO 5 SECONDS FOR THE DUT MASTER RESET BIT TO CLEAR.
5593      ; IF TIME-OUT OCCURS, THEN EXIT THIS TEST.
5594
5595      MOV      @5000.,R1      ;TIME-OUT VALUE IS 5.0 SECONDS.
5596      MOV      @BIT05,R2     ;WAITING FOR MASTER RESET BIT.
5597      CLR      R3            ;WAITING FOR BIT TO CLEAR.
5598      MOV      CSRA,R4       ;BIT IS IN THE DUT'S CSR.
5599      JSR      PC,MSLGET     ;WAIT FOR DUT_CSR_MR BIT TO CLEAR.
5600      BCC     50$           ;ABORT THE TEST IF MR DID NOT CLEAR.
5601
5602      ; DETERMINE IF THE DUT TAKES TOO SHORT OR TOO LONG A TIME TO SKIP THE SELF-TEST
5603      ; SET-UP A TIME-OUT OF 50 MILLI-SECOND, IF MR IS CLEAR IN LESS THAN 10 MILLI
5604      ; -SECOND, OR GREATER THAN 50 MILLI-SECONDS, REPORT THE ERROR.
5605
5606      MOV      @50.,R1        ;TIME-OUT VALUE IS 50 MILLI-SECONDS.
5607      MOV      R2,(R4)       ;SET THE DUT MASTER RESET BIT.
5608      JSR      PC,SKPSTS     ;WRITE THE SKIP SELFTEST CODES TO THE DUT.
5609      JSR      PC,MSLGET     ;WAIT FOR DUT_CSR_MR BIT TO CLEAR.
5610      BCC     2$            ;GO REPORT ERR IF SKIPPING STEST TOOK TOO LONG.
5611      CMP     R1,@40.
5612      BGT     4$            ;GO REP ERR IF SELFTEST COMPLETED IN < 10 MS.
5613
5614      ; SELF-TEST COMPLETED WITHIN 10 MILLI-SEC TO 50 MILLI-SECONDS.
5615      ; VERIFY THAT THE SELF-TEST CODES IN THE FIFO ARE "GOOD" CODES ,IE THE DUT
5616      ; SUCCESSFULLY COMPLETED THE SELF-TEST.
5617      ; THIS SUBROUTINE REPORTS ERRORS WITH NUMBERS >>>> 1003 THRU 1007 <<<<.
5618
5619      MOV      @1003.,ERRNBR   ;SET ERROR NUMBER TO 1003.
5620      JSR      PC,RSTRPT     ;CHECK SELF-TEST CODES IN THE FIFO.
5621      BR      60$           ;EXIT TEST.
5622
5623      ; ERROR REPORTS:
5624
5625      ;REPORT SKIP SELF-TEST TOOK TOO LONG.
5626      MOV      @1001.,ERRNBR ;SET THE ERROR NUMBER IN THE ERROR TABLE.
    
```



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5695 030164 012701 000001          MOV    #1,R1          ;SET TIME-OUT OF 1 MILLI-SECOND.
5696 030170 052703 020000          BIS    #BIT13,R3     ;WAIT FOR BIT TO SET.
5697 030174 004767 167764          JSR    PC,MSLGET     ;DOUBLE CHECK TO ELIMINATE NOISE PROBLEMS.
5698 030200 103016                    BCC    60$           ;EXIT IF DIAG_FAIL BIT STILL CLEAR.
5699 030202 010501          MOV    R5,R1         ;PASS THE REMAINING TIME-OUT VALUE.
5700 030204 000762          BR     2$            ;LOOP TO CHECK AGAIN.
5701
5702          ;+
5703          ; ERROR REPORTS:
5704          ;-
5705 030206 012767 002115 153554 4$: ;REPORT DIAGNOSTIC FAIL BIT BAD.
5706 030214 012701 011517          MOV    #1101,ERRNBR ;SET THE ERROR NUMBER IN THE ERROR TABLE.
5707 030220 104460          MOV    #EM1205,R1   ;SELECT ERROR MESSAGE.
5708 030222 000405          ERROR                    ;REPORT ERROR.          >>>>> ERROR #1101 <<<<<
5709          BR     60$           ;EXIT THE TEST.          TRAP    C$ERROR
5710 030224 012767 002116 153536 50$: MOV    #1102,ERRNBR ;SET THE ERROR NUMBER FOR TSABRT RTN.
5711 030232 004767 172234          JSR    PC,TSABRT    ;REPORT NON-TEST RELATED ERROR.
5712
5713 030236                    60$: SETPRI #PRI07      ;DISABLE ALL INTERRUPTS.
5714 030242 012700 000340                    MOV    #PRI07,R0
5715 030244 005067 152036          CLR    CTRLCF       ;INDICATE THAT WE COMPLETED A TEST.
5715 030250                    TRAP    C$SPRI
5715 030250 104401                    L10040: TRAP    C$ETST
    
```

```

5717 .SBTTL HARDWARE TEST - SELFTS -
5718 :* *****
5719 :* - SELF-TEST TEST -
5720 :* THIS TEST VERIFIES THAT THE DUT'S SELF-TEST EXECUTES WITHIN THE
5721 :* TIME ALLOWED, AND THAT THE FIFO CONTAINS THE CORRECT CODES AFTER ITS
5722 :* COMPLETION.
5723 :*
5724 :*
5725 :* *****
5725 030252 BGNTST
5726 030252 SETPRI #PRI05 ;ALLOW LTC INTERRUPTS. T12::
5727 030252 012700 000240 ;
5728 030252 104441 ;
5729 030252 000014 ;
5727 030260 012767 000014 152036 TNUM == TNUM + 1 ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
5728 030260 012767 152012 MOV #TNUM,TSTNUM ;SET UP THE TEST NUMBER. (12)
5729 030266 012767 177777 MOV #-1,CTRLCF ;INDICATE THAT WE ARE WITHIN A TEST.
5730 030274 012767 000001 153464 MOV #1,ERRTYP ;SET ERROR TYPE AS FATAL IN ERROR TABLE.
5731 030302 012767 011273 153462 MOV #EM1201,ERRMSG ;SET ERROR MESSAGE ADDRESS IN ERROR TABLE.
5732 030310 012767 016136 153456 MOV #ER0503,ERRBLK ;SET ERROR ROUTINE ADDRESS IN ERROR TABLE.
5733 :*
5734 :* WAIT UP TO 5 SECONDS FOR THE DUT MASTER RESET BIT TO CLEAR.
5735 :* IF TIME-OUT OCCURS, THEN EXIT THIS TEST.
5736 :*
5737 030316 012701 011610 ;
5738 030322 012702 000040 ;
5739 030326 005003 ;
5740 030330 016704 151706 ;
5741 030334 004767 167624 ;
5742 030340 103067 ;
5743 :*
5744 :*
5745 :*
5746 :*
5747 :*
5748 030342 012701 011610 ;
5749 030346 010214 ;
5750 030350 004767 167610 ;
5751 030354 103034 ;
5752 030356 012702 011610 ;
5753 030362 160102 ;
5754 030364 020227 000062 ;
5755 030370 002435 ;
5756 030372 020227 000764 ;
5757 030376 002441 ;
5758 :*
5759 :*
5760 :*
5761 :*
5762 030400 032714 020000 ;
5763 030404 001412 ;
5764 :*
5765 030406 012767 002264 153354 ;
5766 030414 012701 011517 ;
5767 030420 030420 104460 ;
5768 :*
5769 :*

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5770
5771 030422 032767 000100 151574 BIT #BIT06,OPTION ;EXIT WITH TEST FAILURE MESSAGE IF
5772 030430 001440 BEQ 60$ ;NO EXTENDED ERROR REPORTING HAS BEEN REQUESTED
5773 ;DURING THE SOFTWARE QUESTIONS.
5774
5775 ;+
5776 ; VERIFY THAT THE SELF-TEST CODES IN THE FIFO ARE "GOOD" CODES ,IE THE DUT
5777 ; SUCCESSFULLY COMPLETED THE SELF-TEST.
5778 ; THIS SUBROUTINE REPORTS ERRORS WITH NUMBERS >>>> 1205 THRU 1209 <<<<.
5779 030432 012767 002265 153330 2$: MOV #1205.,ERRNBR ;SET ERROR NUMBER TO 1205.
5780 030440 004767 171050 JSR PC,RSTRPT ;CHECK SELF-TEST CODES IN THE FIFO.
5781 030444 000432 BR 60$ ;EXIT TEST.
5782
5783 ;+
5784 ; ERROR REPORTS:
5785 ;-
5786 030446 012767 002261 153314 4$: ;REPORT SELF-TEST TOOK TOO LONG TO COMPLETE.
5787 030454 012701 011321 MOV #1201.,ERRNBR ;SET THE ERROR NUMBER IN THE ERROR TABLE.
5788 030460 104460 MOV #EM1202,R1 ;SELECT ERROR MESSAGE.
5789 030462 000423 ERROR ;REPORT ERROR. >>>> ERROR #1201 <<<<
5790 BR 60$ TRAP C$ERROR
5791 ;EXIT THE TEST.
5792 030464 012767 002262 153276 6$: ;REPORT SELF-TEST DID NOT EXECUTE AFTER DUT RESET.
5793 030472 012701 011463 MOV #1202.,ERRNBR ;SET THE ERROR NUMBER IN ERROR TABLE.
5794 030476 104460 MOV #EM1204,R1 ;SELECT ERROR MESSAGE.
5795 030500 000414 ERROR ;REPORT ERROR. >>>> ERROR #1202 <<<<
5796 BR 60$ TRAP C$ERROR
5797 ;EXIT THE TEST.
5798 030502 012767 002263 153260 8$: ;REPORT SELF-TEST COMPETED TOO SOON.
5799 030510 012701 011405 MOV #1203.,ERRNBR ;SET THE ERROR NUMBER IN THE ERROR TABLE.
5800 030514 104460 MOV #EM1203,R1 ;SELECT ERROR MESSAGE.
5801 030516 000405 ERROR ;REPORT ERROR. >>>> ERROR #1203 <<<<
5802 BR 60$ TRAP C$ERROR
5803 ;EXIT THE TEST.
5803 030520 012767 002272 153242 50$: MOV #1210.,ERRNBR ;SET THE ERROR NUMBER FOR TSABRT RTN.
5804 030526 004767 171740 JSR PC,TSABRT ;REPORT NON-TEST RELATED ERROR.
5805
5806 030532 012700 000340 60$: SETPRI #PRI07 ;DISABLE ALL INTERRUPTS.
5807 030536 104441 MOV #PRI07,RO
5808 030540 005067 151542 CLR CTRLCF ;INDICATE THAT WE COMPLETED THE TEST.
5808 030544 104401 TRAP C$SPRI
5808 030544 104401 L10041: TRAP C$ETST

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5820 030546
      030546
5821 030546
      030546 012700 000240
      030552 104441
5822      000015
5823 030554 012767 000015 151542
5824 030562 012767 177777 151516
5825 030570 012767 000001 153170
5826 030576 012767 011543 153166
5827 030604 012767 016136 153162
5828 030612 012767 002425 153150
5829
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5833 030620 012701 011610
5834 030624 012702 000040
5835 030630 005003
5836 030632 016704 151404
5837 030636 004767 167322
5838 030642 103064
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5844 030644 012777 000040 151370
5845 030652 012704 000031
5846 030656 004767 167242
5847 030662 012777 146314 151370
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5852 030670 005267 153074
5853 030674 012701 011610
5854 030700 012702 000040
5855 030704 005003
5856 030706 016704 151330
5857 030712 004767 167246
5858 030716 103036
5859
5860
5861
5862
5863 030720 005267 153044

.SBTTL HARDWARE TEST - STFAIL -
;+ *****
;* - SELF-TEST FAIL TEST -
;* THIS TEST VERIFIES THAT THE DUT WILL REPORT SELFTEST ERRORS VIA THE
;* FIFO. AND THAT THE DIAGNOSTIC FAIL BIT WILL INDICATE THE ERROR.
;* THIS IS ACCOMPLISHED VIA A SOFTWARE 'HOOK' IN THE SELF-TEST, WHICH
;* FORCES A "PROC1 TO RAM ERROR" TO BE PLACED IN THE FIFO.
;*
;-- *****
      BGNTST
      SETPRI #PRI05 ;ALLOW LTC INTERRUPTS. T13::
      MOV #PRI05,RO
      TRAP C$SPRI
      TNUM == TNUM + 1 ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
      MOV #TNUM,TSTNUM ;SET UP THE TEST NUMBER. (13)
      MOV #-1,CTRLCF ;INDICATE THAT WE ARE WITHIN A TEST.
      MOV #1,ERRTYP ;SET ERROR TYPE AS FATAL IN ERROR TABLE.
      MOV #EM1301,ERRMSG ;SET ERROR MESSAGE ADDRESS IN ERROR TABLE.
      MOV #ER0503,ERRBLK ;SET ERROR ROUTINE ADDRESS IN ERROR TABLE.
      MOV #1301.,ERRNBR ;SET ERROR NUMBER TO 1301.
;+
; WAIT UP TO 5 SECONDS FOR THE DUT MASTER RESET BIT TO CLEAR.
; IF TIME-OUT OCCURS, THEN EXIT THIS TEST.
;--
      MOV #5000.,R1 ;TIME-OUT VALUE IS 5.0 SECONDS.
      MOV #BIT05,R2 ;WAITING FOR MASTER RESET BIT.
      CLR R3 ;WAITING FOR BIT TO CLEAR.
      MOV CSRA,R4 ;BIT IS IN THE DUT'S CSR.
      JSR PC,MSLGET ;WAIT FOR DUT_CSR_MR BIT TO CLEAR.
      BCC 50$ ;GO REPORT ERROR IF MR DID NOT CLEAR.
;+
; RESET THE DUT, DELAY FOR 25 MILLI-SECONDS BEFORE WRITING THE FAIL_SELF_TEST
; CODE TO TBUFFCT REGISTER ON CHANNEL 0.
;--
      MOV #BIT05,BCSRA ;SET DUT MASTER RESET BIT, SELECT CHANNEL 0.
      MOV #25.,R4 ;PASS DELAY PERIOD OF 25 MILLI SECS.
      JSR PC,DELAY ;WAIT FOR SELFTEST TO INITIALISE.
      MOV #146314,@TXBFCA ;WRITE THE FAIL SELF-TEST CODE TO TBUFFCT REG.
;+
; WAIT UP TO 5 SECONDS FOR THE SELF-TEST TO COMPLETE.
; IF TIME-OUT OCCURS, THEN EXIT THIS TEST.
;--
      INC ERRNBR ;SET ERROR NUMBER TO 1302.
      MOV #5000.,R1 ;TIME-OUT VALUE IS 5.0 SECONDS.
      MOV #BIT05,R2 ;PASS THE BIT MAP OF THE BIT TO TEST.
      CLR R3 ;SET UP THE EXPECTED STATE.
      MOV CSRA,R4 ;BIT IS IN THE DUT'S CSR.
      JSR PC,MSLGET ;WAIT FOR DUT_CSR_MR BIT TO CLEAR.
      BCC 50$ ;GO REPORT ERROR IF MR DID NOT CLEAR.
;+
; VERIFY THE DIAGNOSTIC FAIL BIT IS SET, INDICATING THE ERROR.
; REPORT ERROR IF DIAGNOSTIC FAIL BIT IS CLEAR.
;--
      INC ERRNBR ;SET ERROR NUMBER TO 1303.
    
```



```

5864 030724 032714 020000          BIT      #BIT13,(R4)      ;CHECK THE STATE OF THE DIAG_FAIL BIT.
5865 030730 001425          BEQ      10$              ;GO REPORT ERROR IF DIAG_FAIL BIT CLEAR.
5866                                     ;*
5867                                     ; REMOVE THE 8 SELF-TEST CODES FORM THE FIFO, AND VERIFY THAT AT LEAST
5868                                     ; ONE IS A PROC1 TO RAM ERROR CODE (231).
5869                                     ;-
5870 030732 005267 153032          INC      ERRNBR          ;SET ERROR NUMBER TO 1304.
5871 030736 012700 000010          MOV      #8.,R0         ;SET MAXIMUM READ COUNT.
5872 030742 005001          CLR      R1             ;CLEAR THE CORRECT CODE COUNTER.
5873 030744 016704 151274          MOV      RBUFA,R4       ;GET ADDRESS OF THE RECEIVER BUFFER REGISTER.
5874 030750 011402 6$:          MOV      (R4),R2        ;READ A CODE FROM THE FIFO.
5875 030752 100020          BPL      50$           ;GO REPORT ERROR IF THE FIFO IS EMPTY.
5876 030754 042702 007400          BIC      #7400,R2       ;REMOVE THE LINE NUMBER FROM THE CODE.
5877 030760 120227 170231          CMPB    R2,#170231     ;IS IT THE CORRECT ERROR CODE?.
5878 030764 001001          BNE      8$           ;SKIP NEXT INSTRUCTION, IF NOT A 231 CODE.
5879 030766 005201          INC      R1             ;INCREMENT COUNTER.
5880 030770 005300 8$:          DEC      R0             ;DECREMENT MAX READ COUNTER.
5881 030772 001366          BNE      6$           ;LOOP IF 8 CODES HAVE NOT BEEN READ.
5882 030774 005701          TST     R1             ;WERE ANY 231 CODES FOUND?.
5883 030776 001010          BNE      60$          ;YES, THEN EXIT.
5884 031000 005267 152764          INC      ERRNBR        ;NO, SET ERROR NUMBER TO 1305 AND REPORT ERROR.
5885                                     ;REPORT SELF-TEST ERROR REPORTING BAD.
5886 031004 012701 011576 10$:   MOV      #EM1302,R1     ;SELECT ERROR MESSAGE.
5887 031010 104460          ERROR                                ;REPORT ERROR.          >>>> ERROR <<<<<
5888 031012 000402          BR      60$           ;EXIT THE TEST.          TRAP      C$ERROR
5889
5890 031014 004767 171452 50$:   JSR      PC,TSABRT     ;REPORT NON-RELATED TEST ERROR.
5891
5892 031020 012700 000340 60$:   SETPRI  #P:IO7        ;DISABLE ALL INTERRUPTS.
5893 031024 104441          MOV      #PRI07,R0     ;INDICATE THAT WE COMPLETED THE TEST.
5894 031026 005067 151254          CLR      CTRLCF        ;INDICATE THAT WE COMPLETED THE TEST.
5894 031032          ENDTST
5894 031032 104401          L10042: TRAP      C$ETST

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031034
031034
031034 012700 000240
031040 104441
000016
031042 012767 000016 151254
031050 012767 177777 151230
031056 012767 000001 152702
031064 012767 011635 152700
031072 012767 016136 152674

031100 012701 005670
031104 012702 000040
031110 005003
031112 016704 151124
031116 004767 167042
031122 103131

031124 010214
031126 004767 171212
031132 012701 011610
031136 004767 167022
031142 103121

031144 012705 000040
031150 012703 000143
031154 010304
031156 012767 002571 152604
031164 012701 011674

031170 017702 151050
031174 100077

```
.SBTTL  HARDWARE TEST          - ROMVER -
;+ *****
;*          - ROM VERSION TEST -
;*  THIS TEST VERIFIES THAT THE DUT'S SELF-TEST PLACES VALID ROM VERSION
;*  NUMBERS IN THE FIFO AFTER IT HAS BEEN SKIPPED.  THE ROM VERSION NUMBERS
;*  WILL BE REPORTED (ON THE FIRST PASS ONLY), IF AN AFFIRMATIVE ANSWER
;*  WAS GIVEN TO THE SOFTWARE P-TABLE QUESTION.
;+ *****
;-- *****
          BGNTST
          SETPRI  #PRI05          ;ALLOW LTC INTERRUPTS.          T14::
          MOV     #PRI05,RO      TRAP    C$SPRI
          TNUM == TNUM + 1      ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
          MOV     #TNUM,TSTNUM  ;SET UP THE TEST NUMBER.          (14)
          MOV     #-1,CTRLCF    ;INDICATE THAT WE ARE WITHIN A TEST.
          MOV     #1,ERRTYP     ;SET ERROR TYPE AS FATAL IN ERROR TABLE.
          MOV     #EM1401,ERRMSG ;SET ERROR MESSAGE ADDRESS IN ERROR TABLE.
          MOV     #ER0503,ERRBLK ;SET ERROR ROUTINE ADDRESS IN ERROR TABLE.
;+
; WAIT UP TO 3 SECONDS FOR THE DUT MASTER RESET BIT TO CLEAR.
; IF TIME-OUT OCCURS, THEN EXIT THIS TEST.
;--
          MOV     #3000.,R1      ;TIME-OUT VALUE IS 3.0 SECONDS.
          MOV     #BIT05,R2      ;WAITING FOR MASTER RESET BIT.
          CLR     R3              ;WAITING FOR BIT TO CLEAR.
          MOV     CSRA,R4        ;BIT IS IN THE DUT'S CSR.
          JSR     PC,MSLGET      ;WAIT FOR DUT_CSR_MR BIT TO CLEAR.
          BCC     50$            ;ABORT THE TEST IF MR DID NOT CLEAR.
;+
; SET THE MASTER RESET BIT, AND SKIP THE SELF TEST.
;--
          MOV     R2,(R4)        ;SET THE MASTER RESET BIT.
          JSR     PC,SKPSTS      ;SKIP THE SELF TEST.
          MOV     #5000.,R1      ;TIME-OUT VALUE IS 5.0 SECONDS.
          JSR     PC,MSLGET      ;WAIT FOR DUT_CSR_MR BIT TO CLEAR.
          BCC     50$            ;ABORT THE TEST IF MR DID NOT CLEAR.
;+
; REMOVE CHARACTERS FROM THE FIFO UNTIL EITHER:
; (A) THE FIFO IS PURGED, GO REPORT THE ERROR.
; (B) THE MAXIMUM TRY COUNTER IS ZERO, GO REPORT THE ERROR.
; (C) PROC_1'S ROM VERSION NUMBER WAS FOUND BEFORE PROC_2'S, GO REPORT ERROR.
; (D) BOTH ROM VERSION NUMBERS HAVE BEEN FOUND.
;--
          MOV     #32.,R5        ;SET MAXIMUM TRY COUNTER.
          MOV     #99.,R3        ;SET AN INVALID ROM VERSION NUMBER FOR PROC_1.
          MOV     R3,R4          ;SET AN INVALID ROM VERSION NUMBER FOR PROC_2.
          MOV     #1401.,ERRNBR  ;SET THE ERROR NUMBER TO 1401.
          MOV     #EM1402,R1     ;SELECT MESSAGE TO BE REPORTED IF FIFO EMPTY.
2$:      MOV     @RBUFA,R2      ;READ THE NEXT CHAR FROM THE FIFO.
          BPL     12$           ;GO REPORT ERROR IF FIFO EMPTY.
;+
; CHECK IF THE READ DATA IS A BMP CODE.
```



```

5950
5951 031176 012700 000301      ;-
5952 031202 040200              MOV    #301,R0      ;SET-UP A BIT MASK OF A BMP CODE.
5953 031204 001003              BIC    R2,R0        ;TRY TO CLEAR THE BIT MASK WITH THE READ DATA.
5954 031206 004767 171064      BNE    4$           ;BRANCH IF NOT A BMP CODE.
5955 031212 000435              JSR    PC,SAVBMP    ;SAVE THE BMP CODE ON THE QUEUE.
5956                          BR     8$           ;
5957                          ;+
5958                          ; CHECK IF THE READ DATA IS A SELF-TEST CODE.
5959 031214 012700 000201      4$:-
5960 031220 040200              MOV    #201,R0      ;SET-UP A BIT MASK OF A SELFTEST CODE.
5961 031222 001431              BIC    R2,R0        ;TRY TO CLEAR THE BIT MASK WITH THE READ DATA.
5962                          BEQ    8$           ;BRANCH IF IT IS A SELFTEST CODE.
5963                          ;+
5964                          ; THE READ DATA IS A ROM VERSION NUMBER, DETERMINE WHICH ONE IT IS.
5965                          ;-
5966 031224 032702 000002      BIT    #BIT1,R2     ;CHECK THE PROCESSOR NUMBER BIT IN THE CODE.
5967 031230 001407              BEQ    6$           ;BRANCH IF IT IS PROC_1 ROM VERSION NUMBER.
5968 031232 010204              MOV    R2,R4        ;SAVE PROC_2 ROM VERSION NUMBER.
5969 031234 042704 177603      BIC    #177603,R4   ;CLEAR ANY UNWANTED BITS.
5970 031240 000241              CLC                    ;CLEAR THE CARRY BIT.
5971 031242 006004              ROR    R4            ;SHIFT THE CODES ALONG TO GET THE ROM
5972 031244 006004              ROR    R4            ; VERSION NUMBER IN THE LOW 5 BITS.
5973 031246 000417              BR     8$           ;
5974 031250 010203      6$:-
5975 031252 042703 177603      MOV    R2,R3        ;SAVE PROC_1 ROM VERSION NUMBER.
5976 031256 000241              BIC    #177603,R3   ;CLEAR ANY UNWANTED BITS.
5977 031260 006003              CLC                    ;CLEAR THE CARRY BIT.
5978 031262 006003              ROR    R3            ;SHIFT THE CODE ALONG TO GET THE ROM
5979 031264 020427 000143      ROR    R3            ; VERSION NUMBER IN THE LOW 5 BITS.
5980 031270 001016              CMP    R4,#99.      ;CHECK IF WE HAVE RECEIVE PROC_2 ROM CODE.
5981                          BNE    10$          ;GO REPORT BOTH ROM VERSION NUMBERS.
5982                          ;+
5983                          ; RECEIVED ROM VERSION NUMBERS OUT OF SEQUENCE.
5984                          ; IE. PROC_1'S ROM VERSION NUMBER FOUND IN THE FIFO BEFORE PROC_2'S.
5985 031272 012701 011762      ;-
5986 031276 012767 002572 152464 MOV    #EM1403,R1    ;SELECT THE ERROR MESSAGE TO BE REPORTED.
5987 031304 000433              MOV    #1402.,ERRNBR ;SET THE ERROR NUMBER.
5988                          BR     12$          ;GO REPORT ERROR.
5989 031306 005305      8$:-
5990 031310 001327              DEC    R5           ;DECREMENT THE MAX TRY COUNTER.
5991 031312 012701 012035      BNE    2$           ;LOOP TO GET THE NEXT CHAR FROM THE FIFO.
5992 031316 012767 002573 152444 MOV    #EM1404,R1    ;SELECT THE ERROR MESSAGE TO BE REPORTED.
5993 031324 000423              MOV    #1403.,ERRNBR ;SET THE ERROR NUMBER.
5994                          BR     12$          ;GIVE UP, GO REPORT ERROR.
5995                          ;+
5996                          ; IF THIS IS THE FIRST PASS, AND SOFTWARE P-TABLE QUESTION WAS ANSWERED YES,
5997                          ; THEN REPORT THE ROM VERSION NUMBERS TO THE OPERATOR.
5998 031326 032767 000001 150670 ;-
5999 031334 001431      10$:-
6000 031336 026727 150750 000001 BIT    #BIT0,OPTION ;CHECK ON THE STATE OF THE SOFTWARE SWITCH.
6001 031344 003025              BEQ    60$          ;EXIT IF NO ROM VERSION PRINTOUT WAS REQUESTED.
6002 031346 010446              CMP    PASCNT,#1    ;CHECK IF THIS IS THE FIRST PASS.
6002 031346 010346              BGT    60$          ;EXIT IF ROM VERS HAVE ALREADY BEEN REPORTED.
6002 031346 012746 004154      PRINTB #EF1401,R3,R4 ;PRINT THE ROM VERSION NUMBERS.
6002 031350 010346              MOV    R4,-(SP)
6002 031352 012746 004154              MOV    R3,-(SP)
6002 031356 012746 000003              MOV    #EF1401,-(SP)
6002                          MOV    #3,-(SP)

```

```

031362 010600
031364 104414
031366 062706 000010
6003 031372 000412          BR      60$          ;EXIT THIS TEST.
6004
6005          ;
6006          ; ERROR REPORTS:
6007 031374 012767 016254 152372 12$: MOV  @ER1401,ERRBLK ;SELECT THE ERROR REPORTING ROUTINE.
6008 031402          104460          ERROR          ;REPORT ERROR.          >>>>> ERROR <<<<<
6009 031404 000405          BR      60$          TRAP      C$ERROR
6010
6011 031406 012767 002575 152354 50$: MOV  @1405.,ERRNBR ;SET UP ERROR NUMBER FOR TSABRT RTN.
6012 031414 004767 171052          JSR   PC,TSABRT ;REPORT NON-TEST RELATED ERROR.
6013
6014 031420          60$: SETPRI @PRI07 ;DISABLE ALL INTERRUPTS.
031420 012700 000340
031424 104441
6015 031426 005067 150654          CLR   CTRLCF          MOV      @PRI07,RO
6016 031432          ENDTST          ;INDICATE THAT WE COMPLETED THE TEST. TRAP      C$SPRI
031432 104401          L10043: TRAP      C$ETST

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6018 .SBTTL  HARDWARE TEST          - CSR4 -
6019 ;*****
6020 ;*
6021 ;*   - CSR BIT 4 TEST -
6022 ;*   THIS TEST VERIFIES THAT WHEN THIS BIT IS SET (AT THE SAME TIME
6023 ;*   AS MASTER RESET) THE DUT REMAINS INACTIVE WITH THE MASTER RESET
6024 ;*   BIT SET; AND WHEN CSR BIT 4 IS SUBSEQUENTLY CLEARED, THE BOARD
6025 ;*   BECOMES ACTIVE AND REPORTS SIX SKIP-SELFTEST CODES IN THE RXFIFO.
6026 ;*   ANY BMP CODES FOUND IN THE FIFO ARE SAVED TO BE REPORTED LATER.
6027 ;*
6028 ;*-----
6029 031434      BGNTST
        031434
6030 031434      SETPRI  #PRI05          ;ALLOW LTC INTERRUPTS.      T15::
        031434      012700      000240
        031440      104441
6031
6032
6033 031442      000017      TNUM == TNUM + 1      ;INCREMENT THE ASSMBLY TIME TEST COUNTER.
6034 031450      012767      000017      150654      MOV      #TNUM,TSTNUM      ;SET UP THE TEST NUMBER.
6035 031456      012767      177777      150630      MOV      #-1,CTRLCF      ;INDICATE THAT WE ARE WITHIN A TEST.
6036 031464      012767      000001      152302      MOV      #1,ERRTYP      ;SET ERROR TYPE AS FATAL IN ERROR TABLE.
6037 031472      012767      002735      152276      MOV      #1501,ERRNBR      ;SET THE ERROR NUMBER TO 1501.
6038 031500      012767      012155      152272      MOV      #EM1501,ERRMSG      ;SET THE ERROR MESSAGE ADDR.
6039
6040
6041
6042
6043 ;*
6044 ;*   WAIT FOR THE MASTER RESET BIT TO CLEAR. REPORT THE ERROR IF IT FAILS
6045 ;*   TO CLEAR.
6046 ;*
6047 031506      012701      011610      MOV      #5000.,R1      ;SET THE TIME-OUT VALUE OF 5 SEC.
6048 031512      012702      000040      MOV      #BIT05,R2      ;INDICATE TO TEST BIT 5.
6049 031516      005003      CLR      R3      ;INDICATE TO TEST FOR BIT CLEAR.
6050 031520      016704      150516      MOV      CSRA,R4      ;INDICATE TO TEST THE CSR REG.
6051 031524      004767      166550      JSR      PC,MSLOOP      ;WAIT FOR THE BIT TO CLEAR
6052 031530      103115      BCC     50$      ;JUMP TO REPORT ERROR IF BIT FAILED TO CLEAR.
6053
6054 ;*
6055 ;*   SET THE MASTER RESET AND CSR BIT 4 BITS. AND THEN WAIT 5.5 SECS TO ENSURE
6056 ;*   THAT THE MR BIT DOESN'T CLEAR.
6057 ;*
6058 031532      005267      152232      INC      ERRNBR      ;SET THE ERROR NUMBER TO 1502.
6059 031536      012777      000060      150476      MOV      #60,@CSRA      ;RESET THE BOARD WITH BIT 4 SET.
6060
6061 ;*
6062 ;*   VERIFY THAT CSR BIT 4 IS SET.
6063 ;*
6064 031544      017700      150472      MOV      @CSRA,R0      ;READ THE CSR.
6065 031550      032700      000020      BIT      #BIT04,R0      ;TEST BIT 4.
6066 031554      001477      BEQ     40$      ;EXIT WITH ERROR IF THE BIT IS CLEAR.
6067
6068 ;*
6069 ;*   WAIT 5 SECONDS FOR THE MR BIT TO CLEAR.
6070 ;*
6071 031556      005267      152206      INC      ERRNBR      ;SET THE ERROR NUMBER TO 1503.
        031562      012701      012574      MOV      #5500.,R1      ;SET THE TIME-OUT VALUE OF 5.5 SECS.
        031566      004767      166506      JSR      PC,MSLOOP      ;WAIT FOR THE MASTER RESET BIT TO CLEAR.
        031572      103470      BCS     40$      ;REPORT THE ERROR IF THE MR BIT CLEARED.
    
```

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6072
6073
6074 031574 017705 150442
6075 031600 042705 000020
6076 031604 010577 150432
6077
6078
6079
6080 031610 005267 152154
6081 031614 017705 150422
6082 031620 032705 000020
6083 031624 001053
6084
6085
6086
6087 031626 005267 152136
6088 031632 012701 000764
6089 031636 004767 166436
6090 031642 103044
6091
6092
6093
6094
6095
6096 031644 012767 017126 152122
6097 031652 012701 015241
6098
6099 031656 012704 000006
6100
6101 031662 012767 002742 152100 2$:
6102 031670 017702 150350
6103 031674 100033
6104 031676 010203
6105 031700 042703 177400
6106 031704 012705 000301
6107 031710 040305
6108 031712 001003
6109 031714 004767 170356
6110 031720 000760
6111
6112 031722 020327 000203 4$:
6113 031726 001407
6114
6115
6116
6117 031730 005267 152034
6118 031734
6119 031734 104460
6120
6121
6122
6123 031736 032767 000100 150260
6124 031744 001411
6125
6126 031746 005304
6127 031750 001344

; CLEAR CSR BIT 4 AND VERIFY THAT THE MASTER RESET BIT ALSO CLEARS.
;
MOV @CSRA,R5 ;READ THE CSR.
BIC @BIT04,R5 ;CLEAR BIT 4.
MOV R5,@CSRA ;RESTORE THE CONTENTS OF THE CSR.
;
; *
; VERIFY THAT CSR BIT 4 CLEARED.
;
INC ERRNBR ;SET THE ERROR NUMBER TO 1504.
MOV @CSRA,R5 ;READ THE CSR.
BIT @BIT04,R5 ;TEST BIT 4.
BNE 40$ ;BRANCH AND REPORT ERROR IF SET.
;
; *
; WAIT FOR THE MR BIT TO CLEAR.
;
INC ERRNBR ;SET THE ERROR NUMBER TO 1505.
MOV @500.,R1 ;SET A TIME-OUT OF 1/2 SECS.
JSR PC,MSLOOP ;WAIT FOR THE MR BIT TO CLEAR.
BCC 40$ ;JUMP AND REPORT ERROR,
; MR BIT FAILED TO CLEAR.
;
; *
; READ SIX CHARACTERS FROM THE RXFIFO AND VERIFY THEY ARE SKIP-SELFTEST
; CODES. SAVE ANY BMP CODES FOUND TO BE REPORTED LATER.
;
MOV @ER9008,ERRBLK ;SET UP THE ERROR ROUTINE.
MOV @EM9024,R1 ;SET THE ERROR MESSAGE,
; "IMPROPER SELFTEST CODE FOUND".
MOV @6.,R4 ;SET THE NUMBER OF CHAR'S TO READ.
;
MOV @1506.,ERRNBR ;SET THE ERROR NUMBER TO 1506.
MOV @RBUFA,R2 ;READ A CODE FROM THE RXFIFO.
BPL 50$ ;EXIT WITH ERROR IF THE FIFO IS EMPTY.
MOV R2,R3 ;COPY THE CODE.
BIC @177400,R3 ;CLEAR THE LINE NUMBER AND ERROR FLAGS.
MOV @301,R5 ;SET THE BMP CODE MASK.
BIC R3,R5 ;CHECK IF THE CODE IS A BMP CODE.
BNE 4$ ;AVOID SAVING THE BMP CODE IF IT ISN'T.
JSR PC,SAVBMP ;SAVE THE BMP CODE.
BR 2$ ;AVOID COUNTING THIS CODE, AND BRANCH
; TO READ MORE DATA FROM THE RXFIFO.
; IS THE CODE A SKIP-SELFTEST ?
; BRANCH TO AVOID THE ERROR IF IT IS.
;
; *
; REPORT UNEXPECTED SELFTEST CODE FOUND.
;
INC ERRNBR ;SET THE ERROR NUMBER TO 1507.
ERROR ;REPORT THE ERROR.
;
; *
; IF EXTENDED ERROR REPORTING HAS NOT BEEN ENABLED THEN EXIT
; THE TEST WITH THE TEST FAILURE MESSAGE.
;
BIT @BIT06,OPTION ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
BEQ 60$ ;EXIT THE TEST IF IT HASN'T.
;
DEC R4 ;DECREMENT THE CODE COUNT.
BNE 2$ ;BRANCH AND READ ANOTHER CODE IF NOT ALL
;
TRAP C$ERROR

```



```
6128
6129 031752 000406          BR      60$          ;HAVE BEEN READ.
6130                                     ;OTHERWISE, EXIT THE TEST, THE DUT HAS PASSED
6131                                     ;THIS TEST.
6132
6133
6134 ;*
6135 ; REPORT THE ERROR "CSR BIT 4 BAD" AND EXIT THIS TEST.
6136 ;-
6137 031754 012701 012203 40$:   MOV    #EM1502,R1      ;SET UP THE EXTENDED ERROR MESSAGE AS,
6138                                     ; "CSR BIT 4 BAD".
6139 031760 104460          ERROR      ;REPORT THE ERROR.
6140 031762 000402          BR      60$          ;EXIT THE TEST.          TRAP    C$ERROR
6141 ;*
6142 ; REPORT A NON-RELATED TEST ERROR.
6143 ;-
6144 031764 004767 170502 50$:   JSR    PC,TSABRT      ;REPORT THE ERROR.
6145
6146 031770 005067 150312 60$:   CLR    CTRLCF          ;INDICATE THAT WE ARE NOT WITHIN A TEST.
6147
6148 031774          ENDTST
031774
031774 104401          L10044:   TRAP    C$ETST
```

```

6150 .SBTTL  HARDWARE TEST          - REGWRW -
6151 :** *****
6152 :*          - DEVICE REGISTER WORD ACCESS READ AND WRITE TEST -
6153 :*
6154 :* THIS TEST VERIFIES THAT THE DEVICE REGISTERS CAN BE READ AND WRITTEN
6155 :* CORRECTLY USING WORD ACCESSES.
6156 :*
6157 :-- *****
6158
6159 031776          BGNTST
        031776
6160 031776          SETPRI #PRI05          ;ALLOW THE LTC TO INTERRUPT.          T16::
        031776 012700 000240
        032002 104441
6161          000020
6162 032004 012767 000020 150312          TNUM == TNUM + 1          ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
6163 032012 012767 177777 150266          MOV #TNUM,TSTNUM          ;SET UP THE TEST NUMBER.          (16)
6164 032020 012767 000001 151740          MOV #-1,CTRLCF          ;INDICATE THAT WE ARE WITHIN A TEST.
6165 032026 012767 003101 151734          MOV #1,ERRTYP          ;SET UP DEVICE FATAL INDICATOR IN ERROR TYPE.
6166 032034 012767 012304 151730          MOV #1601.,ERRNBR          ;SET UP ERROR NUMBER IN THE ERROR TABLE.
6167 032042 005067 150414          MOV #EM1604,ERRMSG          ;SET UP ERROR MESSAGE FOR TEST IN ERROR TABLE.
6168 032046 012700 002464          CLR ERSRFR          ;CLEAR THE ERROR SUMMARY FLAGS.
6169 032052 004767 165746          MOV #ERCNTB,R0
6170 032056 005067 150222          JSR PC,CLR16W          ;CLEAR THE ERROR COUNTER TABLE.
        CLR EXOERR          ;CLEAR THE "EXIT ON ERROR" FLAG
6171
6172 ;*
6173 ; RESET THE DUT TO A KNOWN STATE, DO NOT REMOVE THE STATUS CODES FROM THE FIFO.
6174 ; CLEAR TX AND RX INTERRUPT ENABLE BITS IN THE CSR.
6175 ; THIS SUBROUTINE REPORTS ERRORS >>>> 1601 <<<<<.
6176 032062 004767 167270          JSR PC,RESETT          ;RESET THE DHU-11, REPORT ANY ERRORS FOUND.
6177 032066 103402          BCS .+6          ;FATAL RESET ERROR? NO, CONTINUE WITH TEST.
6178 032070 000167 000142          JMP 60$          ;YES, EXIT THE TEST.
6179
6180 ;*
6181 ; VERIFY READ/WRITE CAPABILITY TO INDIRECT ADDRESS FIELD OF CSR
6182 032074 005267 151670          INC ERRNBR          ;SET THE ERROR REPORT NUMBER TO 1602.
6183 032100 012702 000017          MOV #17,R2          ;SET LOOP COUNT.
6184 032104 016704 150132          MOV CSRA,R4          ;GET CSR ADDRESS.
6185 032110 010214          2$: MOV R2,(R4)          ;WRITE COUNT TO CSR.
6186 032112 011401          MOV (R4),R1          ;READ BACK THE CONTENTS OF THE CSR
6187 032114 042701 177760          BIC #177760,R1          ;MASK OUT ALL BUT THE IND.ADR.REG FIELD.
6188 032120 020102          CMP R1,R2          ;CHECK FOR CORRECT DATA WRITTEN/READ.
6189 032122 001412          BEQ 4$          ;IS EXPECTED DATA BAD? NO, SKIP ERROR REPORT.
6190          ;REPORT "BAD BIT(S) IN DEVICE CSR REGISTER FOR LINE 0 (D)."
6191 032124 012767 016416 151642          MOV #ER1601,ERRBLK          ;SELECT THE PROPER ERROR REPORT ROUTINE.
6192 032132 005003          CLR R3          ;SET OFFSET TO 0 TO CAUSE REPORT OF CSR REG.
6193 032134 005005          CLR R5          ;CAUSE REPORT OF LINE 0.
6194 032136          ERROR          ; >>>> ERROR # 1602 <<<<<
        032136 104460          TRAP C$ERROR
6195
6196 ;*
6197 ; EXIT THE TEST IF EXTENDED ERROR REPORTING HAS NOT BEEN REQUESTED.
6198 032140 032767 000100 150056          BIT #BIT06,OPTION          ;HAS EXTENDED ERROR BEEN REQUESTED ?
6199 032146 001433          BEQ 60$          ;EXIT THE TEST IF IT HASN'T.
6200
6201 032150 005302          4$: DEC R2          ;DECREMENT LOOP COUNT/IND.ADD.REG ADDRESS.
6202 032152 002356          BGE 2$          ;LOOP BACK TO TEST NEXT ADDRESS IF NOT DONE.
    
```



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6203
6204
6205
6206
6207
6208 032154 005267 151610
6209 032160 005003
6210 032162 012704 000002
6211 032166 004767 166704
6212
6213
6214
6215
6216 032172 005767 150106
6217 032176 001017
6218
6219
6220
6221
6222
6223 032200 012767 003106 151562
6224 032206 005003
6225 032210 005404
6226 032212 004767 166660
6227
6228
6229
6230
6231 032216 005767 150062
6232 032222 001005
6233
6234
6235
6236
6237 032224 012767 003111 151536
6238 032232 004767 167072
6239 032236 005067 150044
6240 032242
        032242
        032242 104401

; *
; WRITE AND VERIFY 16 DATA PATTERNS IN ALL USED BITS OF ALL REGISTERS ON ALL
; ACTIVE LINES. BEFORE WRITING EACH PATTERN, CLEAR ALL THE BITS.
; REGTST ROUTINE REPORTS ERRORS WITH NUMBERS >>>> ERROR 1603 - 1605 <<<<.
;-
        INC     ERRNBR      ;SET THE ERROR NUMBER TO 1603.
        CLR     R3          ;INDICATE THAT WORD ACCESSES ARE TO BE USED.
        MOV     @2,R4       ;INDICATE R/W ACCESS, CLEAR FIRST.
        JSR     PC,REGTST   ;WRITE AND VERIFY DATA PATTERNS.
; *
; EXIT THE TEST IF AN ERROR HAS BEEN FOUND AND EXTENDED ERROR REPORTING HAS
; NOT BEEN REQUESTED, I.E. EXOERR IS NON-ZERO.
;-
        TST     EXOERR      ;IS THE "EXIT ON ERROR" FLAG SET ?
        BNE     60$         ;EXIT IF IT IS.
; *
; WRITE AND VERIFY 16 DATA PATTERNS IN ALL USED BITS OF ALL REGISTERS ON ALL
; ACTIVE LINES. BEFORE WRITING EACH PATTERN, SET ALL THE BITS.
; REGTST ROUTINE REPORTS ERRORS WITH NUMBERS >>>> ERROR 1606 - 1608 <<<<.
;-
        MOV     @1606.,ERRNBR ;SET UP ERROR NUMBER FOR REGTST ROUTINE.
        CLR     R3          ;INDICATE THAT WORD ACCESSES ARE TO BE USED.
        NEG     R4          ;INDICATE R/W ACCESS, SET FIRST.
        JSR     PC,REGTST   ;WRITE AND VERIFY DATA PATTERNS.
; *
; EXIT THE TEST IF AN ERROR HAS BEEN FOUND AND EXTENDED ERROR REPORTING HAS
; NOT BEEN REQUESTED, I.E. EXOERR IS NON-ZERO.
;-
        TST     EXOERR      ;IS THE "EXIT ON ERROR" FLAG SET ?
        BNE     60$         ;EXIT IF IT IS.
; *
; PRINT ERROR SUMMARY REPORTS IF NECESSARY.
; THE FOLLOWING ROUTINE REPORTS ERRORS WITH NUMBER >>>> ERROR # 1609 <<<<
;-
        MOV     @1609.,ERRNBR ;SET UP ERROR NUMBER FOR NEXT RTN.
        JSR     PC,REPSMR   ;REPORT ERROR SUMMARY IF NECESSARY.
60$:    CLR     CTRLCF      ;INDICATE THAT WE COMPLETED THE TEST.
        ENDTST

                                L10045:
                                TRAP   C$ETST
    
```

6242
 6243
 6244
 6245
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 6247
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 6249
 6250
 6251 032244
 032244
 6252 032244 012700 000240
 032244 104441
 032250 000021
 6253
 6254 032252 012767 000021 150044
 6255 032260 012767 177777 150020
 6256 032266 012767 000001 151472
 6257 032274 012767 003245 151466
 6258 032302 012767 012360 151462
 6259 032310 005067 150146
 6260 032314 012700 002464
 6261 032320 004767 165500
 6262 032324 005067 147754
 6263
 6264
 6265
 6266
 6267
 6268 032330 004767 167022
 6269 032334 103402
 6270 032336 000167 000064
 6271
 6272
 6273
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 6279
 6280
 6281
 6282 032342 012767 003247 151420
 6283 032350 005003
 6284 032352 012704 000001
 6285 032356 004767 166514
 6286
 6287
 6288
 6289
 6290 032362 005767 147716
 6291 032366 001017
 6292
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 6294
 6295

```

.SBTTL  HARDWARE TEST          - REGWRM -
; * *****
; *   - DEVICE REGISTER WORD ACCESS READ/MODIFY/WRITE TEST -
; *
; * THIS TEST VERIFIES THAT THE DEVICE REGISTERS CAN BE WRITTEN CORRECTLY
; * USING WORD READ/MODIFY/WRITE ACCESSES.
; *
; - *****

        BGNTST
                SETPRI #PRI05          ;ALLOW THE LTC TO INTERRUPT.
                                T17::
                                MOV     #PRI05,R0
                                TRAP   C$SPRI

        TNUM == TNUM + 1          ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
        MOV     #TNUM,TSTNUM      ;SET UP THE TEST NUMBER. (17)
        MOV     #-1,CTRLCF        ;INDICATE THAT WE ARE WITHIN A TEST.
        MOV     #1,ERRTYP         ;SET UP DEVICE FATAL INDICATOR IN ERROR TYPE.
        MOV     #1701,ERRNBR      ;SET UP ERROR NUMBER IN THE ERROR TABLE.
        MOV     #EM1701,ERRMSG    ;SET UP ERROR MESSAGE FOR TEST IN ERROR TABLE.
        CLR     ERSMRF            ;CLEAR THE ERROR SUMMARY FLAGS.
        MOV     #ERCNTB,R0
        JSR     PC,CLR16W         ;CLEAR THE ERROR COUNTER TABLE.
        CLR     EXOERR            ;CLEAR THE "EXIT ON ERROR" FLAG

; *
; * RESET THE DUT TO A KNOWN STATE, DO NOT REMOVE THE STATUS CODES FROM THE FIFO.
; * CLEAR TX AND RX INTERRUPT ENABLE BITS IN THE CSR.
; * THIS SUBROUTINE REPORTS ERRORS >>>> 1701 <<<<<.
; -
        JSR     PC,RESETT        ;RESET THE DHU-11. REPORT ANY ERRORS FOUND.
        BCS     .+6              ;FATAL RESET ERROR? NO. CONTINUE WITH TEST.
        JMP     60$              ;YES. EXIT THE TEST.

; *
; * THE READ/MODIFY/WRITE CAPABILITY TO INDIRECT ADDRESS FIELD OF CSR IS
; * NOT TESTED THIS THIS FORM OF ACCESS IS ILLEGAL.
; -

; *
; * WRITE AND VERIFY 16 DATA PATTERNS IN ALL USED BITS OF ALL REGISTERS ON ALL
; * ACTIVE LINES USING R/M/W. BEFORE WRITING EACH PATTERN, CLEAR ALL THE BITS.
; * REGTST ROUTINE REPORTS ERRORS WITH NUMBERS >>>> ERROR 1703 - 1705 <<<<<.
; -
        MOV     #1703,ERRNBR     ;SET THE ERROR NUMBER TO 1703.
        CLR     R3              ;INDICATE THAT WORD ACCESSES ARE TO BE USED.
        MOV     #1,R4           ;INDICATE R/M/W ACCESS, CLEAR FIRST.
        JSR     PC,REGTST       ;WRITE AND VERIFY DATA PATTERNS.

; *
; * EXIT THE TEST IF AN ERROR HAS BEEN FOUND AND EXTENDED ERROR REPORTING HAS
; * NOT BEEN REQUESTED, I.E. EXOERR IS NON-ZERO.
; -
        TST     EXOERR          ;IS THE "EXIT ON ERROR" FLAG SET ?
        BNE     60$            ;EXIT IF IT IS.

; *
; * WRITE AND VERIFY 16 DATA PATTERNS IN ALL USED BITS OF ALL REGISTERS ON ALL
; * ACTIVE LINES USING R/M/W. BEFORE WRITING EACH PATTERN, SET ALL THE BITS.
; * REGTST ROUTINE REPORTS ERRORS WITH NUMBERS >>>> ERROR 1706 - 1708 <<<<<.
    
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6370 032610 005302
6371 032612 002356
6372
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6378 032614 005267 151150
6379 032620 012703 177777
6380 032624 012704 000002
6381 032630 004767 166242
6382
6383
6384
6385
6386 032634 005767 147444
6387 032640 001041
6388
6389
6390
6391
6392
6393
6394
6395 032642 012767 003416 151120
6396 032650 005403
6397 032652 004767 166220
6398
6399
6400
6401
6402 032656 005767 147422
6403 032662 001030
6404
6405
6406
6407
6408
6409
6410 032664 012767 003421 151076
6411 032672 005403
6412 032674 005404
6413 032676 004767 166174
6414
6415
6416
6417
6418 032702 005767 147376
6419 032706 001016
6420
6421
6422
6423
6424
6425
6426 032710 012767 003424 151052
    
```

```

4$:   DEC   R2           ;DECREMENT LOOP COUNT/IND.ADD.REG ADDRESS.
      BGE   2$           ;LOOP BACK TO TEST NEXT ADDRESS IF NOT DONE.
;+
; WRITE AND VERIFY 16 DATA PATTERNS IN ALL USED BITS OF ALL LOWER BYTES OF ALL
;   REGISTERS ON ALL ACTIVE LINES. USE READ/WRITE ACCESSES. BEFORE WRITING
;   EACH PATTERN, CLEAR ALL THE USED BITS OF ALL ACTIVE REGISTERS.
;   REGTST ROUTINE REPORTS ERRORS WITH NUMBERS >>>> ERROR 1803 - 1805 <<<<.
;-
      INC   ERRNBR       ;SET THE ERROR NUMBER TO 1803.
      MOV   #-1,R3      ;INDICATE THAT LO BYTE ACCESSES ARE TO BE USED.
      MOV   #2,R4      ;INDICATE R/W ACCESS, CLEAR FIRST.
      JSR   PC,REGTST   ;WRITE AND VERIFY DATA PATTERNS.
;+
; EXIT THE TEST IF AN ERROR HAS BEEN FOUND AND EXTENDED ERROR REPORTING HAS
;   NOT BEEN REQUESTED, I.E. EXOERR IS NON-ZERO.
;-
      TST   EXOERR      ;IS THE "EXIT ON ERROR" FLAG SET ?
      BNE   60$         ;EXIT IF IT IS.
;+
; WRITE AND VERIFY 16 DATA PATTERNS IN ALL USED BITS OF ALL HIGH BYTES OF ALL
;   REGISTERS ON ALL ACTIVE LINES. USE READ/WRITE ACCESSES. BEFORE WRITING
;   EACH PATTERN, CLEAR ALL THE USED BITS OF ALL ACTIVE REGISTERS.
;   REGTST ROUTINE REPORTS ERRORS WITH NUMBERS >>>> ERROR 1806 - 1808 <<<<.
;-
      MOV   #1806.,ERRNBR ;SET UP ERROR NUMBER FOR REGTST ROUTINE.
      NEG   R3           ;INDICATE THAT HI BYTE ACCESSES ARE TO BE USED.
      JSR   PC,REGTST   ;WRITE AND VERIFY DATA PATTERNS.
;+
; EXIT THE TEST IF AN ERROR HAS BEEN FOUND AND EXTENDED ERROR REPORTING HAS
;   NOT BEEN REQUESTED, I.E. EXOERR IS NON-ZERO.
;-
      TST   EXOERR      ;IS THE "EXIT ON ERROR" FLAG SET ?
      BNE   60$         ;EXIT IF IT IS.
;+
; WRITE AND VERIFY 16 DATA PATTERNS IN ALL USED BITS OF ALL LOWER BYTES OF ALL
;   REGISTERS ON ALL ACTIVE LINES. USE READ/WRITE ACCESSES. BEFORE WRITING
;   EACH PATTERN, SET ALL THE USED BITS OF ALL ACTIVE REGISTERS.
;   REGTST ROUTINE REPORTS ERRORS WITH NUMBERS >>>> ERROR 1809 - 1811 <<<<.
;-
      MOV   #1809.,ERRNBR ;SET UP ERROR NUMBER FOR REGTST ROUTINE.
      NEG   R3           ;INDICATE THAT LO BYTE ACCESSES ARE TO BE USED.
      NEG   R4           ;INDICATE R/W ACCESS, SET FIRST.
      JSR   PC,REGTST   ;WRITE AND VERIFY DATA PATTERNS.
;+
; EXIT THE TEST IF AN ERROR HAS BEEN FOUND AND EXTENDED ERROR REPORTING HAS
;   NOT BEEN REQUESTED, I.E. EXOERR IS NON-ZERO.
;-
      TST   EXOERR      ;IS THE "EXIT ON ERROR" FLAG SET ?
      BNE   60$         ;EXIT IF IT IS.
;+
; WRITE AND VERIFY 16 DATA PATTERNS IN ALL USED BITS OF ALL HIGH BYTES OF ALL
;   REGISTERS ON ALL ACTIVE LINES. USE READ/WRITE ACCESSES. BEFORE WRITING
;   EACH PATTERN, SET ALL THE USED BITS OF ALL ACTIVE REGISTERS.
;   REGTST ROUTINE REPORTS ERRORS WITH NUMBERS >>>> ERROR 1812 - 1814 <<<<.
;-
      MOV   #1812.,ERRNBR ;SET UP ERROR NUMBER FOR REGTST ROUTINE.
    
```

```

6427 032716 005403
6428 032720 004767 166152
6429
6430
6431
6432
6433 032724 005767 147354
6434 032730 001005
6435
6436
6437
6438
6439 032732 012767 003427 151030
6440 032740 004767 166364
6441 032744 005067 147336
6442 032750
        032750
        032750 104401

        NEG      R3
        JSR      PC,REGTST      ;INDICATE THAT HI BYTE ACCESSES ARE TO BE USED.
                                ;WRITE AND VERIFY DATA PATTERNS.
;+
; EXIT THE TEST IF AN ERROR HAS BEEN FOUND AND EXTENDED ERROR REPORTING HAS
; NOT BEEN REQUESTED, I.E. EXOERR IS NON-ZERO.
;-
        TST      EXOERR
        BNE      60$           ;IS THE "EXIT ON ERROR" FLAG SET ?
                                ;EXIT IF IT IS.
;+
; PRINT ERROR SUMMARY REPORTS IF NECESSARY.
; THE FOLLOWING ROUTINE REPORTS ERRORS WITH NUMBER >>>> ERROR # 1815 <<<<
;-
        MOV      #1815.,ERRNBR ;SET UP ERROR NUMBER FOR NEXT RTN.
        JSR      PC,REPSMR    ;REPORT ERROR SUMMARY IF NECESSARY.
60$:    CLR      CTRLCF      ;INDICATE THAT WE COMPLETED THE TEST.
        ENDTST

                                L10047:
                                TRAP   C$ETST
    
```



```

6444 .SBTTL HARDWARE TEST - REGBRM -
6445 ;** *****
6446 ;* - DEVICE REGISTER BYTE ACCESS READ/MODIFY/WRITE TEST -
6447 ;*
6448 ;* THIS TEST VERIFIES THAT THE DEVICE REGISTERS CAN BE READ AND WRITTEN
6449 ;* CORRECTLY USING BYTE ACCESSES IN READ/MODIFY/WRITE MODE.
6450 ;*
6451 ;-- *****
6452
6453 032752 BGNTST
        032752
6454 032752          SETPRI #PRI05          ;ALLOW THE LTC TO INTERRUPT.
        032752 012700 000240
        032756 104441
6455          000023          TNUM == TNUM + 1          ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
6456 032760 012767 000023 147336          MOV #TNUM,TSTNUM          ;SET UP THE TEST NUMBER. (19)
6457 032766 012767 177777 147312          MOV #-1,CTRLCF          ;INDICATE THAT WE ARE WITHIN A TEST.
6458 032774 012767 000001 150764          MOV #1,ERRTYP          ;SET UP DEVICE FATAL INDICATOR IN ERROR TYPE.
6459 033002 012767 003555 150760          MOV #1901.,ERRNBR          ;SET UP ERROR NUMBER IN THE ERROR TABLE.
6460 033010 012767 012517 150754          MOV #EM1901.,ERRMSG          ;SET UP ERROR MESSAGE FOR TEST IN ERROR TABLE.
6461 033016 005067 147440          CLR ERSRFR          ;CLEAR THE ERROR SUMMARY FLAGS.
6462 033022 012700 002464          MOV #ERCNTB,R0
6463 033026 004767 164772          JSR PC,CLR16W          ;CLEAR THE ERROR COUNTER TABLE.
6464 033032 005067 147246          CLR EXOERR          ;CLEAR THE "EXIT ON ERROR" FLAG.
6465
6466 ;*
6467 ; RESET THE DUT TO A KNOWN STATE, DO NOT REMOVE THE STATUS CODES FROM THE FIFO.
6468 ; CLEAR TX AND RX INTERRUPT ENABLE BITS IN THE CSR.
6469 ; THIS SUBROUTINE REPORTS ERRORS >>>> 1901 <<<<<.
6470 033036 004767 166314          JSR PC,RESET          ;RESET THE DHU-11, REPORT ANY ERRORS FOUND.
6471 033042 103402          BCS .+6          ;FATAL RESET ERROR? NO, CONTINUE WITH TEST.
6472 033044 000167 000136          JMP 60$          ;YES, EXIT THE TEST.
6473 033050 012767 003557 150712          MOV #1903.,ERRNBR          ;SET THE ERROR REPORT NUMBER TO 1903.
6474
6475 ;*
6476 ; THE READ/MODIFY/WRITE CAPABILITY TO INDIRECT ADDRESS FIELD OF CSR IS NOT
6477 ; TESTED SINCE THIS IS AN ILLEGAL FORM OF ACCESS TO THIS REGISTER.
6478 ;
6479
6480 ;*
6481 ; WRITE AND VERIFY 16 DATA PATTERNS IN ALL USED BITS OF ALL LOWER BYTES OF ALL
6482 ; REGISTERS ON ALL ACTIVE LINES. USE READ/MODIFY/WRITE ACCESSES. BEFORE
6483 ; WRITING EACH PATTERN, CLEAR ALL THE USED BITS OF ALL ACTIVE REGISTERS.
6484 ; REGTST ROUTINE REPORTS ERRORS WITH NUMBERS >>>> ERROR 1903 - 1905 <<<<<.
6485 ;
6486 033056 005267 150706          INC ERRNBR          ;SET THE ERROR NUMBER TO 1903.
6487 033062 012703 177777          MOV #-1,R3          ;INDICATE THAT LO BYTE ACCESSES ARE TO BE USED.
6488 033066 012704 000001          MOV #1,R4          ;INDICATE R/M/W ACCESS, CLEAR FIRST.
6489 033072 004767 166000          JSR PC,REGTST          ;WRITE AND VERIFY DATA PATTERNS.
6490
6491 ;*
6492 ; EXIT THE TEST IF AN ERROR HAS BEEN FOUND AND EXTENDED ERROR REPORTING HAS
6493 ; NOT BEEN REQUESTED, I.E. EXOERR IS NON-ZERO.
6494 033076 005767 147202          TST EXOERR          ;IS THE "EXIT ON ERROR" FLAG SET ?
6495 033102 001041          BNE 60$          ;EXIT IF IT IS.
6496
6497 ;*
        ; WRITE AND VERIFY 16 DATA PATTERNS IN ALL USED BITS OF ALL HIGH BYTES OF ALL
    
```

```

6498 ; REGISTERS ON ALL ACTIVE LINES. USE READ/MODIFY/WRITE ACCESSES. BEFORE
6499 ; WRITING EACH PATTERN, CLEAR ALL THE USED BITS OF ALL ACTIVE REGISTERS.
6500 ; REGTST ROUTINE REPORTS ERRORS WITH NUMBERS >>>> ERROR 1906 - 1908 <<<<<.
6501 ;-
6502 033104 012767 003562 150656      MOV    #1906.,ERRNBR ;SET UP ERROR NUMBER FOR REGTST ROUTINE.
6503 033112 005403                    NEG    R3            ;INDICATE THAT HI BYTE ACCESSES ARE TO BE USED.
6504 033114 004767 165756            JSR    PC,REGTST    ;WRITE AND VERIFY DATA PATTERNS.
6505 ;+
6506 ; EXIT THE TEST IF AN ERROR HAS BEEN FOUND AND EXTENDED ERROR REPORTING HAS
6507 ; NOT BEEN REQUESTED, I.E. EXOERR IS NON-ZERO.
6508 ;-
6509 033120 005767 147160            TST    EXOERR        ;IS THE "EXIT ON ERROR" FLAG SET ?
6510 033124 001030                    BNE    60$          ;EXIT IF IT IS.
6511 ;+
6512 ; WRITE AND VERIFY 16 DATA PATTERNS IN ALL USED BITS OF ALL LOWER BYTES OF ALL
6513 ; REGISTERS ON ALL ACTIVE LINES. USE READ/MODIFY/WRITE ACCESSES. BEFORE
6514 ; WRITING EACH PATTERN, SET ALL THE USED BITS OF ALL ACTIVE REGISTERS.
6515 ; REGTST ROUTINE REPORTS ERRORS WITH NUMBERS >>>> ERROR 1909 - 1911 <<<<<.
6516 ;-
6517 033126 012767 003565 150634      MOV    #1909.,ERRNBR ;SET UP ERROR NUMBER FOR REGTST ROUTINE.
6518 033134 005403                    NEG    R3            ;INDICATE THAT LO BYTE ACCESSES ARE TO BE USED.
6519 033136 005404                    NEG    R4            ;INDICATE R/M/W ACCESS, SET FIRST.
6520 033140 004767 165732            JSR    PC,REGTST    ;WRITE AND VERIFY DATA PATTERNS.
6521 ;+
6522 ; EXIT THE TEST IF AN ERROR HAS BEEN FOUND AND EXTENDED ERROR REPORTING HAS
6523 ; NOT BEEN REQUESTED, I.E. EXOERR IS NON-ZERO.
6524 ;-
6525 033144 005767 147134            TST    EXOERR        ;IS THE "EXIT ON ERROR" FLAG SET ?
6526 033150 001016                    BNE    60$          ;EXIT IF IT IS.
6527 ;+
6528 ; WRITE AND VERIFY 16 DATA PATTERNS IN ALL USED BITS OF ALL HIGH BYTES OF ALL
6529 ; REGISTERS ON ALL ACTIVE LINES. USE READ/MODIFY/WRITE ACCESSES. BEFORE
6530 ; WRITING EACH PATTERN, SET ALL THE USED BITS OF ALL ACTIVE REGISTERS.
6531 ; REGTST ROUTINE REPORTS ERRORS WITH NUMBERS >>>> ERROR 1912 - 1914 <<<<<.
6532 ;-
6533 033152 012767 003570 150610      MOV    #1912.,ERRNBR ;SET UP ERROR NUMBER FOR REGTST ROUTINE.
6534 033160 005403                    NEG    R3            ;INDICATE THAT HI BYTE ACCESSES ARE TO BE USED.
6535 033162 004767 165710            JSR    PC,REGTST    ;WRITE AND VERIFY DATA PATTERNS.
6536 ;+
6537 ; EXIT THE TEST IF AN ERROR HAS BEEN FOUND AND EXTENDED ERROR REPORTING HAS
6538 ; NOT BEEN REQUESTED, I.E. EXOERR IS NON-ZERO.
6539 ;-
6540 033166 005767 147112            TST    EXOERR        ;IS THE "EXIT ON ERROR" FLAG SET ?
6541 033172 001005                    BNE    60$          ;EXIT IF IT IS.
6542 ;+
6543 ; PRINT ERROR SUMMARY REPORTS IF NECESSARY.
6544 ; THE FOLLOWING ROUTINE REPORTS ERRORS WITH NUMBER >>>> ERROR # 1915 <<<<<
6545 ;-
6546 033174 012767 003573 150566      MOV    #1915.,ERRNBR ;SET UP ERROR NUMBER FOR NEXT RTN.
6547 033202 004767 166122            JSR    PC,REPSMR    ;REPORT ERROR SUMMARY IF NECESSARY.
6548 033206 005067 147074            CLR    CTRLCF       ;INDICATE THAT WE COMPLETED THE TEST.
6549 033212
        033212
        033212 104401
    
```

L10050: TRAP C\$ETST


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6587 .SBTTL HARDWARE TEST - TXENBI-
6588 ;* *****
6589 ;* - TX_ENABLE (INACTIVE) TEST -
6590 ;* THIS TEST VERIFIES THAT WHEN THE LINE UNDER TEST'S TX_ENABLE BIT IS
6591 ;* CLEAR, TRANSMISSION WILL NOT TAKE PLACE ON THAT LINE.
6592 ;* THIS TEST IS PERFORMED IN INTERNAL LOOPBACK, AND ON ALL ACTIVE LINES.
6593 ;*
6594 ;* *****
6595 ;*
6596 033330 BGNTST
6597 033330
033330 012700 000240
033334 104441
6598 000025
6599 033336 012767 000025 146760 TNUM == TNUM + 1 ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
6600 033344 012767 177777 146734 MOV #TNUM,TSTNUM ;SET UP THE TEST NUMBER. (23)
6601 033352 012767 000001 150406 MOV #-1,CTRLCF ;INDICATE THAT WE ARE IN A TEST.
6602 033360 012767 004375 150402 MOV #1,ERRTP ;SET ERROR TYPE AS FATAL IN ERROR TABLE.
6603 033366 012767 012725 150376 MOV #2301,ERRNBR ;SET THE FIRST ERROR NUMBER IN ERROR TABLE.
6604 033374 012767 017222 150376 MOV #EM2301,ERRMSG ;SET ERROR MESSAGE ADDRESS IN ERRTABL.
6605 MOV #ER9101,ERRBLK ;SELECT THE CORRECT ERROR REPORTING ROUTINE.
6606 ;*
6607 ; RESET THE DUT TO A KNOWN STATE, REMOVE THE STATUS CODES FROM THE FIFO.
6608 ; CLEAR TX AND RX INTERRUPT ENABLE BITS IN THE CSR.
6609 ; THIS SUBROUTINE REPORTS ERROR >>>> 2301 <<<<<.
6610 033402 004767 164374
6611 033406 103114
6612 JSR PC,CLNRST ;RESET THE DHU-11, REPORT ANY ERRORS FOUND.
6613 BCC 60$ ;RESET FAILURE?, ABORT THIS TEST.
6614 ;*
6615 ; SET INTERNAL LOOPBACK ON ALL ACTIVE LINES.
6616 ; SET LPR ON ALL LINES TO 38.4K BAUD, 8 BITS PER CHARACTER, ODD PARITY,
6617 ; 2 STOP BITS.
6618 ; ENABLE TRANSMITTERS ON ALL LINES.
6619 ;*
6619 033410 016705 146614 MOV ACTLNS,R5 ;PASS THE ACTIVE LINE BIT MAP.
6620 033414 012700 000200 MOV #200,R0 ;PASS THE LNCTR CONTENTS.
6621 033420 004767 170126 JSR PC,WTMLNC ;INITIALISE THE LNCTR REGISTERS.
6622 033424 012700 177670 MOV #177670,R0 ;PASS THE LPR CONTENTS.
6623 033430 004767 170146 JSR PC,WTMLPR ;INITIALISE THE LPR REGISTERS ON ALL LINES.
6624 033434 012704 000012 MOV #10,R4 ;PASS DELAY TIME OF 10 MILLI-SECONDS.
6625 033440 004767 164460 JSR PC,DELAY ;WAIT FOR LNCTR AND LPR REGS TO BE UPDATED.
6626 033444 012705 177777 MOV #MAPLNS,R5 ;PASS THE BIT MAP CORRESPONDING TO ALL LINES.
6627 033450 004767 167224 JSR PC,TXENBL ;ENABLE TRANSMITTERS ON ALL LINES.
6628 ;*
6629 ; TEST ALL ACTIVE LINES INDIVIDUALLY.
6630 ; DISABLE TRANSMISSION ON EACH ACTIVE LINE.
6631 ;*
6631 033454 012703 000001
6632 033460 005004
6633 033462 012767 004376 150300 2$: MOV #1,R3 ;SET UP THE LINE BIT MAP FOR CHANNEL 0.
6634 033470 030367 146534 CLR R4 ;CLEAR THE LINE NUMBER COUNTER.
6635 033474 001453 MOV #2302,ERRNBR ;SET THE ERROR NUMBER TO 2302.
BIT R3,ACTLNS ;CHECK IF THE LINE IS ACTIVE.
BEQ 6$ ;SKIP TESTING THIS LINE IF IT IS INACTIVE.
6636 ;*
6637 ; CLEAR THE TX_ENABLE BIT IN TBUFFAD2 REGISTER.
6638 ; SELECT THE LINE UNDER TEST.
6639 ; VERIFY IT IS CLEAR, REPORT ERROR IF SET.
6640 ;*
    
```



```

6641 033476 010305
6642 033500 004767 167100
6643 033504 010477 146532
6644 033510 005777 146542
6645 033514 100433
6646
6647
6648
6649
6650
6651 033516 012767 004377 150244
6652 033524 112777 000012 146516
6653 033532 012701 170003
6654 033536 016702 146500
6655 033542 004767 167522
6656 033546 103416
6657
6658
6659
6660 033550 005267 150214
6661 033554 012701 070012
6662 033560 016702 146456
6663 033564 004767 167500
6664 033570 103405
6665 033572 005267 150172
6666 033576 017702 146442
6667 033602 100010
6668
6669 033604 010401
6670 033606 012702 012772
6671
6672 033612
033612 104460
6673
6674 033614 032767 000100 146402
6675 033622 001406
6676
6677
6678
6679
6680
6681 033624 000241
6682 033626 006103
6683 033630 005204
6684 033632 020427 000020
6685 033636 002711
6686
6687 033640 005067 146442
6688 033644
033644
033644 104401

```

```

MOV R3,R5 ;PASS THE BIT MAP OF THE LINE UNDER TEST.
JSR PC,TXDSBL ;DISABLE TRANSMISSION ON THE LINE UNDER TEST.
MOV R4,@CSRA ;SELECT THE LINE CURRENTLY UNDER TEST.
TST @TXAD2A ;VERIFY THE TX_ENABLE BIT IS SET.
BMI 4$ ;GO REPORT ERROR IF TX_ENABLE BIT SET.
;*
; WRITE DATA BYTE (ASCII <LF>) TO THE OUTPUT FIFO.
; WAIT FOR A TX_ACTION TO BE RETURNED, REPORT ERROR IF A TX_ACTION
; IS FOUND BEFORE TIME-OUT OCCURS.
;-
MOV @2303.,ERRNBR ;SET ERROR NUMBER TO 2303.
MOVB @12,@FDATA ;WRITE THE DATA BYTE TO THE DUT'S OUTPUT FIFO.
MOV @170003,R1 ;TEST BIT 15, TIMEOUT OF 3 MILLI SECS.
MOV CSRA,R2 ;PASS THE ADDRESS OF THE REGISTER TO TEST.
JSR PC,WAIBIS ;WAIT FOR TX_ACTION TO COME BACK.
BCS 4$ ;GO REPORT ERROR IF A TX-ACTION FOUND.
;*
; WAIT FOR THE DATA TO APPEAR IN THE FIFO, REPORT ERROR IF DATA FOUND.
;-
INC ERRNBR ;SET ERROR NUMBER TO 2304.
MOV @70012,R1 ;TEST BIT 7, TIMEOUT OF 10 MILLI SECS.
MOV CSRA,R2 ;PASS THE ADDRESS OF THE REGISTER TO TEST.
JSR PC,WAIBIS ;WAIT FOR RX_DATA_AVAILABLE TO SET.
BCS 4$ ;REPORT ERROR IF DATA RECEIVED IN THE FIFO.
INC ERRNBR ;SET ERROR NUMBER TO 2305.
MOV @RBUFA,R2 ;READ THE DATA FROM THE FIFO.
BPL 6$ ;SKIP ERROR REPORT IF DATA ISN'T THERE.
4$: MOV R4,R1 ;PASS THE NUMBER OF CURRENT LINE UNDER TEST.
MOV @EM2302,R2 ;PASS THE MESSAGE TO BE REPORTED.
ERROR ;"TX_ENABLE BIT BAD ON LINE: NN".
; >>>> ERROR <<<<<.
TRAP C$ERROR
BIT @BIT06,OPTION ;EXIT THE TEST, WITH THE TEST FAILED MESSAGE.
BEQ 60$ ;IF EXTENDED ERROR REPORTING HAS NOT BEEN
;REQUESTED.
;*
; VERIFY ALL ACTIVE LINES HAVE BEEN TESTED.
;-
6$: CLC ;CLEAR THE CARRY BIT PRIOR TO ROTATION.
ROL R3 ;SHIFT THE BIT MAP FOR THE NEXT LINE.
INC R4 ;INCREMENT THE LINE NUMBER COUNTER.
CMP R4,@NUMLNS ;HAVE ALL THE LINES BEEN TESTED?.
BLT 2$ ;NO; BRANCH TO TEST THE NEXT LINE.
60$: CLR CTRLCF ;INDICATE THAT WE ARE NOT WITHIN A TEST.
ENDTST
L10052: TRAP C$ETST

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6690 .SBTTL HARDWARE TEST - TXENBA-
6691 ;** *****
6692 ;* - TX_ENABLE (ACTIVE) TEST -
6693 ;* THIS TEST VERIFIES THAT WHEN THE TX_ENABLE BIT IS SET IN THE APPROPRIATE
6694 ;* LINE REGISTER, TRANSMISSION WILL TAKE PLACE ON THAT LINE.
6695 ;* THIS TEST IS PERFORMED IN INTERNAL LOOPBACK, AND ON ALL ACTIVE LINES.
6696 ;*
6697 ;-- *****
6698
6699 033646 BGNTST
033646
6700 033646 SETPRI #PRIOS ;ALLOW LTC INTERRUPTS. T22::
033646 012700 000240
033652 104441
6701 000026
6702 033654 012767 000026 146442 TNUM == TNUM + 1 ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
6703 033662 012767 177777 146416 MOV #TNUM,TSTNUM ;SET UP THE TEST NUMBER. (24)
6704 033670 012767 000001 150070 MOV #-1,CTRLCF ;INDICATE THAT WE ARE IN A TEST.
6705 033676 012767 004541 150064 MOV #1,ERRTYP ;SET ERROR TYPE AS FATAL IN ERROR TABLE.
6706 033704 012767 013030 150060 MOV #2401.,ERRNBR ;SET THE FIRST ERROR NUMBER IN ERROR TABLE.
6707 033712 012767 017222 150054 MOV #EM2401,ERRMSG ;SET ERROR MESSAGE ADDRESS IN ERRTABL.
6708 MOV #ER9101,ERRBLK ;SELECT THE CORRECT ERROR REPORTING ROUTINE.
6709 ;*
6710 ; RESET THE DUT TO A KNOWN STATE, REMOVE THE STATUS CODES FROM THE FIFO.
6711 ; CLEAR TX AND RX INTERRUPT ENABLE BITS IN THE CSR.
6712 ; THIS SUBROUTINE REPORTS ERROR >>>> 2401 <<<<<.
6713 033720 004767 164056
6714 033724 103133 JSR PC,CLNRST ;RESET THE DHU-11, REPORT ANY ERRORS FOUND.
6715 BCC 60$ ;RESET FAILURE?, ABORT THIS TEST.
6716 ;*
6717 ; SET INTERNAL LOOPBACK ON ALL ACTIVE LINES.
6718 ; SET LPR ON ALL LINES TO 38.4K BAUD, 8 BITS PER CHARACTER, ODD PARITY,
6719 ; 2 STOP BITS.
6720 ; DISABLE TRANSMITTERS ON ALL LINES.
6721 033726 016705 146276
6722 033732 012700 000200
6723 033736 004767 167610
6724 033742 012700 177670
6725 033746 004767 167630
6726 033752 012704 000012
6727 033756 004767 164142
6728 033762 012705 177777
6729 033766 004767 166612
6730
6731 ;*
6732 ; TEST ALL ACTIVE LINES INDIVIDUALLY.
6733 ; ENABLE TRANSMISSION ON EACH ACTIVE LINE.
6734 033772 012703 000001
6735 033776 005004
6736 034000 012767 004542 147762 2$:
6737 034006 030367 146216
6738 034012 001467
6739
6740 ;*
6741 ; SELECT THE LINE UNDER TEST.
6742 ; SET THE TX_ENABLE BIT IN TBUFFAD2 REGISTER.
6743 ; VERIFY IT IS SET, REPORT ERROR IF CLEAR.

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```

6744 034014 010305          MOV    R3,R5          ;PASS THE BIT MAP OF THE LINE UNDER TEST.
6745 034016 004767 166656  JSR    PC,TXENBL     ;ENABLE TRANSMISSION ON THE LINE UNDER TEST.
6746 034022 012705 000012    MOV    #10.,R5       ;SET TXCHAR/LOOP COUNT TO 10.
6747 034026 010477 146210    MOV    R4,@CSRA     ;SELECT THE LINE CURRENTLY UNDER TEST.
6748 034032 005777 146220    TST   @TXAD2A       ;VERIFY THE TX_ENABLE BIT IS SET.
6749 034036 100045          BPL    6$           ;GO REPORT ERROR IF TX_ENABLE BIT CLEAR.
6750
6751          ;*
6752          ; WRITE DATA BYTE (ASCII <LF>) TO OUTPUT FIFO.
6753          ; WAIT FOR A TX_ACTION TO BE RETURNED, REPORT ERROR IF NO TX_ACTION
6754          ; FOUND BEFORE TIME-OUT OCCURS.
6755 034040 012767 004543 147722 4$:  MOV    #2403.,ERRNBR ;SET ERROR NUMBER TO 2403.
6756 034046 112777 000012 146174  MOVB   #12,@FDATA   ;WRITE THE DATA BYTE TO THE DUT'S OUTPUT FIFO.
6757 034054 012701 170004    MOV    #170004,R1   ;TEST BIT 15, TIMEOUT OF 4 MILLI SECS.
6758 034060 016702 146156    MOV    CSRA,R2      ;PASS THE ADDRESS OF THE REGISTER TO TEST.
6759 034064 004767 167200    JSR    PC,WAIBIS    ;WAIT FOR TX_ACTION TO COME BACK.
6760 034070 103030          BCC    6$           ;GO REPORT ERROR IF NO TX-ACTION FOUND.
6761
6762          ;*
6763          ; WAIT FOR THE DATA TO APPEAR IN THE FIFO, REPORT ERROR IF TIME-OUT.
6764 034072 005267 147672    INC    ERRNBR       ;SET ERROR NUMBER TO 2404.
6765 034076 012701 070012    MOV    #70012,R1   ;TEST BIT 7, TIMEOUT OF 10 MILLI SECS.
6766 034102 016702 146134    MOV    CSRA,R2      ;PASS THE ADDRESS OF THE REGISTER TO TEST.
6767 034106 004767 167156    JSR    PC,WAIBIS    ;WAIT FOR RX_DATA_AVAILABLE TO SET.
6768 034112 103017          BCC    6$           ;REPORT ERROR IF NO DATA RECEIVED IN THE FIFO.
6769 034114 005267 147650    INC    ERRNBR       ;SET ERROR NUMBER TO 2405.
6770 034120 017702 146120    MOV    @RBUFA,R2   ;READ THE DATA FROM THE FIFO.
6771 034124 100012          BPL    6$           ;GO REPORT ERROR IF THER IS'NT ANY DATA THERE.
6772 034126 005267 147636    INC    ERRNBR       ;SET ERROR NUMBER TO 2406.
6773 034132 000302          SWAB   R2           ;PUT THE LINE NUMBER IN THE LOW BYTE.
6774 034134 042702 177760    BIC   #177760,R2   ;CLEAR THE UNWANTED BITS.
6775 034140 020204          CMP    R2,R4       ;DID THE DATA COME FROM THE CORRECT LINE?.
6776 034142 001003          BNE    6$           ;NO; GO REPORT THE ERROR.
6777 034144 005305          DEC    R5           ;DECREMENT THE TXCHAR/LOOP COUNTER.
6778 034146 001334          BNE    4$           ;LOOP TO TX THE NEXT CHAR.
6779 034150 000410          BR    8$           ;GO TEST THE NEXT LINE.
6780
6781 034152 010401          MOV    R4,R1        ;PASS THE NUMBER OF CURRENT LINE UNDER TEST.
6782 034154 012702 012772    MOV    #EM2302,R2  ;PASS THE MESSAGE TO BE REPORTED.
6783
6784 034160          ERROR             ; "TX_ENABLE BIT BAD ON LINE: NN".
6785 034160 104460          ; >>>> ERROR <<<<<.
6786 034162 032767 000100 146034    BIT   #BIT06,OPTION ;EXIT THE TEST IF EXTENDED ERROR REPORTING
6787 034170 001411          BEQ    60$         ;HAS NOT BEEN ENABLED, SINCE THE TEST HAS FAILED.
6788
6789          ;*
6790          ; VERIFY ALL ACTIVE LINES HAVE BEEN TESTED.
6791          ;-
6792 034172 010305          MOV    R3,R5        ;PASS THE BIT MAP OF THE LINE UNDER TEST.
6793 034174 004767 166404    JSR    PC,TXDSBL   ;CLEAR THE TX_ENABLE BIT ON THIS LINE.
6794 034200 000241          CLC                ;CLEAR THE CARRY BIT PRIOR TO ROTATION.
6795 034202 006103          ROL    R3           ;SHIFT THE BIT MAP FOR THE NEXT LINE.
6796 034204 005204          INC    R4           ;INCREMENT THE LINE NUMBER COUNTER.
6797 034206 020427 000020    CMP   R4,#NUMLNS   ;HAVE ALL THE LINES BEEN TESTED?.
6798 034212 002672          BLT   2$           ;NO; BRANCH TO TEST THE NEXT LINE.
6799
    
```

6800 034214 005067 146066
6801 034220
034220
034220 104401

60\$: CLR CTRLCF
ENDTST

;INDICATE THAT WE ARE NOT WITHIN A TEST.

L10053: TRAP C\$ETST


```

6803 .SBTTL HARDWARE TEST - INTA -
6804 ;** *****
6805 ;* - INTERRUPT TEST -
6806 ;* THIS TEST VERIFIES THAT THE DEVICE UNDER TEST (DUT) WILL GENERATE
6807 ;* RECEPTION AND TRANSMISSION INTERRUPTS CORRECTLY. THIS TEST DOES
6808 ;* NOT DEPEND ON THE USE OF THE SERIAL LINE TRANSMISSION OR RECEPTION
6809 ;* CAPABILITIES OF THE DUT. THE LINES ARE PUT IN INTERNAL LOOPBACK
6810 ;* TO MINIMIZE ANY EXTERNAL EFFECTS THAT COULD BE CAUSED ON DEVICES
6811 ;* ATTACHED TO THE SERIAL LINES.
6812 ;*
6813 ;*
6814 ;-- *****
6815 034222 BGNTST
        034222
6816 034222 SETPRI #PRI05 ;ALLOW THE LTC TO INTERRUPT. T23::
        034222 012700 000240
        034226 104441
6817 000027
6818 034230 012767 000027 146066 TNUM == TNUM + 1 ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
6819 034236 012767 177777 146042 MOV #TNUM,TSTNUM ;SET UP THE TEST NUMBER. (26)
6820 034244 012767 000001 147514 MOV #-1,CTRLCF ;INDICATE THAT WE ARE IN A TEST.
6821 034252 012767 003101 147510 MOV #1,ERRTYP ;SET ERROR FATAL ERROR TYPE IN ERROR TABLE.
6822 034260 012767 013073 147504 MOV #1601.,ERRNBR ;SET FIRST ERROR REPORT NUMBER IN ERROR TABLE.
6823 MOV #EM2601.,ERRMSG ;SET TEST ERROR MESSAGE IN ERROR TABLE.
6824 ;*
6825 ; RESET THE DUT TO A KNOWN STATE. DO NOT REMOVE THE STATUS CODES FROM THE FIFO.
6826 ; CLEAR TX AND RX INTERRUPT ENABLE BITS IN THE CSR.
6827 ; THIS SUBROUTINE REPORTS ERRORS FROM >>>> 2601 THRU 2602 <<<<<.
6828 034266 004767 165064
6829 034272 103402
6830 034274 000167 001044
6831 034300 012767 005053 147462 2$: JSR PC,RESETT ;RESET THE DHU-11. REPORT ANY ERRORS FOUND.
6832 BCS 2$ ;SKIP AROUND ABORTING TEST IF NO ERROR FOUND.
6833 JMP 60$ ;ABORT TEST IF FATAL ERROR FOUND DURING RESET.
6834 ;*
6835 034306 012705 177777 4$: MOV #MAPLNS,R5 ;PASS ACTIVE LINE BIT MAP.
6836 034312 004767 166362 JSR PC,TXENBL ;ENABLE TRANSMISSION ON ALL LINES.
6837 ;*
6838 ; TEST RECEPTION INTERRUPTS.
6839 ; SET UP FOR RX AND TX INTERRUPTS:
6840 ; RX INTERRUPT SERVICE ROUTINE INPUTS A CHAR AND COUNTS THE INTERRUPT.
6841 ; TX INTERRUPT SERVICE ROUTINE COUNTS TX INTERRUPTS.
6842 ;*
6843 034316 005067 145772
6844 034322 005067 145770
6845 034326 005067 145774
6846 034332 012767 002726 145742
6847 034340
        034340 012746 000300
        034344 012746 024036
        034350 016746 145656
        034354 012746 000003
        034360 104437
        034362 062706 000010
6848 034366 SETVEC TXVECA,#CACHTX,#PRI06 ;SET UP INTERRUPT VECTOR TO CATCH TX INT.
        034366 012746 000300
        034372 012746 023660
    
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034376 016746 145632
034402 012746 000003
034406 104437
034410 062706 000010
6849 034414 SETPRI #PRI04 ;ALLOW DEVICE INTERRUPTS.
034414 012700 000200
034420 104441
6850
6851 ;+
6852 ;ENABLE RECEPTION INTERRUPTS.
6853 ;DELAY 4 MS TO ALLOW TIME FOR THE INTERRUPTS TO TAKE PLACE.
6854 ;DISABLE RECEPTION INTERRUPTS.
6855 034422 004767 165624 ;-
6856 034426 012704 000004 JSR PC,RXIE1 ;ENABLE THE RECEPTION INTERRUPTS.
6857 034432 004767 163466 MOV #4,R4 ;PASS 4 MS COUNT TO THE DELAY ROUTINE.
6858 034436 004767 165550 JSR PC,DELAY ;DELAY 4 MILLI-SECONDS.
6859 JSR PC,RXIE0 ;DISABLE RECEPTION INTERRUPTS.
6860 ;+
6861 ; VERIFY THAT THE CORRECT INTERRUPTS TOOK PLACE.
6862 ; TEST THE INT COUNTER TO VERIFY THAT INTERRUPTS TOOK PLACE.
6863 034442 005767 145646 ;-
6864 034446 001017 TST RXINTC ;CHECK THE RX INTERRUPT COUNT.
6865 BNE 6$ ;SKIP THE FOLLOWING ERRORS IF COUNT <> 0.
6866 ;+
6867 ; DETERMINE REASON FOR NO RX INTERRUPTS AND PRINT PROPER ERROR MESSAGE.
6868 034450 012701 013310 ;-
6869 034454 032777 000200 145560 MOV #EM2604,R1 ;SET UP MSG IN CASE "RX.DATA.AVAIL IS CLR".
6870 034462 001416 BIT #BIT7,@CSRA ;TEST THE RX.DATA.AVAIL BIT OF THE CSR.
6871 034464 012701 013222 BEQ 8$ ;GO REPORT ERROR IF RX.DATA.AVAIL IS CLR.
6872 034470 032777 100000 145546 MOV #EM2603,R1 ;SET UP MSG IN CASE "DATA.VALID IS CLEAR".
6873 034476 001410 BIT #BIT15,@RBUFA ;TEST THE DATA.VALID BIT OF THE FIFO.
6874 034500 012701 013131 BEQ 8$ ;GO REPORT ERROR IF DATA.VALID IS CLEAR.
6875 034504 000405 MOV #EM2602,R1 ;SET UP MSG,"DATA.VALID IS SET".
6876 BR 8$ ;GO REPORT THE ERROR.
6877 ;+
6878 ; IF RX INTS OCCURRED WITH RX.DATA.AVAIL CLEAR, REPORT THE ERROR.
6879 034506 005767 145604 6$: TST RXINTF ;CHECK THE RX INTERRUPT FLAGS.
6880 034512 100014 BPL 10$ ;SKIP THE ERROR IF FLAG IS CLEAR.
6881 034514 012701 013404 MOV #EM2605,R1 ;SET UP THE PROPER MESSAGE.
6882 ;+
6883 ; REPORT THE ERROR WHICH HAS BEEN FOUND.
6884 ;-
6885 034520 8$: ERRDF 2603,EM2601,ER0503; >>>> ERROR #2603 <<<<<.
034520 104455 TRAP C$ERDF
034522 005053 .WORD 2603
034524 013073 .WORD EM2601
034526 016136 .WORD ER0503
6886
6887 ;+
6888 ; EXIT THE TEST IF EXTENDED ERROR REPORTING HAS NOT BEEN ENABLED
6889 ;-
6890
6891 034530 032767 000100 145466 BIT #BIT06,OPTION ;EXIT WITH TEST FAILURE MESSAGE IF
6892 034536 001002 BNE .+6 ;
6893 034540 000167 000556 JMP 34$ ;NO EXTENDED ERROR REPORTING HAS BEEN REQUESTED
6894
6895 ;+

```



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6896
6897
6898 034544 016702 145556
6899 034550 001414
6900
6901 034552 012701 007505
6902 034556
      034556 104455
      034560 005054
      034562 013073
      034564 016174
6903
6904
6905
6906
6907
6908 034566 032767 000100 145430
6909 034574 001002
6910 034576 000167 000520
6911
6912
6913
6914
6915 034602
      034602 012700 000300
      034606 104441
6916 034610
      034610 016700 145416
      034614 104436
6917 034616
      034616 016700 145412
      034622 104436
6918
6919
6920
6921
6922
6923
6924 034624 005067 145464
6925 034630 005067 145472
6926 034634 005067 145470
6927 034640
      034640 012746 000300
      034644 012746 023632
      034650 016746 145356
      034654 012746 000003
      034660 104437
      034662 062706 000010
6928 034666
      034666 012746 000300
      034672 012746 024146
      034676 016746 145332
      034702 012746 000003
      034706 104437
      034710 062706 000010
6929 034714
      034714 012700 000200

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; VERIFY THAT NO TX INTERRUPTS HAVE BEEN GENERATED SO FAR IN THIS TEST.
;+
; -
10$:  MOV    TXINTC,R2      ;LOAD # OF TX INTERRUPTS FOR ERO504 RTN.
      BEQ    12$           ;SKIP ERROR IF NO TX INTERRUPTS.
;REPORT "TX INTERRUPTS(S) RECEIVED WITH TX INTERRUPTS DISABLED."
      MOV    #EM0526,R1    ;SET UP MESSAGE ADR FOR INDIRECT PRINT.
      ERDF   2604,EM2601,ERO504; >>>> ERROR #2604 <<<<<.
;                                TRAP    C$ERDF
;                                .WORD   2604
;                                .WORD   EM2601
;                                .WORD   ERO504
;+
; EXIT THE TEST IF EXTENDED ERROR REPORTING HAS NOT BEEN ENABLED
; -
      BIT    #BIT06,OPTION ;EXIT WITH TEST FAILURE MESSAGE IF
      BNE   .+6            ;
      JMP   34$           ;NO EXTENDED ERROR REPORTING HAS BEEN REQUESTED
;+
; CLEAN OUT THE INTERRUPT VECTORS USED IN THIS TEST.
; -
12$:  SETPRI #PRI06        ;DISABLE DEVICE INTERRUPTS.
;                                MOV     #PRI06,R0
;                                TRAP   C$SPRI
      CLRVEC RXVECA        ;RETURN RX INT VECTOR TO UNUSED POOL.
;                                MOV     RXVECA,R0
;                                TRAP   C$CVEC
      CLRVEC TXVECA        ;RETURN TX INT VECTOR TO UNUSED POOL.
;                                MOV     TXVECA,R0
;                                TRAP   C$CVEC
;+
; TEST TRANSMISSION INTERRUPTS.
; SET UP FOR RX AND TX INTERRUPTS:
; RX INTERRUPT SERVICE ROUTINE COUNTS RX INTERRUPTS.
; TX INTERRUPT SERVICE ROUTINE COUNTS THE INTERRUPT AND SETS FLAGS.
; -
      CLR   RXINTC        ;CLEAR THE RX INTERRUPT COUNTER.
      CLR   TXINTC        ;CLEAR THE TX INTERRUPT COUNTER.
      CLR   TXINTF        ;CLEAR THE RX INTERRUPT FLAGS.
      SETVEC RXVECA,#CACHRX,#PRI06 ;SET UP INTERRUPT VECTOR TO CATCH RX INT.
;                                MOV     #PRI06,-(SP)
;                                MOV     #CACHRX,-(SP)
;                                MOV     RXVECA,-(SP)
;                                MOV     #3,-(SP)
;                                TRAP   C$SVEC
;                                ADD    #10,SP
      SETVEC TXVECA,#TXINTR,#PRI06 ;SET UP INT VECTOR TO TX INT ROUTINE.
;                                MOV     #PRI06,-(SP)
;                                MOV     #TXINTR,-(SP)
;                                MOV     TXVECA,-(SP)
;                                MOV     #3,-(SP)
;                                TRAP   C$SVEC
;                                ADD    #10,SP
      SETPRI #PRI04        ;ALLOW DEVICE INTERRUPTS.
;                                MOV     #PRI04,R0

```

```

034720 104441
6930 TRAP C$SPRI
6931 ;+
6932 ; VERIFY THAT THE TX_ACTION BIT IS CLEAR.
6933 034722 012705 000022 ;-
6934 034726 012701 000144 MOV #18.,R5 ;INITIALIZE THE LOOP COUNTER.
6935 034732 012702 100000 MOV #100.,R1 ;SET 100 MS TIME-OUT.
6936 034736 016704 145300 MOV #BIT15,R2 ;SELECT TX_ACTION BIT TO TEST.
6937 034742 012703 100000 MOV CSRA,R4 ;PASS OUT CSR AS THE WORD TO TEST.
6938 034746 004767 163326 14$: MOV #BIT15,R3 ;WAIT FOR TX_ACTION TO BE SET.
        JSR PC,MSLOOP ;WAIT UP TO 100 MS FOR TX_ACTION SET.
        BCC 20$ ;IF TIME-OUT, CONSIDER TX_ACTION CLEAR.
        CLR R3 ;NOW, WAIT FOR TX_ACTION CLEAR.
        JSR PC,MSLOOP ;WAIT UP TO 100 MS FOR TX_ACTION CLEAR.
        BCC 16$ ;IF TIME-OUT, REPORT TX_ACTION WON'T CLEAR.
        DEC R5 ;DECREMENT THE TX_ACTION SET COUNTER.
        BNE 14$ ;LOOP IF NOT TOO MANY TX_ACTIONS FOUND.
        ;REPORT "TX_ACTION SET REPEATEDLY AFTER RESET, NO DATA SENT."
        MOV #EM2607,R1 ;SELECT ERROR MESSAGE.
        BR 18$ ;GO TO REPORT THE ERROR.
16$: MOV #EM2608,R1 ;SELECT TX_ACTION STUCK SET MSG.
18$: ERRDF 2605,EM2606,ER0503; >>>> ERROR #2605 <<<<.
        TRAP C$ERDF
        .WORD 2605
        .WORD EM2606
        .WORD ER0503
6945
6946 034770 012701 013526
6947 034774 000402
6948 034776 012701 013622
6949 035002
        035002 104455
        035004 005055
        035006 013467
        035010 016136
6950
6951 ;+
6952 ; EXIT THE TEST IF EXTENDED ERROR REPORTING HAS NOT BEEN ENABLED
6953 ;-
6954 035012 032767 000100 145204 BIT #BIT06,OPTION ;EXIT WITH TEST FAILURE MESSAGE IF
6955 035020 001540 BEQ 34$ ;NO EXTENDED ERROR REPORTING HAS BEEN REQUESTED
6956 035022 004767 166006 JSR PC,TXIE1 ;ENABLE TX INTERRUPTS FOR THE TX_INT TESTING.
6957 035026 000430 BR 24$ ;GO TO TEST WITH TX_ACTION SET.
6958
6959 ;+
6960 ; VERIFY THAT NO INTERRUPTS OCCUR WITH TX_ACTION CLEAR.
6961 035030 004767 166000 20$: JSR PC,TXIE1 ;ENABLE TX_INTERRUPTS.
6962 035034 012704 000062 MOV #50.,R4 ;PASS 50 MS TIME TO THE DELAY ROUTINE.
6963 035040 004767 163060 JSR PC,DELAY ;DELAY 50 MILLI-SECONDS TO ALLOW INTS TO OCCUR.
6964 035044 005767 145256 TST TXINTC ;TEST THE TX INTERRUPT COUNT.
6965 035050 001417 BEQ 24$ ;SKIP THE ERROR IF NO TX INTERRUPTS.
6966 035052 012701 013526 MOV #EM2607,R1 ;SELECT MESSAGE IN CASE TX INT FLAG CLEAR.
6967 035056 005767 145246 TST TXINTF ;TEST THE TX INTERRUPT FLAGS.
6968 035062 100002 BPL 22$ ;GO REPORT ERROR IF TX FLAG IS CLEAR.
6969 035064 012701 013673 MOV #EM2609,R1 ;TX FLAG IS SET, SELECT PROPER ERROR MESSAGE.
6970 ;REPORT "TRANSMIT INTERRUPT TEST ERROR:..."
22$: ERRDF 2606,EM2606,ER0503; >>>> ERROR #2606 <<<<.
        TRAP C$ERDF
        .WORD 2606
        .WORD EM2606
        .WORD ER0503
6971 035070
        035070 104455
        035072 005056
        035074 013467
        035076 016136
6972
6973 ;+
6974 ; EXIT THE TEST IF EXTENDED ERROR REPORTING HAS NOT BEEN ENABLED
6975 ;-
6976 035100 032767 000100 145116 BIT #BIT06,OPTION ;EXIT WITH TEST FAILURE MESSAGE IF
6977 035106 001500 BEQ 32$ ;NO EXTENDED ERROR REPORTING HAS BEEN REQUESTED

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6978
6979
6980
6981
6982 035110 005067 145212
6983 035114 005067 145210
6984
6985
6986
6987 035120 012705 177777
6988 035124 012700 000200
6989 035130 004767 166416
6990 035134 012700 156430
6991 035140 004767 166436
6992
6993
6994
6995 035144 016701 145140
6996 035150 005002
6997 035152 010177 145064
6998
6999 035156 112777 000000 145064
7000 035164 005201
7001 035166 005202
7002 035170 020227 000020
7003 035174 002766
7004
7005
7006
7007 035176 012704 000372
7008 035202 004767 162716
7009
7010
7011
7012 035206 005767 145114
7013 035212 001010
7014
7015
7016
7017 035214 012701 013752
7018 035220 005777 145016
7019 035224 100410
7020 035226 012701 014044
7021 035232 000405
7022
7023
7024
7025 035234 005767 145070
7026 035240 100012
7027 035242 012701 013673
7028
7029
7030
7031 035246
    035246 104455
    035250 005057
    035252 013467

;+
; PREPARE TX INTERRUPT COUNTER AND FLAGS.
;
24$: CLR TXINTC ;CLEAR THE TX INTERRUPT COUNT.
    CLR TXINTF ;CLEAR THE TX INTERRUPT FLAGS.
;+
; SET UP LINE PARAMETERS FOR TRANSMISSION.
;
    MOV #MAPLNS,R5 ;PASS ACTIVE LINES BIT MAP.
    MOV #200,R0 ;PASS INERT STATE, INTERNAL LOOPBACK.
    JSR PC,WTWLN ;DISABLE RECEPTION AND DMA, ETC. ON DUT.
    MOV #156430,R0 ;SPECIFY 9600BPS,1STOP,NO PARITY,8BITS/CHAR.
    JSR PC,WTWLP ;WRITE TO ALL LPR REGISTERS.
;+
; SEND A NULL CHAR TO EACH LINE.
;
    MOV IESTAT,R1 ;SET UP THE STATE OF THE INTERRUPT ENABLE BITS.
    CLR R2 ;CLEAR THE LINE COUNTER.
25$: MOV R1,@CSRA ;SET UP THE LINE NUMBER AND INTERRUPT ENABLE
    ;BITS IN THE CSR.
    MOVB #0,@FDATA ;SEND A NULL CHARACTER TO THE OUTPUT FIFO.
    INC R1 ;NEXT CSR CONTENTS.
    INC R2 ;NEXT LINE.
    CMP R2,#NUMLNS ;IF ALL LINES HAVE NOT BEEN SERVICED THEN
    BLT 25$ ;BRANCH.
;+
; DELAY 250 MILLI-SECONDS TO ALLOW INTERRUPTS TO OCCUR.
;
    MOV #250.,R4 ;SET UP FOR 250 MS DELAY.
    JSR PC,DELAY ;WAIT 250 MS.
;+
; VERIFY THAT TX INTERRUPTS OCCURRED.
;
    TST TXINTC ;CHECK THE TX INTERRUPT COUNTER.
    BNE 26$ ;SKIP THE FOLLOWING ERROR IF WE GOT TX INTS.
;+
; DETERMINE THE REASON THAT WE RECEIVED NO INTERRUPTS.
;
    MOV #EM2610,R1 ;SET UP MSG IN CASE "TX_ACTION IS SET".
    TST @CSRA ;CHECK THE DUT CSR.
    BMI 28$ ;GO TO REPORT ERROR IF TX_ACTION IS SET.
    MOV #EM2611,R1 ;SET UP "TX_ACTION NOT SET" MESSAGE.
    BR 28$ ;GO AND REPORT THE ERROR.
;+
; CHECK TO VERIFY THAT TX_ACTION WAS SET FOR EACH INTERRUPT.
;
26$: TST TXINTF ;CHECK THE TX INTERRUPT FLAGS.
    BPL 30$ ;SKIP ERROR IF TX_ACTION CLR FLAG IS CLEAR.
    MOV #EM2609,R1 ;SET UP TX INT WITH "TX_ACTION CLR" MSG.
;+
; REPORT "TRANSMIT INTERRUPT TEST ERROR:...."
;
28$: ERRDF 2607,EM2606,ER0503; >>>> ERROR #2607 <<<<<.
    TRAP C$ERDF
    .WORD 2607
    .WORD EM2606
    
```

```

035254 016136 .WORD ER0503
7032
7033
7034 ;+
7035 ; EXIT THE TEST IF EXTENDED ERROR REPORTING HAS NOT BEEN ENABLED
7036 035256 032767 000100 144740 ;-
7037 035264 001411 BIT #BIT06,OPTION ;EXIT WITH TEST FAILURE MESSAGE IF
BEQ 32$ ;NO EXTENDED ERROR REPORTING HAS BEEN REQUESTED
7038
7039 ;+
7040 ; VERIFY THAT NO RX INTERRUPTS HAVE BEEN GENERATED SO FAR IN THIS TEST.
7041 ;-
7042 035266 016702 145022 30$: MOV RXINTC,R2 ;LOAD # OF RX INTERRUPTS FOR ER0504 RTN.
7043 035272 001406 BEQ 32$ ;SKIP ERROR IF NO RX INTERRUPTS.
7044 035274 012701 007415 MOV #EM0525,R1 ;SET UP MESSAGE ADR FOR INDIRECT PRINT.
7045 ;REPORT "RX INTERRUPTS(S) RECEIVED WITH RX INTERRUPTS DISABLED."
7046 035300 ERRDF 2608,EM2606,ER0504; >>>> ERROR #2608 <<<<<.
035300 104455 TRAP C$ERDF
035302 005060 .WORD 2608
035304 013467 .WORD EM2606
035306 016174 .WORD ER0504
7047
7048 ;+
7049 ; DISABLE INTERRUPTS AND CLEAN OUT THE INTERRUPT VECTORS USED IN THIS TEST.
7050 035310 005001 ;-
7051 035312 004767 165456 32$: CLR R1 ;CLEAR BOTH TRANSMITTER
7052 035316 004767 164670 JSR PC,TXIE0 ; INTERRUPT ENABLE AND RECEIVER
7053 035322 JSR PC,RXIE0 ; INTERRUPT ENABLE BITS IN THE DUT CSR.
035322 012700 000300 34$: SETPRI #PRI06 ;DISABLE DEVICE INTERRUPTS.
035326 104441 MOV #PRI06,RO
7054 035330 CLRVEC RXVECA ;RETURN RX INT VECTOR TO UNUSED POOL. C$SPRI
035330 016700 144676 MOV RXVECA,RO
035334 104436 TRAP C$CVEC
7055 035336 CLRVEC TXVECA ;RETURN TX INT VECTOR TO UNUSED POOL.
035336 016700 144672 MOV TXVECA,RO
035342 104436 TRAP C$CVEC
7056
7057 035344 005067 144736 60$: CLR CTRLCF ;INDICATE THAT WE ARE NOT WITHIN A TEST.
7058 035350 SETPRI #PRI07 ;DISABLE ALL INTERRUPTS.
035350 012700 000340 MOV #PRI07,RO
035354 104441 TRAP C$SPRI
7059 035356 ENDTST
035356 104401 L10054:
7060 TRAP C$ETST

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7062 .SBTTL HARDWARE TEST - BRLEVA -
7063 ;** *****
7064 ;* - BR LEVEL TEST B -
7065 ;* THIS TEST VERIFIES THAT THE DEVICE UNDER TEST (DUT) WILL GENERATE
7066 ;* RECEPTION AND TRANSMISSION INTERRUPTS AT THE CORRECT BR LEVEL.
7067 ;* THIS TEST DOES NOT DEPEND ON THE USE OF THE SERIAL LINE TRANSMISSION
7068 ;* OR RECEPTION CAPABILITIES OF THE DUT. THE LINES ARE PUT IN INTERNAL
7069 ;* LOOPBACK TO MINIMIZE ANY EXTERNAL EFFECTS THAT COULD BE CAUSED ON
7070 ;* DEVICES ATTACHED TO THE SERIAL LINES.
7071 ;*
7072 ;-- *****
7073
7074 035360 BGNTST
7075 035360 SETPRI #PRI05 ;ALLOW LTC INTERRUPTS. T24::
035360 012700 000240
035364 104441
7076 000030 TNUM == TNUM + 1 ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
7077 035366 012767 000030 144730 MOV #TNUM,TSTNUM ;SET UP THE TEST NUMBER. (30)
7078 035374 012767 177777 144704 MOV #-1,CTRLCF ;INDICATE THAT WE ARE IN A TEST.
7079 035402 012767 000001 146356 MOV #1,ERRTYP ;SET ERROR TYPE AS FATAL IN ERROR TABLE.
7080 035410 012767 005671 146352 MOV #3001.,ERRNBR ;SET THE FIRST ERROR NUMBER IN ERROR TABLE.
7081 035416 012767 014223 146346 MOV #EM3001,ERRMSG ;SET ERROR MESSAGE ADDRESS IN ERRTBL.
7082 035424 005067 145032 CLR ERSMRF ;INITIALIZE THE "REPORT ERROR SUMMARY" FLAGS.
7083 ;+
7084 ; RESET THE DUT TO A KNOWN STATE, DO NOT REMOVE THE STATUS CODES FROM THE FIFO.
7085 ; CLEAR TX AND RX INTERRUPT ENABLE BITS IN THE CSR.
7086 ; THIS SUBROUTINE REPORTS ERRORS FROM >>>> 3001 THRU 3002 <<<<<.
7087 ;-
7088 035430 004767 163722 JSR PC,RESETT ;RESET THE DHU-11, REPORT ANY ERRORS FOUND.
7089 035434 103402 BCS 2$ ;SKIP AROUND ABORTING TEST IF NO ERROR FOUND.
7090 035436 000167 000644 JMP 60$ ;ABORT TEST IF FATAL ERROR FOUND DURING RESET.
7091 035442 012767 005673 146320 2$: MOV #3003.,ERRNBR ;SET THE ERROR REPORT NUMBER TO 3003.
7092 ;+
7093 ; ENABLE TRANSMITTERS ON ALL LINES.
7094 ;-
7095 035450 012705 177777 4$: MOV #MAPLNS,R5 ;PASS ACTIVE LINE BIT MAP.
7096 035454 004767 165220 JSR PC,TXENBL ;ENABLE TRANSMISSION ON ALL LINES.
7097 ;+
7098 ; GENERATE A TRANSMISSION INTERRUPT REQUEST.
7099 ; PROCESSOR PRIORITY SHOULD BE AT 7 DISABLING INTS.
7100 ;-
7101 035460 SETPRI #PRI07 ;DISABLE ALL INTERRUPTS.
035460 012700 000340
035464 104441
7102 035466 SETVEC TXVECA,#TXINTR,#PRI07 ;SET UP INTERRUPT VECTOR TO CATCH TX INT.
035466 012746 000340 MOV #PRI07,-(SP)
035472 012746 024146 MOV #TXINTR,-(SP)
035476 016746 144532 MOV TXVECA,-(SP)
035502 012746 000003 MOV #3,-(SP)
035506 104437 TRAP C$SVEC
035510 062706 000010 ADD #10,SP
7103 ;+
7104 ; SET UP DUT FOR TRANSMISSION INTERRUPTS:
7105 ; SET UP INTERNAL LOOPBACK.
7106 ; SET UP LINE PARAMETERS FOR TRANSMISSION.
7107 ;-
    
```

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7108 035514 012705 177777      MOV      #MAPLNS,R5      ;PASS ACTIVE LINES BIT MASK.
7109 035520 012700 000200      MOV      #200,R0        ;PASS INERT STATE, INTERNAL LOOPBACK.
7110 035524 004767 166022      JSR      PC,WTWLNC      ;DISABLE RECEPTION AND DMA, ETC. ON DUT.
7111 035530 012700 156430      MOV      #156430,R0     ;SPECIFY 9600BPS,1STOP,NO PARITY,8BITS/CHAR.
7112 035534 004767 166042      JSR      PC,WTWLPR     ;WRITE INTO ALL LPR REGISTERS.
7113
7114      ;*
7115      ; SEND A NULL CHAR TO EACH LINE.
7116 035540 016701 144544      MOV      IESTAT,R1      ;SET UP THE STATE OF THE INTERRUPT ENABLE BITS.
7117 035544 010177 144472      5$:     MOV      R1,@CSRA     ;SET UP THE LINE NUMBER AND INTERRUPT ENABLE
7118                                     ;BITS IN THE CSR.
7119 035550 112777 000000 144472      MOV      #0,@FDATA     ;SEND A NULL CHARACTER TO THE OUTPUT FIFO.
7120 035556 005201                                     ;NEXT LINE.
7121 035560 020127 000020      INC      R1
7122 035564 002767                                     ;IF ALL LINES HAVE NOT BEEN SERVICED THEN
7123                                     ;BRANCH.
7124                                     ;*
7125                                     ; DELAY 50 MS TO ALLOW TIME FOR THE INTERRUPT TO BE GENERATED.
7126 035566 012704 000062      MOV      #50.,R4       ;PASS 50 MS TIME TO THE DELAY ROUTINE.
7127 035572 004767 162326      JSR      PC,DELAY      ;DELAY 50 MILLI-SECONDS.
7128
7129      ;*
7130      ; GENERATE A RECEPTION INTERRUPT REQUEST.
7131      ;-
7131 035576                                     SETVEC  RXVECA,@RXBRRT,@PRI07 ;SET UP INTERRUPT VECTOR TO CATCH RX INT.
7131 035576 012746 000340                                     MOV      #PRI07,-(SP)
7131 035602 012746 023756                                     MOV      @RXBRRT,-(SP)
7131 035606 016746 144420                                     MOV      RXVECA,-(SP)
7131 035612 012746 000003                                     MOV      #3,-(SP)
7131 035616 104437                                     TRAP     C$SVEC
7131 035620 062706 000010                                     ADD      #10,SP
7132
7133      ;*
7134      ; SET UP FOR THE LOOP WHICH TESTS THE INTERRUPT BR LEVELS.
7135 035624 012705 000340      MOV      #340,R5      ;SET UP THE PRIORITY LEVEL TO 7.
7136 035630 005003      CLR      R3          ;CLEAR THE RX PRIORITY STORE AND FLAGS.
7137 035632 005002      CLR      R2          ;CLEAR THE TX PRIORITY STORE AND FLAGS.
7138
7139      ;*
7140      ; ENABLE TX AND RX INTERRUPTS.
7141      ; PROCESSOR PRIORITY SHOULD BE AT 7 DISABLING THE INTERRUPTS.
7142 035634 004767 164412      JSR      PC,RXIE1     ;ENABLE RECEIVER INTERRUPTS.
7143 035640 004767 165170      JSR      PC,TXIE1     ;ENABLE TRANSMITTER INTERRUPTS.
7144
7145      ;*
7146      ; LOOP, LOWERING THE PROCESSOR PRIORITY UNTIL THE DUT INTERRUPTS ON RX AND TX.
7147 035644 005067 144456      6$:     CLR      TXINTC      ;CLEAR THE TX INTERRUPT COUNTER.
7148 035650 005067 144454      CLR      TXINTF      ;CLEAR THE TX INTERRUPT FLAGS.
7149 035654 005067 144434      CLR      RXINTC      ;CLEAR THE RX INTERRUPT COUNTER.
7150 035660 005067 144432      CLR      RXINTF      ;CLEAR THE RX INTERRUPT FLAGS.
7151 035664                                     SETPRI  R5          ;SET PROCESSOR PRIORITY TO THE SELECTED VALUE.
7151 035664 010500                                     MOV      R5,R0
7151 035666 104441                                     TRAP     C$SPRI
7152 035670 012704 000001      MOV      #1,R4       ;PASS 1 MS COUNT TO THE DELAY ROUTINE.
7153 035674 004767 162224      JSR      PC,DELAY     ;DELAY 1 MS TO ALLOW INTERRUPTS TO OCCUR.
7154
7155      ;*
7156      ; DETERMINE IF ANY RX DUT INTERRUPTS OCCURRED.
7156      ; LOG THE PROCESSOR PRIORITY FOR THE RX INTERRUPT IF FIRST RX INT.

```



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7157
7158 035700 005767 144410
7159 035704 001412
7160
7161
7162
7163 035706 005703
7164 035710 001010
7165 035712 010503
7166 035714 052703 100000
7167 035720 016700 144372
7168 035724 042700 137777
7169 035730 050003
7170
7171
7172
7173
7174 035732 005767 144370
7175 035736 001405
7176
7177
7178
7179 035740 005702
7180 035742 100403
7181 035744 010502
7182 035746 052702 100000
7183
7184
7185
7186
7187 035752 162705 000040
7188 035756 002402
7189 035760 030203
7190 035762 100330
7191
7192
7193
7194 035764
035764 012700 000340
035770 104441
7195 035772
035772 016700 144234
035776 104436
7196 036000
036000 016700 144230
036004 104436
7197
7198
7199
7200
7201
7202
7203 036006 005702
7204 036010 100420
7205
7206
7207

;
; TST RXINTC ;CHECK THE RECEIVE INTERRUPT COUNTER.
; BEQ 8$ ;SKIP THE PRIORITY LOG IF NO RX INT OCCURRED.
;
; IF THIS IS THE FIRST RX INTERRUPT, LOG THE PRIORITY.
;
; TST R3 ;CHECK THE RX PRIORITY STORE AND FLAGS.
; BNE 8$ ;GOTO TEST FOR TX INTS IF NOT THE FIRST RX INT.
; MOV R5,R3 ;LOG THE PRESENT PRIORITY IN THE RX PRIO STORE.
; BIS #BIT15,R3 ;SET THE RX INT HAS OCCURRED FLAG.
; MOV RXINTF,R0 ;GET THE RX INTERRUPT ROUTINE FLAGS.
; BIC #137777,R0 ;CLEAR ALL BUT THE TX INT ERROR FLAG.
; BIS R0,R3 ;IF TX INT ERROR, SET BIT 14 OF THE PRIO FLAGS.
;
; DETERMINE IF ANY TX DUT INTERRUPTS HAVE OCCURRED.
; LOG THE PRESENT PROCESSOR PRIORITY IF THIS IS THE FIRST TX INTERRUPT.
;
; TST TXINTC ;CHECK THE TRANSMIT INTERRUPT COUNTER.
; BEQ 10$ ;SKIP THE PRIORITY LOG IF NO TX INT OCCURRED.
;
; IF THIS IS THE FIRST TX INTERRUPT, LOG THE PRIORITY.
;
; TST R2 ;CHECK THE TX PRIORITY STORE AND FLAGS.
; BMI 10$ ;SKIP THE LOGGING IF NOT FIRST TX INTERRUPT.
; MOV R5,R2 ;LOG THE PRESENT PRIORITY IN THE TX PRIO STORE.
; BIS #BIT15,R2 ;SET THE TX INT HAS OCCURRED FLAG.
;
; SELECT NEXT PROCESSOR PRIORITY.
; TEST FOR BOTH RX AND TX INTERRUPTS HAVING OCCURRED, LOOP IF NOT.
;
; SUB #40,R5 ;DECREMENT PRIORITY LEVEL BY ONE.
; BLT 12$ ;GOTO CHECK FOR ERRORS IF BELOW PRIORITY ZERO.
; BIT R2,R3 ;AND PRIO FLAGS TOGETHER, ALTER NONE OF THEM.
; BPL 6$ ;LOOP IF RX AND TX INTS HAVEN'T BOTH OCCURRED.
;
; DISABLE INTERRUPTS AND CLEAR INTERRUPT VECTORS.
;
; SETPRI #PRI07 ;DISABLE ALL INTERRUPTS.
;
; CLRVEC RXVECA ;RETURN RX INT VECTOR TO UNUSED POOL.
; CLRVEC TXVECA ;RETURN TX INT VECTOR TO UNUSED POOL.
;
; MOV #PRI07,R0
; TRAP C$SPRI
; MOV RXVECA,R0
; TRAP C$CVEC
; MOV TXVECA,R0
; TRAP C$CVEC
;
; VERIFY THAT RX AND TX INTERRUPTS OCCURRED,
; AT THE PROPER BR LEVEL, AND
; IN THE PROPER ORDER.
; DETERMINE IF TX INTERRUPT OCCURRED.
;
; TST R2 ;DETERMINE WHETHER TX INT OCCURRED OR NOT.
; BMI 16$ ;SKIP THESE ERRORS IF TX INT OCCURRED.
;
; DETERMINE REASON THAT NO TX INT OCCURRED.
;

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7208 036012 012701 013752      MOV    #EM2610,R1      ;SELECT "NO TX INT FROM TX.ACTION" MESSAGE.
7209 036016 005777 144220      TST    @CSRA          ;CHECK THE TX.ACTION BIT OF THE DUT CSR.
7210 036022 100402              BMI    14$           ;SKIP TX.ACTION CLR MSG SELECTION IF IT IS SET.
7211 036024 012701 014044      MOV    #EM2611,R1      ;SELECT "TX.ACTION CLEAR AFTER CHARS SENT" MSG.
7212                                ;REPORT "INTERRUPT BR LEVEL TEST ERROR:"
7213 036030 14$: ERRDF 3003,EM3001,ER0503; >>>> ERROR #3003 <<<<<.
                                TRAP    C$ERDF
                                .WORD   3003
                                .WORD   EM3001
                                .WORD   ER0503
7214
7215
7216                                ;*
7217                                ; EXIT THE TEST IF EXTENDED ERROR REPORTING HAS NOT BEEN ENABLED
7218 036040 032767 000100 144156 ;-
7219 036046 001513              BIT    #BIT06,OPTION ;EXIT WITH TEST FAILURE MESSAGE IF
7220                                BEQ    26$           ;NO EXTENDED ERROR REPORTING HAS BEEN REQUESTED
7221 036050 000427              BR     18$           ;SKIP THE BR LEVEL CHECK, NO TX INT OCCURRED.
7222                                ;*
7223                                ; VERIFY THAT THE TX INTERRUPT WAS AT THE PROPER BR LEVEL.
7224                                ;-
7225 036052 010204 16$: MOV    R2,R4          ;CALCULATE THE BR LEVEL
7226 036054 042704 177400      BIC    #177400,R4     ; THAT THE TRANSMIT
7227 036060 006204              ASR    R4             ; INTERRUPT WAS
7228 036062 006204              ASR    R4             ; REQUESTED AT, WHICH
7229 036064 006204              ASR    R4             ; IS ONE GREATER THAN
7230 036066 006204              ASR    R4             ; THE PROCESSOR PRIORITY
7231 036070 006204              ASR    R4             ; LEVEL AT WHICH THE
7232 036072 005204              INC    R4             ; TRANSMIT INTERRUPT OCCURRED.
7233 036074 116705 144140      MOVB   BRLEVL,R5      ;GET THE EXPECTED INTERRUPT BR LEVEL.
7234 036100 120405              CMPB   R4,R5          ;COMPARE THE INTERRUPT BR LEVEL WITH EXPECTED.
7235 036102 001412              BEQ    18$           ;SKIP THE ERROR IF BR LEVEL IS CORRECT.
7236                                ;REPORT "TX INTERRUPT GENERATED AT WRONG BR LEVEL: ..."
7237 036104 012701 014356      MOV    #EM3003,R1      ;SELECT THE ERROR MESSAGE FOR THE ERROR CALL.
7238 036110 104455              ERRDF  3004,EM3001,ER3001; >>>> ERROR #3004 <<<<<.
                                TRAP    C$ERDF
                                .WORD   3004
                                .WORD   EM3001
                                .WORD   ER3001
7239
7240                                ;*
7241                                ; EXIT THE TEST IF EXTENDED ERROR REPORTING HAS NOT BEEN ENABLED
7242 036120 032767 000100 144076 ;-
7243 036126 001463              BIT    #BIT06,OPTION ;EXIT WITH TEST FAILURE MESSAGE IF
7244                                BEQ    26$           ;NO EXTENDED ERROR REPORTING HAS BEEN REQUESTED
7245
7246                                ;*
7247                                ; DETERMINE IF RX INTERRUPT OCCURRED.
7248 036130 005703 18$: TST    R3             ;CHECK THE RX INT OCCURRED FLAG.
7249 036132 100421              BMI    22$           ;SKIP THESE ERRORS IF RX INT OCCURRED.
7250
7251                                ;*
7252                                ; DETERMINE REASON THAT NO RX INT OCCURRED.
7253 036134 012701 014127      MOV    #EM2612,R1      ;SELECT "NO RX INT FROM RX.DATA.AVAIL" MSG.
7254 036140 032777 000200 144074 ;-
7255 036146 001002              BIT    #BIT7,@CSRA    ;CHECK THE RX.DATA.AVAIL BIT OF THE DUT CSR.
7256 036150 012701 014262      BNE    20$           ;SKIP RX.DATA.AVAIL CLR MSG IF BIT IS SET.
                                MOV    #EM3002,R1      ;SELECT "NO RX.DATA.AVAIL AFTER RESET" MSG.

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7257
7258 036154 104455 ;REPORT "INTERRUPT BR LEVEL TEST ERROR:"
036154 005675 20$: ERRDF 3005,EM3001,ER0503; >>>> ERROR #3005 <<<<.
036156 014223 TRAP C$ERDF
036160 016136 .WORD 3005
036162 016136 .WORD EM3001
.WORD ER0503

7259
7260 ;*
7261 ; EXIT THE TEST IF EXTENDED ERROR REPORTING HAS NOT BEEN ENABLED
7262 036164 032767 000100 144032 ;-
7263 036172 001441 BIT #BIT06,OPTION ;EXIT WITH TEST FAILURE MESSAGE IF
BEQ 26$ ;NO EXTENDED ERROR REPORTING HAS BEEN REQUESTED
7264
7265 036174 000427 BR 24$ ;SKIP THE BR CHECK IF NO RX INT OCCURRED.
7266
7267 ;*
7268 ; VERIFY THAT THE RX INTERRUPT WAS AT THE PROPER BR LEVEL.
7269 036176 010304 22$: MOV R3,R4 ;CALCULATE THE BR LEVEL
7270 036200 042704 177400 BIC #177400,R4 ; THAT THE RECEIVE
7271 036204 006204 ASR R4 ; INTERRUPT WAS
7272 036206 006204 ASR R4 ; REQUESTED AT, WHICH
7273 036210 006204 ASR R4 ; IS ONE GREATER THAN
7274 036212 006204 ASR R4 ; THE PROCESSOR PRIORITY
7275 036214 006204 ASR R4 ; LEVEL AT WHICH THE
7276 036216 005204 INC R4 ; RECEIVE INTERRUPT OCCURRED.
7277 036220 116705 144014 MOVB BRLEVL,R5 ;GET THE EXPECTED INTERRUPT BR LEVEL.
7278 036224 120405 CMPB R4,R5 ;COMARE THE INTERRUPT BR LEVEL WITH EXPECTED.
7279 036226 001412 BEQ 24$ ;SKIP THE ERROR IF BR LEVEL IS CORRECT.
7280 ;REPORT "RX INTERRUPT GENERATED AT WRONG BR LEVEL: ..."
7281 036230 012701 014432 MOV #EM3004,R1 ;SELECT ERROR MESSAGE FOR THE ERROR CALL.
7282 036234 104455 ERRDF 3006,EM3001,ER3001; >>>> ERROR #3006 <<<<.
036236 005676 TRAP C$ERDF
036240 014223 .WORD 3006
036242 016620 .WORD EM3001
.WORD ER3001

7283
7284 ;*
7285 ; EXIT THE TEST IF EXTENDED ERROR REPORTING HAS NOT BEEN ENABLED
7286 036244 032767 000100 143752 ;-
7287 036252 001411 BIT #BIT06,OPTION ;EXIT WITH TEST FAILURE MESSAGE IF
BEQ 26$ ;NO EXTENDED ERROR REPORTING HAS BEEN REQUESTED
7288
7289 ;*
7290 ; TEST FOR INTERRUPTS OCCURING IN THE PROPER ORDER.
7291
7292 036254 032703 040000 24$: BIT #BIT14,R3 ;CHECK THE IMPROPER INT ORDER ERROR FLAG.
7293 036260 001406 BEQ 26$ ;SKIP ERROR REPORT IF ERROR DID NOT OCCUR.
7294 ;REPORT "TX INTERRUPT GIVEN PRECEDENCE OVER SIMULTANEOUS RX INT."
7295 036262 012701 014506 MOV #EM3005,R1 ;SELECT THE ERROR MESSAGE FOR INDIRECT PRINT.
7296 036266 104455 ERRDF 3007,EM3001,ER0503; >>>> ERROR #3007 <<<<.
036266 005677 TRAP C$ERDF
036270 014223 .WORD 3007
036272 016136 .WORD EM3001
036274 016136 .WORD ER0503

7297
7298 ;*
7299 ; CLEAN UP, EXIT THE TEST.
7300 036276 004767 164472 26$: JSR PC,TXIEO ;CLEAR TRANSMITTER INTERRUPTS.
7301 036302 004767 163704 JSR PC,RXIEO ;CLEAR RECEIVER INTERRUPTS.

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7302 036306 005067 143774
 7303 036312
 036312 012700 000340
 036316 104441
 7304 036320
 036320
 036320 104401
 7305

60\$: CLR CTRLCF
 SETPRI #PRI07
 ENDTST

;INDICATE THAT WE ARE NOT WITHIN A TEST.
 ;DISABLE ALL INTERRUPTS.

MOV #PRI07,R0
 TRAP C\$SPRI
 L10055:
 TRAP C\$ETST


```

7307
7308
7309
7310
7311
7312
7313
7314
7315 036322
      036322
7316 036322 012700 000240
      036322 104441
      036326 000031
7317
7318 036330 012767 000031 143766
7319 036336 012767 177777 143742
7320 036344 012767 000001 145414
7321 036352 012767 006035 145410
7322 036360 012767 014600 145404
7323 036366 012767 017222 145400
7324
7325
7326
7327
7328
7329 036374 004767 161402
7330 036400 103103
7331
7332
7333
7334
7335
7336 036402 016705 143622
7337 036406 005004
7338 036410 016703 143626
7339 036414 000241
7340 036416 006005
7341 036420 103070
7342
7343
7344
7345
7346 036422 012767 006036 145340
7347 036430 010413
7348 036432 052777 000002 143606
7349
7350
7351
7352
7353 036440 012701 011750
7354 036444 016702 143576
7355 036450 004767 164540
7356 036454 103042
7357
7358
7359
7360

```

```

.SBTTL  HARDWARE TEST          - DIABMP -
; * *****
; *          - DIAGNOSTIC FIELD (BMP) TEST -
; * THIS TEST VERIFIES THAT A REQUEST TO THE DUT TO REPORT BMP STATUS
; * CODES IS COMPLIED WITH, WITHIN THE SPECIFIED TIME.
; * ALL ACTIVE LINES ARE TESTED.
; - *****

      BGNTST

      SETPRI  #PRI05          ;ALLOW LTC INTERRUPTS.      T25::

                                MOV  #PRI05,R0
                                TRAP C$SPRI

      TNUM == TNUM + 1      ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
      MOV  #TNUM,TSTNUM    ;SET UP THE TEST NUMBER.      (31)
      MOV  #-1,CTRLCF      ;INDICATE THAT WE ARE IN A TEST.
      MOV  #1,ERRTYP       ;SET ERROR TYPE AS FATAL IN ERROR TABLE.
      MOV  #3101,ERRNBR    ;SET THE FIRST ERROR NUMBER IN ERROR TABLE.
      MOV  #EM3101,ERRMSG  ;SET ERROR MESSAGE ADDRESS IN ERRTBL.
      MOV  #ER9101,ERRBLK ;SELECT THE CORRECT ERROR REPORTING ROUTINE.

; *
; * RESET THE DUT TO A KNOWN STATE, REMOVE THE STATUS CODES FROM THE FIFO.
; * CLEAR TX AND RX INTERRUPT ENABLE BITS IN THE CSR.
; * THIS SUBROUTINE REPORTS ERROR >>>> 3101 <<<<<.
; -

      JSR  PC,CLNRST      ;RESET THE DHU-11, REPORT ANY ERRORS FOUND.
      BCC  60$           ;RESET FAILURE?, ABORT THIS TEST.

; *
; * TEST ALL ACTIVE LINES INDIVIDUALLY.
; * WRITE THE REQUEST CODE TO THE DIAGNOSTIC FIELD IN THE LPR REGISTER.
; * VERIFY THAT A BMP CODE IS RETURNED WITHIN THE CORRECT TIME.
; -

      MOV  ACTLNS,R5      ;GET THE ACTIVE LINE BIT MAP.
      CLR  R4             ;CLEAR THE LINE NUMBER COUNTER.
      MOV  CSRA,R3        ;GET THE ADDRESS OF THE DUT'S CSR.
2$:   CLC                ;CLEAR THE CARRY BIT PRIOR TO SHIFTING BIT MAP.
      ROR  R5             ;SHIFT THE BIT MAP INTO THE CARRY BIT.
      BCC  8$            ;DO NOT TEST THE LINE IF IT IS INACTIVE.

; *
; * SELECT THE LINE UNDER TEST.
; * WRITE THE BMP REQUEST CODE TO THE DIAG FIELD IN THE LPR REGISTER.
; -

      MOV  #3102,ERRNBR   ;SET THE ERROR NUMBER TO 3102.
      MOV  R4,(R3)        ;SELECT THE LINE CURRENTLY UNDER TEST.
      BIS  #2,@LPRA       ;WRITE THE BMP REQUEST CODE TO THE LPR.

; *
; * WAIT FOR BMP REQUEST CODE TO BE CLEARED, REPORT ERROR IF TIME-OUT
; * OCCURS.
; -

      MOV  #11750,R1      ;TEST BIT 1, TIMEOUT OF 1 SEC.
      MOV  LPRA,R2        ;PASS THE ADDRESS OF THE REGISTER TO TEST.
      JSR  PC,WAIBIC      ;WAIT FOR REQUEST CODE TO CLEAR.
      BCC  6$            ;GO REPORT ERROR IF CODE DID NOT CLEAR IN TIME.

; *
; * WAIT FOR BMP CODE TO APPEAR IN THE FIFO, REPORT ERROR IF TIME-OUT
; * OCCURS.
; -

```

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7361 036456 005267 145306      INC      ERRNBR      ;SET ERROR NUMBER TO 3103.
7362 036462 012701 070012      MOV      #70012,R1   ;TEST BIT 7, TIMEOUT OF 10 MILLI SECS.
7363 036466 016702 143550      MOV      CSRA,R2     ;PASS THE ADDRESS OF THE REGISTER TO TEST.
7364 036472 004767 164572      JSR      PC,WAIBIS   ;WAIT FOR RX_DATA_AVAILABLE TO SET.
7365 036476 103031                BCC      6$          ;GO REPORT ERROR IF CODE DID NOT CLEAR IN TIME.
7366                                ;*
7367                                ; READ THE BMP CODE (IF IT IS THERE) FROM THE RBUF REGISTER.
7368                                ; DETERMINE IF IT IS A VALID BMP CODE.
7369                                ; VERIFY THE BMP CODE WAS RECEIVED FROM THE CORRECT CHANNEL.
7370                                ; IF THE BMP CODE DOES NOT INDICATE DUT RUNNING OK, THEN SAVE IT ON
7371                                ; THE QUEUE TO BE REPORTED IN A LATER TEST.
7372                                ;-
7373 036500 005267 145264      INC      ERRNBR      ;SET ERROR NUMBER TO 3104.
7374 036504 017702 143534      MOV      @RBUFA,R2   ;GET THE BMP CODE FROM THE FIFO.
7375 036510 100024                BPL      6$          ;GO REPORT ERROR IF NO BMP CODE FOUND.
7376 036512 005267 145252      INC      ERRNBR      ;SET ERROR NUMBER TO 3105.
7377 036516 012700 170301      MOV      #170301,R0  ;SET-UP A BMP CODE MASK.
7378 036522 040200                BIC      R2,R0       ;TRY TO CLEAR THE BMP MASK.
7379 036524 001016                BNE      6$          ;GO REPORT ERROR IF IT IS NOT A VALID BMP CODE.
7380 036526 005267 145236      INC      ERRNBR      ;SET THE ERROR NUMBER TO 3106.
7381 036532 010200                MOV      R2,R0       ;COPY THE BMP CODE.
7382 036534 000300                SWAB     R0          ;PUT THE LINE NUMBER IN THE LOW BYTE.
7383 036536 042700 177760      BIC      #177760,R0  ;CLEAR THE UNWANTED BITS.
7384 036542 120400                CMPB     R4,R0       ;DID THE BMP CODE COME FROM THE CORRECT LINE?.
7385 036544 001006                BNE      6$          ;NO; GO REPORT ERROR.
7386 036546 120227 000305      CMPB     R2,#305    ;IS THE BMP CODE A "GOOD ONE"?.
7387 036552 001413                BEQ      8$          ;YES; SKIP SAVING THE BMP CODE ON THE QUEUE.
7388 036554 004767 163516      JSR      PC,SAVBMP   ;SAVE THE BMP CODE ON THE QUEUE.
7389 036560 000410                BR       8$          ;GO SEE IF THERE ARE ANY MORE LINE TO TEST.
7390
7391 036562 010401                6$:      MOV      R4,R1   ;PASS THE LINE NUMBER TO BE REPORTED.
7392 036564 012702 014643      MOV      #EM3102,R2  ;PASS THE ERROR MESSAGE TO BE REPORTED.
7393                                ; "BMP REQUEST BIT BAD ON LINE:"
7394 036570                ERROR      ;
7395                                ; >>>> ERROR <<<<<.
7396                                ; TRAP      C$ERROR
7397                                ;*
7398                                ; EXIT THE TEST IF EXTENDED ERROR REPORTING HAS NOT BEEN ENABLED
7399 036572 032767 000100 143424  ;-
7400 036600 001403                BIT      @BIT06,OPTION ;EXIT WITH TEST FAILURE MESSAGE IF
7401                                BEQ      60$          ;NO EXTENDED ERROR REPORTING HAS BEEN REQUESTED
7402                                ;*
7403                                ; VERIFY ALL ACTIVE LINES HAVE BEEN TESTED.
7404                                ;-
7405 036602 005204                8$:      INC      R4     ;INCREMENT THE LINE NUMBER COUNTER.
7406 036604 005705                TST     R5          ;ARE THERE ANY MORE ACTIVE LINES TO TEST?.
7407 036606 001302                BNE     2$          ;YES; BRANCH TO TEST THE NEXT LINE.
7408 036610 005067 143472      60$:    CLR      CTRLCF  ;INDICATE THAT WE ARE NOT WITHIN A TEST.
7409 036614                ENDTST
7409 036614 104401                L10056: TRAP      C$ETST

```


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7422 036616
036616
7423 000032
7424 036616 012767 000032 143500
7425 036624 012767 177777 143454
7426 036632 016702 143666
7427 036636 012703 002526
7428 036642 020203
7429 036644 001411
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7435 036646 012701 015433
7436 036652
036652 104455
036654 022125
036656 015257
036660 017262
7437
7438 036662 012767 002526 143634
7439
7440 036670 005067 143412
7441 036674
036674
036674 104401

```
.SBTTL  HARDWARE TEST          - REPBMP -
;+ *****
;+ - REPORT ANY BMP CODES IN THE QUEUE -
;+ THIS IS A PSEUDO-TEST USED TO REPORT ANY BMP CODES THAT WERE FOUND
;+ IN THE DUT'S FIFO DURING PREVIOUS TEST, AND LOGGED IN THE BMP CODE
;+ QUEUE.
;+ IT IS UNLIKELY THAT RUNNING THIS PSEUDO-TEST ALONE WILL PRODUCE ANY
;+ ERROR REPORTS.
;+ *****
;-- *****
      BGNTST
;+
;+ T26::
;+ TNUM == TNUM + 1      ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
;+ MOV  #TNUM,TSTNUM    ;SET UP THE TEST NUMBER. (93)
;+ MOV  #-1,CTRLCF     ;INDICATE THAT WE ARE IN A TEST.
;+ MOV  BMPCQP,R2      ;GET THE CONTENTS OF THE POINTER.
;+ MOV  #BMPCQB,R3     ;GET THE START ADDRESS OF THE QUEUE.
;+ CMP  R2,R3          ;SEE IF THE POINTER HAS MOVED FROM THE BASE.
;+ BEQ  60$            ;EXIT NO CODES IN THE QUEUE.
;+
;+ THERE IS AT LEAST ONE BMP CODE IN THE QUEUE. REPORT THE ERROR.
;+
;+ ;REPORT ERROR BMP CODE FOUND IN TEST NN, BMP CODE:NNNNNN"
;+ MOV  #EM9304,R1     ;PASS THE FIRST MESSAGE TO BE REORTED.
;+ ERRDF 9301,EM9301,ER9301 ; >>>> ERROR #9301 <<<<<.
;+
;+ TRAP  C$ERDF
;+ .WORD 9301
;+ .WORD EM9301
;+ .WORD ER9301
;+
;+ MOV  #BMPCQB,BMPCQP ;SET POINTER BACK TO THE BEGINING OF THE QUE.
;+
;+ 60$: CLR  CTRLCF    ;INDICATE THAT WE ARE NOT WITHIN A TEST.
;+      ENDTST
;+
;+ L10057:
;+ TRAP  C$ETST
```

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.SBTTL HARDWARE PARAMETER CODING SECTION

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7465 036676
036676 000022
036700

BGNHRD

.WORD L10060-L\$HARD/2
L\$HARD::

7466
7476
7477 036700
036700 000031
036702 036744
036704 160000
036706 177776

;DEVICE CSR ADDRESS QUESTION:
GPRMA HWPTQ1,0,0,160000,177776,YES

.WORD T\$CODE
.WORD HWPTQ1
.WORD T\$LOLIM
.WORD T\$HILIM

7478
7479 036710
036710 001031
036712 036762
036714 000040
036716 000776

;DEVICE INTERRUPT VECTOR QUESTION:
GPRMA HWPTQ2,2,0,40,776,YES

.WORD T\$CODE
.WORD HWPTQ2
.WORD T\$LOLIM
.WORD T\$HILIM

7480
7481 036720
036720 002032
036722 037015
036724 177777
036726 000000
036730 177777

;ACTIVE LINES BIT MAP QUESTION:
GPRMD HWPTQ3,4,0,MAPLNS,0,MAPLNS,YES

.WORD T\$CODE
.WORD HWPTQ3
.WORD MAPLNS
.WORD T\$LOLIM
.WORD T\$HILIM

7482
7483 036732
036732 003032
036734 037043
036736 000377
036740 000000
036742 000006

;INTERRUPT BR LEVEL QUESTION:
GPRMD HWPTQ4,6,0,377,0,6,YES

.WORD T\$CODE
.WORD HWPTQ4
.WORD 377
.WORD T\$LOLIM
.WORD T\$HILIM

7484
7485
7486 036744

ENDHRD

.EVEN
L10060:

036744

7487

7494

7495 036744 103 123 122
036747 040 101 104
036752 104 122 105
036755 123 123 072
036760 040 000

HWPTQ1: .ASCIZ /CSR ADDRESS: /

7496	036762	111	116	124
	036765	105	122	122
	036770	125	120	124
	036773	040	126	105
	036776	103	124	117
	037001	122	040	101
	037004	104	104	122
	037007	105	123	123
	037012	072	040	000
7497	037015	101	103	124
	037020	111	126	105
	037023	040	114	111
	037026	116	105	040
	037031	102	111	124
	037034	040	115	101
	037037	120	072	040
	037042	000		
7498	037043	111	116	124
	037046	105	122	122
	037051	125	120	124
	037054	040	102	122
	037057	040	114	105
	037062	126	105	114
	037065	072	040	000

HWPTQ2: .ASCIZ /INTERRUPT VECTOR ADDRESS: /

HWPTQ3: .ASCIZ /ACTIVE LINE BIT MAP: /

HWPTQ4: .ASCIZ /INTERRUPT BR LEVEL: /

7499
7500

.EVEN

7509
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7556

037070
037070 000017
037072
037072 000130
037074 037130
037076 000020
037100
037100 000130
037102 037204
037104 000001
037106
037106 000130
037110 037255
037112 000100
037114
037114 006044
037116
037116 001052
037120 037310
037122 177777
037124 000000
037126 177777
037130
037130
037133
037136
037141
037144

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;--

BGNSFT

.WORD L10061-L\$SOFT/2
L\$SOFT::

;UNIT NUMBER PRINTOUT QUESTION:
GPRML SWPTQ1,0,20,YES

.WORD T\$CODE
.WORD SWPTQ1
.WORD 20

;ROM VERSION NUMBER PRINTOUT ON FIRST PASS QUESTION:
GPRML SWPTQ2,0,1,YES

.WORD T\$CODE
.WORD SWPTQ2
.WORD 1

;EXTENDED ERROR REPORTING QUESTION:
GPRML SWPTQ3,0,100,YES

.WORD T\$CODE
.WORD SWPTQ3
.WORD 100

;; IF EXTENDED ERROR REPORTING IS NOT REQUIRED THEN SKIP THE NEXT QUESTION.

XFERF ENDD

.WORD T\$CODE

;NUMBER OF INDIVIDUAL DATA ERRORS TO REPORT ON A LINE QUESTION:
GPRMD SWPTQ4,2,D,177777,0,177777,YES

.WORD T\$CODE
.WORD SWPTQ4
.WORD 177777
.WORD T\$LOLIM
.WORD T\$HILIM

.EVEN

ENDD: ENDSFT

.EVEN
L10061:

SWPTQ1: .ASCIZ /REPORT UNIT NUMBER AS EACH UNIT IS TESTED: /

122 105 120
117 122 124
040 125 116
111 124 040
116 125 115

	037147	102	105	122	
	037152	040	101	123	
	037155	040	105	101	
	037160	103	110	040	
	037163	125	116	111	
	037166	124	040	111	
	037171	123	040	124	
	037174	105	123	124	
	037177	105	104	072	
	037202	040	000		
7557	037204	122	117	115	SWPTQ2: .ASCIZ /ROM VERSION PRINTOUT ON THE FIRST PASS: /
	037207	040	126	105	
	037212	122	123	111	
	037215	117	116	040	
	037220	120	122	111	
	037223	116	124	117	
	037226	125	124	040	
	037231	117	116	040	
	037234	124	110	105	
	037237	040	106	111	
	037242	122	123	124	
	037245	040	120	101	
	037250	123	123	072	
	037253	040	000		
7558	037255	105	130	124	SWPTQ3: .ASCIZ /EXTENDED ERROR REPORTING: /
	037260	105	116	104	
	037263	105	104	040	
	037266	105	122	122	
	037271	117	122	040	
	037274	122	105	120	
	037277	117	122	124	
	037302	111	116	107	
	037305	072	040	000	
7559	037310	116	125	115	SWPTQ4: .ASCIZ /NUMBER OF INDIVIDUAL DATA ERRORS TO REPORT ON A LINE: /
	037313	102	105	122	
	037316	040	117	106	
	037321	040	111	116	
	037324	104	111	126	
	037327	111	104	125	
	037332	101	114	040	
	037335	104	101	124	
	037340	101	040	105	
	037343	122	122	117	
	037346	122	123	040	
	037351	124	117	040	
	037354	122	105	120	
	037357	117	122	124	
	037362	040	117	116	
	037365	040	101	040	
	037370	114	111	116	
	037373	105	072	040	
7560	037376	000			.EVEN

7569
7570
7571 037400
7572 037400
7573
7580
7581
7582
7583
7584 037450

037450 000000
037452 000000
037454
7585 037454
7586
7587
7588
7589
7590
7591
7592
7593 000001

\$PATCH:: .BLKW 24

LASTAD

L\$LAST:: ENDMOD

.EVEN
.WORD 0
.WORD 0

.END

ACTLNS 002230 G
 ADR 000020 G
 ADRPTR 017764 G
 ALTFLD 017454 G
 ASSEMB 000010
 BCOUNT 002352 G
 BITTBL 002410 G
 BIT0 000001 G
 BIT00 000001 G
 BIT01 000002 G
 BIT02 000004 G
 BIT03 000010 G
 BIT04 000020 G
 BIT05 000040 G
 BIT06 000100 G
 BIT07 000200 G
 BIT08 000400 G
 BIT09 001000 G
 BIT1 000002 G
 BIT10 002000 G
 BIT11 004000 G
 BIT12 010000 G
 BIT13 020000 G
 BIT14 040000 G
 BIT15 100000 G
 BIT2 000004 G
 BIT3 000010 G
 BIT4 000020 G
 BIT5 000040 G
 BIT6 000100 G
 BIT7 000200 G
 BIT8 000400 G
 BIT9 001000 G
 BMPCQB 002526 G
 BMPCQE 002726 G
 BMPCQP 002524 G
 BOE 000400 G
 BRLEVL 002240 G
 BUFBAS 002726 G
 BUFEND 003726 G
 BUFMID 003326 G
 BUFPTR 002302 G
 BUF3QT 003526 G
 CACHRX 023632 G
 CACHTX 023660 G
 CALMSL 017526 G
 CKTRAP 017752 G
 CLKBRL 002336 G
 CLKCSR 002334 G
 CLKHRZ 002342 G
 CLKINT 023706 G
 CLKVEC 002340 G
 CLNRST 020002 G
 CLR16W 020024 G
 CNTERR 020046 G
 CSRA 002242 G
 CTRLCF 002306 G

C#AU 000052
 C#AUTO 000061
 C#BRK 000022
 C#BSEG 000004
 C#BSUB 000002
 C#CEFG 000045
 C#CLCK 000062
 C#CLEA 000012
 C#CLOS 000035
 C#CLP1 000006
 C#CVEC 000036
 C#DCLN 000044
 C#DODU 000051
 C#DRPT 000024
 C#DU 000053
 C#EDIT 000003
 C#ERDF 000055
 C#ERMR 000056
 C#ERRO 000060
 C#ERSF 000054
 C#ERSO 000057
 C#ESCA 000010
 C#ESEG 000005
 C#ESUB 000003
 C#ETST 000001
 C#EXIT 000032
 C#GETB 000026
 C#GETW 000027
 C#GMAN 000043
 C#GPHR 000042
 C#GPLO 000030
 C#GPRI 000040
 C#INIT 000011
 C#INLP 000020
 C#MANI 000050
 C#MEM 000031
 C#MSG 000023
 C#OPEN 000034
 C#PNTB 000014
 C#PNTF 000017
 C#PNTS 000016
 C#PNTX 000015
 C#QIO 000377
 C#RDBU 000007
 C#REFG 000047
 C#RESE 000033
 C#REVI 000003
 C#RFLA 000021
 C#RPT 000025
 C#SEFG 000046
 C#SPRI 000041
 C#SVEC 000037
 C#TPRI 000013
 DELAY 020124 G
 DFPTBL 002212 G
 DIAGMC 000000
 DRADRT 002242 G

DROP 025122
 DR00MG 005464 G
 DR02MG 005470 G
 DR04MG 005475 G
 DR06MG 005501 G
 DR10MG 005517 G
 DR12MG 005526 G
 DR14MG 005537 G
 DR16MG 005550 G
 EDROP 025200
 EF.CON 000036 G
 EF.NEW 000035 G
 EF.PWR 000034 G
 EF.RES 000037 G
 EF.STA 000040 G
 EF0503 004074 G
 EF0505 004101 G
 EF1401 004154 G
 EF1402 004256 G
 EF1601 004313 G
 EF1602 004345 G
 EF1603 004407 G
 EF1604 004451 G
 EF3001 004546 G
 EF3002 004615 G
 EF9006 004664 G
 EF9010 004710 G
 EF9016 004777 G
 EF9017 005074 G
 EF9018 005150 G
 EF9301 005255 G
 EF9302 005333 G
 EM0101 020360 G
 EM0102 020444 G
 EM0103 005560 G
 EM0201 005616 G
 EM0202 005651 G
 EM0203 006024 G
 EM0204 006167 G
 EM0301 006346 G
 EM0302 006420 G
 EM0303 006560 G
 EM0401 006737 G
 EM0402 007015 G
 EM0501 007165 G
 EM0502 007241 G
 EM0525 007415 G
 EM0526 007505 G
 EM0601 007575 G
 EM0602 007637 G
 EM0603 010017 G
 EM0701 010202 G
 EM0702 010245 G
 EM0703 010425 G
 EM0801 010610 G
 EM0802 010656 G
 EM0901 010726 G

EM0902 010766 G
 EM1001 011022 G
 EM1002 011055 G
 EM1003 011142 G
 EM1101 011220 G
 EM1201 011273 G
 EM1202 011321 G
 EM1203 011405 G
 EM1204 011463 G
 EM1205 011517 G
 EM1301 011543 G
 EM1302 011576 G
 EM1401 011635 G
 EM1402 011674 G
 EM1403 011762 G
 EM1404 012035 G
 EM1405 012107 G
 EM1406 012122 G
 EM1407 012135 G
 EM1408 012147 G
 EM1501 012155 G
 EM1502 012203 G
 EM1601 012221 G
 EM1604 012304 G
 EM1701 012360 G
 EM1801 012443 G
 EM1901 012517 G
 EM2001 012602 G
 EM2002 012652 G
 EM2301 012725 G
 EM2302 012772 G
 EM2401 013030 G
 EM2601 013073 G
 EM2602 013131 G
 EM2603 013222 G
 EM2604 013310 G
 EM2605 013404 G
 EM2606 013467 G
 EM2607 013526 G
 EM2608 013622 G
 EM2609 013673 G
 EM2610 013752 G
 EM2611 014044 G
 EM2612 014127 G
 EM3001 014223 G
 EM3002 014262 G
 EM3003 014356 G
 EM3004 014432 G
 EM3005 014506 G
 EM3101 014600 G
 EM3102 014643 G
 EM9014 014722 G
 EM9017 015016 G
 EM9018 015127 G
 EM9019 015137 G
 EM9020 015154 G
 EM9022 015200 G

EM9023 015217 G
 EM9024 015241 G
 EM9301 015257 G
 EM9302 015336 G
 EM9303 015366 G
 EM9304 015433 G
 ENDD 037130
 ENDETB 003726 G
 ENDIT 025042
 ERCNTB 002464 G
 ERLTBL 002726 G
 ERRBLK 003774 G
 ERRMSG 003772 G
 ERRNBR 003770 G
 ERRTP 003766 G
 ERSMRF 002462 G
 ER0101 015510 G
 ER0201 016042 G
 ER0503 016136 G
 ER0504 016174 G
 ER1401 016254 G
 ER1601 016416 G
 ER1603 016526 G
 ER3001 016620 G
 ER9004 016722 G
 ER9007 017036 G
 ER9008 017126 G
 ER9101 017222 G
 ER9301 017262 G
 EVL 000004 G
 EXOERR 002304 G
 E#END 002100
 E#LOAD 000035
 FDATA 002250 G
 FLSA 002250 G
 FLSO 000006 G
 F#AU 000015
 F#AUTO 000020
 F#BGN 000040
 F#CLEA 000007
 F#DU 000016
 F#END 000041
 F#HARD 000004
 F#HW 000013
 F#INIT 000006
 F#JMP 000050
 F#MOD 000000
 F#MSG 000011
 F#PROT 000021
 F#PWR 000017
 F#RPT 000012
 F#SEG 000003
 F#SOFT 000005
 F#SRV 000010
 F#SUB 000002
 F#SW 000014
 F#TEST 000001

GETPRM	024640	LPRO	= 000004 G	L10002	015624	MSLOOP	020300 G	SFPTBL	002224 G
GPRSOB	002450 G	L\$ACP	002110 G	L10003	016134	MSTICK	002354 G	SKPSTS	022344 G
G\$CNTD	000200	L\$APT	002036 G	L10004	016172	NDERPT	002226 G	SVCGBL	= 000000
G\$DELM	= 000372	L\$AU	025206 G	L10005	016252	NEWPAS	024620	SVCINS	= 000001
G\$DISP	= 000003	L\$AUT	002070 G	L10006	016414	NEWRES	024612	SVCSUB	= 000001
G\$EXCP	= 000400	L\$AUTO	025056 G	L10007	016524	NEWSTA	024302	SVCTAG	= 000001
G\$HILI	= 000002	L\$CCP	002106 G	L10010	016616	NUMLNS	= 000020 G	SVCTST	= 000001
G\$LOLI	= 000001	L\$CLEA	025060 G	L10011	016720	OOPS	020314 G	SWAPO	022422 G
G\$NO	= 000000	L\$CO	002032 G	L10012	017034	OPTION	002224 G	SWPTQ1	037130
G\$OFFS	= 000400	L\$DEPO	002011 G	L10013	017124	O\$APTS	= 000000	SWPTQ2	037204
G\$OFFSI	= 000376	L\$DESC	004046 G	L10014	017220	O\$AU	= 000000	SWPTQ3	037255
G\$PRMA	= 000001	L\$DESP	002076 G	L10015	017260	O\$BGNR	= 000001	SWPTQ4	037310
G\$PRMD	= 000002	L\$DEVP	002060 G	L10016	017452	O\$BGNS	= 000001	S\$LSYM	= 010000
G\$PRML	= 000000	L\$DISP	002124 G	L10017	024226	O\$DU	= 000001	TIMER1	002344 G
G\$RADA	= 000140	L\$DLY	002116 G	L10021	025054	O\$ERRT	= 000001	TIMER2	002346 G
G\$RADB	= 000000	L\$DTP	002040 G	L10022	025056	O\$GNSW	= 000001	TIMER3	002350 G
G\$RADD	= 000040	L\$DTYP	002034 G	L10023	025074	O\$POIN	= 000001	TNUM	= 000032 G
G\$RADL	= 000120	L\$DU	025076 G	L10024	025204	O\$SETU	= 000000	TP4FLG	002320 G
G\$RADO	= 000020	L\$DUT	002072 G	L10025	025212	PAROA	002366 G	TP4RTN	024124 G
G\$XFER	= 000004	L\$DVTY	004036 G	L10026	025474	PASCNT	002312 G	TP4VEC	002322 G
G\$YES	= 000010	L\$EF	002052 G	L10027	025724	PCSLOT	= 000016 G	TSABRT	022472 G
HELP	= 000000	L\$ENVI	002044 G	L10030	026170	PNT	= 001000 G	TSTNUM	002324 G
HOE	= 100000 G	L\$ERRT	003766 G	L10031	026366	PREGRT	004020 G	TXAD1A	002254 G
HWPTQ1	036744	L\$ETP	002102 G	L10032	026560	PREG05	003776	TXAD2A	002256 G
HWPTQ2	036762	L\$EXP1	002046 G	L10033	026776	PRI	= 002000 G	TXBFCA	002260 G
HWPTQ3	037015	L\$EXP4	002064 G	L10034	027204	PRI00	= 000000 G	TXBFCO	= 000016 G
HWPTQ4	037043	L\$EXP5	002066 G	L10035	027412	PRI01	= 000040 G	TXDSBL	022604 G
IBE	= 010000 G	L\$HARD	036700 G	L10036	027606	PRI02	= 000100 G	TXENBL	022700 G
IDU	= 000040 G	L\$HIME	002120 G	L10037	030022	PRI03	= 000140 G	TXIEO	022774 G
IER	= 020000 G	L\$HPCP	002016 G	L10040	030250	PRI04	= 000200 G	TXIE1	023034 G
IESTAT	002310 G	L\$HPTP	002022 G	L10041	030544	PRI05	= 000240 G	TXINTC	002326 G
ISR	= 000100 G	L\$HW	002212 G	L10042	031032	PRI06	= 000300 G	TXINTF	002330 G
IXE	= 004000 G	L\$ICP	002104 G	L10043	031432	PRI07	= 000340 G	TXINTR	024146 G
I\$AU	= 000041	L\$INIT	024236 G	L10044	031774	PUFIFO	020542 G	TXVECA	002234 G
I\$AUTO	= 000041	L\$LADP	002026 G	L10045	032242	RBUFA	002244 G	T\$ARGC	= 000003
I\$CLN	= 000041	L\$LAST	037454 G	L10046	032432	RDPDR	020624 G	T\$CODE	= 001052
I\$DU	= 000041	L\$LOAD	002100 G	L10047	032750	REGTST	021076 G	T\$ERRN	= 022125
I\$HRD	= 000041	L\$LUN	002074 G	L10050	033212	REPSMR	021330 G	T\$EXCP	= 000000
I\$INIT	= 000041	L\$MREV	002050 G	L10051	033326	RESETT	021356 G	T\$FLAG	= 000050
I\$MOD	= 000041	L\$NAME	002000 G	L10052	033644	RMATBB	002262 G	T\$GMAN	= 000000
I\$MSG	= 000041	L\$PRIO	002042 G	L10053	034220	ROLDAP	021470 G	T\$HILI	= 177777
I\$PROT	= 000040	L\$PROT	024230 G	L10054	035356	RSTRPT	021514 G	T\$LAST	= 000001
I\$PTAB	= 000041	L\$PRT	002112 G	L10055	036320	RXBRRT	023756 G	T\$LOLI	= 000000
I\$PWR	= 000041	L\$REPP	002062 G	L10056	036614	RXIEO	022212 G	T\$LSYM	= 010000
I\$RPT	= 000041	L\$REV	002010 G	L10057	036674	RXIE1	022252 G	T\$LTNO	= 000032
I\$SEG	= 000041	L\$RPT	024222 G	L10060	036744	RXINPT	024036 G	T\$NEST	= 177777
I\$SETU	= 000041	L\$SOFT	037072 G	L10061	037130	RXINTC	002314 G	T\$NSO	= 000000
I\$SFT	= 000041	L\$SPC	002056 G	MAPLNS	= 177777 G	RXINTF	002316 G	T\$NS1	= 000005
I\$SRV	= 000041	L\$SPCP	002020 G	MFUNIT	005433 G	RXTMA	002244 G	T\$PTNU	= 000000
I\$SUB	= 000041	L\$SPTP	002024 G	MMENAB	002364 G	RXVECA	002232 G	T\$SAVL	= 177777
I\$TST	= 000041	L\$STA	002030 G	MMPRES	002362 G	ROSLOT	= 000002 G	T\$SEGL	= 177777
J\$JMP	= 000167	L\$SW	002224 G	MMSRO	002360 G	R1SLOT	= 000004 G	T\$SUBN	= 000000
LNCTRA	002252 G	L\$TEST	002114 G	MSG1	015626 G	R2SLOT	= 000006 G	T\$TAGL	= 177777
LOE	= 040000 G	L\$TIML	002014 G	MSG2	015704 G	R3SLOT	= 000010 G	T\$TAGN	= 010062
LOT	= 000010 G	L\$UNIT	002012 G	MSG3	015763 G	R4SLOT	= 000012 G	T\$TEMP	= 000000
LPCSLT	= 000036 G	L10000	002222	MSLCNT	002356 G	R5SLOT	= 000014 G	T\$TEST	= 000032
LPRA	002246 G	L10001	002230	MSLGET	020164 G	SAVBMP	022276 G	T\$TSTM	= 177777

T\$TSTS= 000001	T\$\$SOF= 010061	T17	032244 G	T3	025726 G	WAIBIC	023214 G
T\$\$AU = 010025	T\$\$SW = 010001	T18	032434 G	T4	026172 G	WAIBIS	023270 G
T\$\$AUT= 010022	T\$\$TES= 010057	T19	032752 G	T5	026370 G	WDPDR	023344 G
T\$\$CLE= 010023	T1	T2	025476 G	T6	026562 G	WORD1	002332 G
T\$\$DU = 010024	T10	T20	033214 G	T7	027000 G	WTWLNC	023552 G
T\$\$HAR= 010060	T11	T21	033330 G	T8	027206 G	WTWLPR	023602 G
T\$\$HW = 010000	T12	T22	033646 G	T9	027414 G	X\$ALWA=	000000
T\$\$INI= 010021	T13	T23	034222 G	UAM	= 000200 G	X\$FALS=	000040
T\$\$MSG= 010016	T14	T24	035360 G	UNBTTB	002370 G	X\$OFFS=	000400
T\$\$PRO= 010020	T15	T25	036322 G	UNITN	002236 G	X\$TRUE=	000020
T\$\$RPT= 010017	T16	T26	036616 G	UNSDIV	023060 G	\$PATCH	037400 G

. ABS. 037454 000
000000 001

ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 28661 WORDS (112 PAGES)
DYNAMIC MEMORY: 20060 WORDS (77 PAGES)
ELAPSED TIME: 00:05:14
CZDHUBO.BIN,CZDHUBO.LST/-SP=SVC34R/ML,CZDHUBO.P11